

Using Frequency-domain (Bode Plot) Design for the Double Integrator

Double Integrator

A very common system and a difficult design problem.

It's Newton's fault: $F = m \cdot a = m \cdot \frac{d^2}{dt^2} x$

$$x = \frac{1}{m} \cdot \left(\int \int F dt dt \right)$$

$$\mathbf{X}(s) = \mathbf{F}(s) \cdot \frac{1}{m \cdot s^2}$$

Same for angular motion: $T = J \cdot \alpha = J \cdot \frac{d^2}{dt^2} \theta$

$$\& \mathbf{P}(s) = \frac{1}{m \cdot s^2}$$

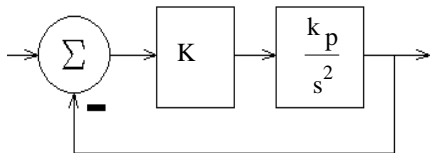
This problem arises anytime force is the input and position is the output.

Force is the ONLY way to get the motion of any object to change, so yes, this is a common problem.

In the Inverted Pendulum lab, the movement of the base is simplified to a first-order system to avoid the difficulties that come from this very issue.

The example used in section 5.3.9 is a VERY REAL example.

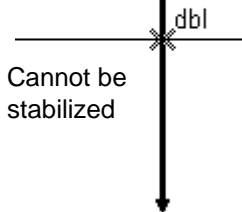
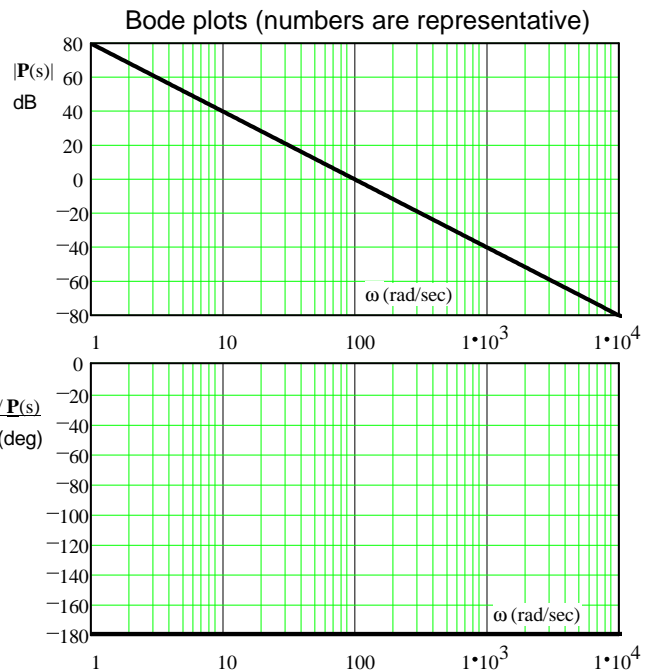
in general: $\mathbf{P}(s) = \frac{k_p}{s^2}$



Root-locus for the double-integrator in a closed-loop (CL) feedback system.

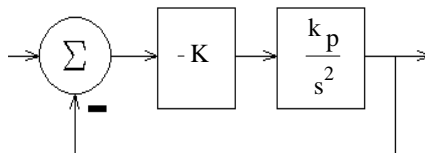
$$\mathbf{H}(s) = \frac{K \cdot \frac{k_p}{s^2}}{1 + K \cdot \frac{k_p}{s^2}} = \frac{K \cdot k_p}{s^2 + K \cdot k_p}$$

MUST use a compensator.



Cannot be stabilized

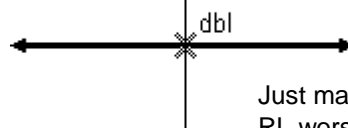
If the angle is always 180, then wouldn't positive feedback work? (make the gain negative)



$$\mathbf{H}(s) = \frac{K \cdot \frac{k_p}{s^2}}{1 - K \cdot \frac{k_p}{s^2}} = \frac{K \cdot k_p}{s^2 - K \cdot k_p}$$

CL poles $\pm \sqrt{K \cdot k_p}$

Negative gain makes root-locus rules work backwards, here the real-axis rule:



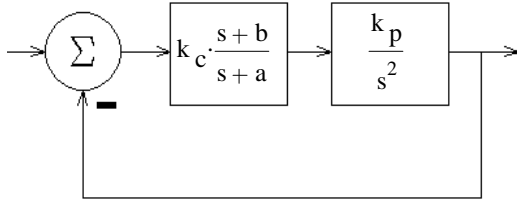
Given the issues with a PD (the differentiator). lets use a Lead controller.

Just makes the RL worse.

Lead controller

See section 5.3.9

$$C(s) = k_c \cdot \frac{s+b}{s+a}$$



Put the two together,

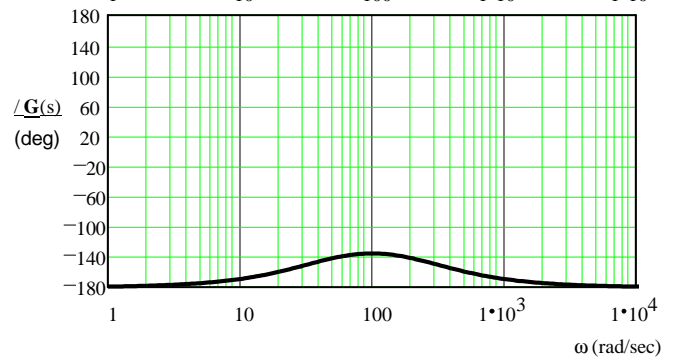
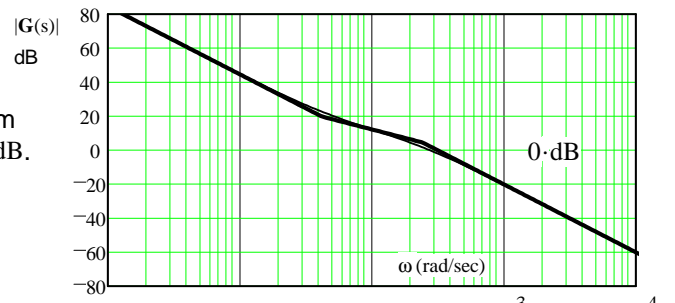
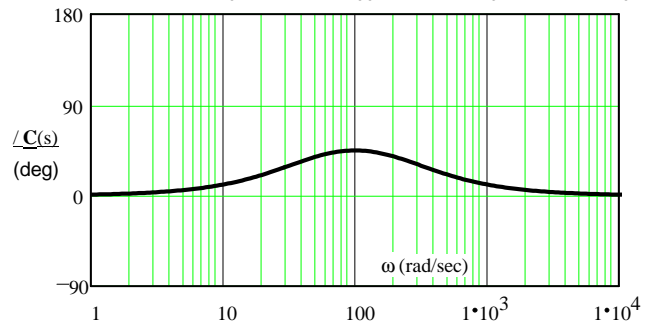
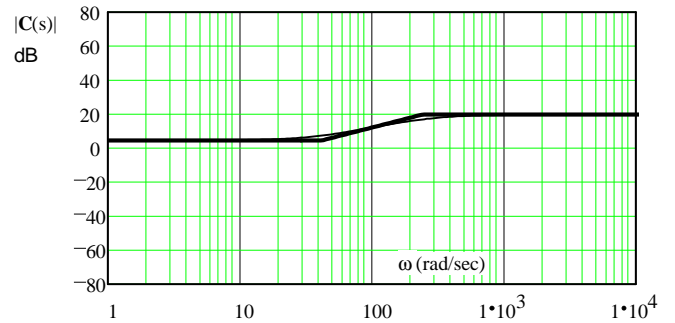
$$G(s) = k_c \cdot \frac{s+b}{s+a} \cdot \frac{k_p}{s^2} = k_p \cdot k_c \cdot \frac{s+b}{s^2 \cdot (s+a)}$$

But now the maximum phase angle difference from 180 doesn't occur where the magnitude crosses 0dB.

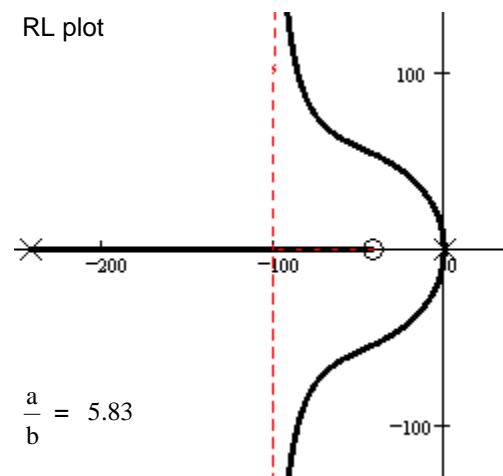
This problem is resolved in the math shown in the book, which makes:

$$\begin{aligned} \omega_c &= \omega_p \\ \text{freq. of maximum} &= \text{freq. where } G(s) \\ \text{phase difference} & \text{ crosses } 0\text{dB.} \end{aligned}$$

Bode plots (numbers are representative)



RL plot



$$\frac{a}{b} = 5.83$$

The Bottom Line

I've combined information from the table in section 5.3.7 with the table in section 5.3.9.

	For double integrator problem				approximation from simpler system of section 5.3.7
	$\left(\frac{a}{b}\right)$	$\phi_p = \text{PM}$	ζ	%OS = PO = percent overshoot based on ζ approx.	
1. Select your a/b ratio, use this ratio as a single number in following equations.	5.83	45°	0.44	20.5%	PM, ζ relationship is also shown in section 5.3.7, 2nd eq. (5.63)
	9	53.1°	0.55	14%	
	13.9	60°	0.6	9.5%	
use $\left(\frac{a}{b}\right)$ as a single number	Or use eq. 5.67 (now 5.73)		Extension of table using approximate relationship between PM and overshoot developed in section 5.3.7		

2. Use eq. 5.69 (now 5.75) to relate ω_c to k_p and k_c . $\frac{k_p \cdot k_c}{\omega_c^2} \cdot \sqrt{\frac{b}{a}} = 1$ OR, rearranged: $\omega_p = \omega_c = \sqrt{k_p \cdot k_c} \cdot \sqrt{\frac{b}{a}}$

Note: $\frac{b}{a} = \frac{1}{\left(\frac{a}{b}\right)}$

Depending on your knowns and unknowns, other rearrangements may be useful:

$$k_p \cdot k_c = \omega_c^2 \cdot \sqrt{\frac{a}{b}} \qquad k_p = \frac{\omega_c^2}{k_c} \cdot \sqrt{\frac{a}{b}} \qquad k_c = \frac{\omega_c^2}{k_p} \cdot \sqrt{\frac{a}{b}}$$

To get answers and plots for BP3, prob.7, I arbitrarily used:

$$\omega_c := 10 \qquad k_p := 1 \qquad \text{and found } k_c \text{ from the eq.}$$

3. Find: $a = \omega_c \cdot \sqrt{\frac{a}{b}} = \omega_p \cdot \sqrt{\frac{a}{b}}$ the pole location of $C(s)$

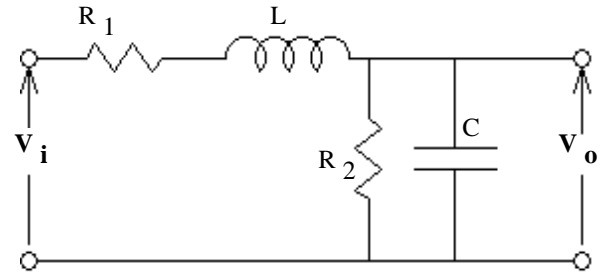
$b = \omega_c \cdot \sqrt{\frac{b}{a}} = \omega_p \cdot \sqrt{\frac{b}{a}}$ the zero location of $C(s)$

Why Bode Plots?

1. Provides a method to find the approximate transfer function as used in the Flexible Beam lab.
2. Terms GM and PM are in wide use and you need to know what they mean.
3. Sometimes used for design method as in the Flexible Beam lab.

You will find from Bp3, prob.7, that the approximations of overshoot given in the table above are not very good (off by about a factor of 2), but, those predicted by the second-order approximation are even worse (b/c of zero close to origin).

Let's find the transfer function of this circuit.



$$\begin{aligned}
 H(s) &= ? \\
 H(s) &= \frac{V_o(s)}{V_i(s)} = \frac{\frac{1}{R_2 + C \cdot s}}{R_1 + L \cdot s + \frac{1}{R_2 + C \cdot s}} \\
 &= \frac{1}{R_1 \cdot \left(\frac{1}{R_2} + C \cdot s\right) + L \cdot s \cdot \left(\frac{1}{R_2} + C \cdot s\right) + 1} \\
 &= \frac{1}{\frac{R_1}{R_2} + R_1 \cdot C \cdot s + \frac{L \cdot s}{R_2} + L \cdot s \cdot C \cdot s + 1} \\
 &= \frac{\frac{1}{L \cdot C}}{s^2 + \left(\frac{R_1}{L} + \frac{1}{R_2 \cdot C}\right) \cdot s + \left(1 + \frac{R_1}{R_2}\right) \cdot \frac{1}{L \cdot C}}
 \end{aligned}$$

$$\begin{aligned}
 &= \frac{\frac{1}{L \cdot C}}{\frac{R_1}{R_2} \cdot \frac{1}{L \cdot C} + \frac{R_1 \cdot C}{L \cdot C} \cdot s + \frac{L \cdot s}{R_2} \cdot \frac{1}{L \cdot C} + s^2 + \frac{1}{L \cdot C}}
 \end{aligned}$$

Input Impedance

What load does this circuit place on the source of V_i ?

$$Z_{in}(s) = R_1 + L \cdot s + \frac{1}{\frac{1}{R_2} + C \cdot s} \quad \text{OR, if this circuit is followed by another circuit with } Z_{in2}(s), \text{ then,}$$

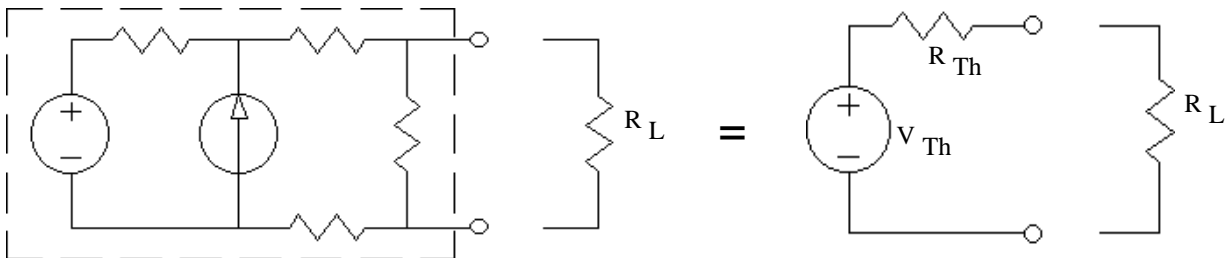
$$Z_{in}(s) = R_1 + L \cdot s + \frac{1}{\frac{1}{R_2} + C \cdot s + \frac{1}{Z_{in2}(s)}}$$

Usually, the higher the input impedance, the better.

Output Impedance

Output Impedance is just like the Thévenin Resistance

Thévenin Equivalent Circuit



Thévenin equivalent

To calculate a circuit's Thévenin equivalent:

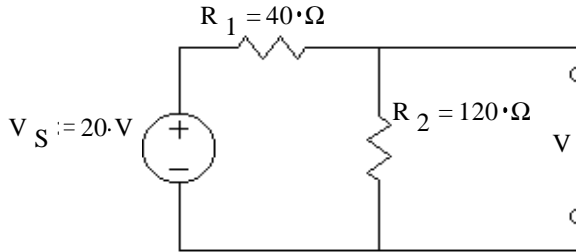
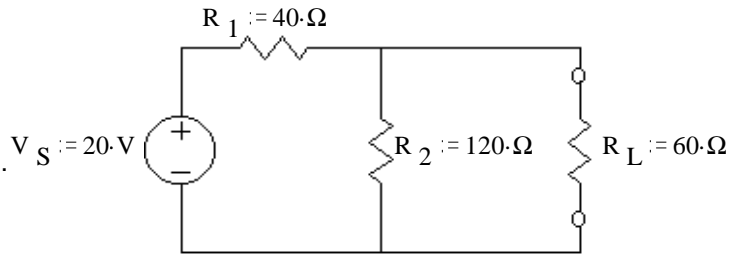
- 1) Remove the load and calculate the open-circuit voltage where the load used to be. This is the Thévenin voltage (V_{Th}).
- 2) Zero all the sources. (To zero a voltage source, replace it with a short. To zero a current source, replace it with an open.)
- 3) Compute the total resistance between the load terminals. (DO NOT include the load in this resistance.) This is the Thévenin source resistance (R_{Th}).
- 4) Draw the Thévenin equivalent circuit and add your values.

Ex 1 Find the Thévenin equivalent:

To find a circuit's Thévenin equivalent:

1) Remove the load and calculate the open-circuit voltage where the load used to be.

This is the Thévenin voltage (V_{Th}).

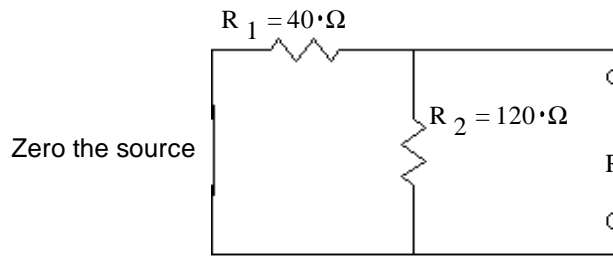


Find the open circuit voltage:

$$V_{oc} = V_{Th} := V_S \cdot \frac{R_2}{R_1 + R_2} \quad V_{Th} = 15 \cdot V$$

2) Zero all the sources.

(To zero a voltage source, replace it with a short. To zero a current source, replace it with an open.)



3) Compute the total resistance between the load terminals.

(DO NOT include the load in this resistance.)

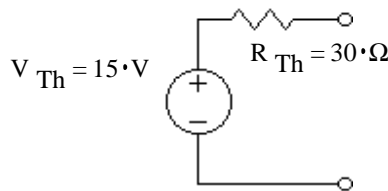
This is the Thévenin source resistance (R_{Th}).

Find the Thevenin resistance:

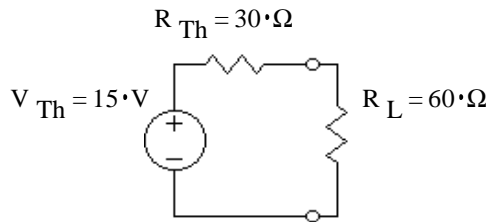
$$R_{Th} := \frac{1}{\frac{1}{R_1} + \frac{1}{R_2}} \quad R_{Th} = 30 \cdot \Omega$$

4) Draw the Thévenin equivalent circuit and add your values.

Thevenin equivalent circuit:



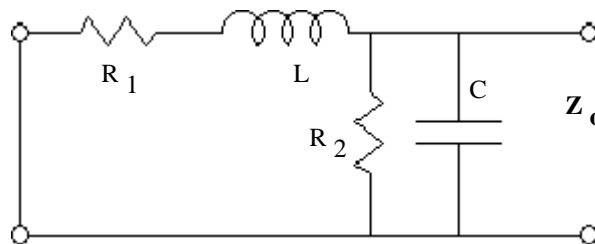
If the load were reconnected:



$$V_L = V_{Th} \cdot \frac{R_L}{R_{Th} + R_L} = 10 \cdot V$$

$$I_L = \frac{V_{Th}}{R_{Th} + R_L} = 166.7 \cdot mA$$

Output Impedance



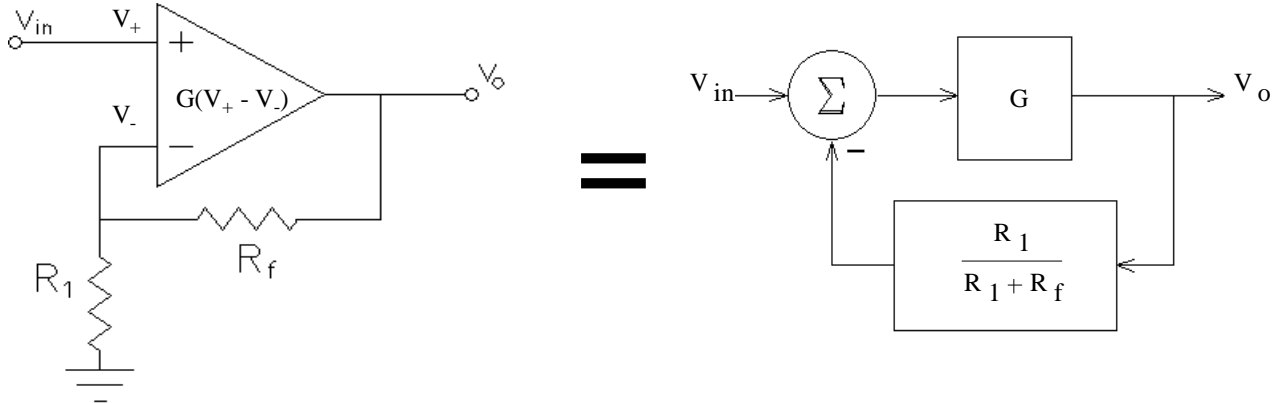
$$Z_{out}(s) = \frac{R_1 + L \cdot s}{1 + \frac{R_1}{R_2} + \frac{L}{R_2} \cdot s + C \cdot s \cdot R_1 + L \cdot C \cdot s^2} \cdot \left(\frac{1}{L \cdot C} \right) = \frac{\frac{1}{C} \cdot s + \frac{R_1}{L \cdot C}}{s^2 + \left(\frac{1}{C \cdot R_2} + \frac{R_1}{L} \right) \cdot s + \frac{1}{L \cdot C} \cdot \left(1 + \frac{R_1}{R_2} \right)}$$

OR, if this circuit is preceded by another circuit with $Z_{out0}(s)$, then,
$$Z_{out}(s) = \frac{1}{\frac{1}{Z_{out0}} + \frac{1}{R_1 + L \cdot s} + \frac{1}{R_2} + C \cdot s}$$

Usually, the lower the output impedance, the better

Amplifier Compensation & Bandwidth Extension (Especially, Op Amps)

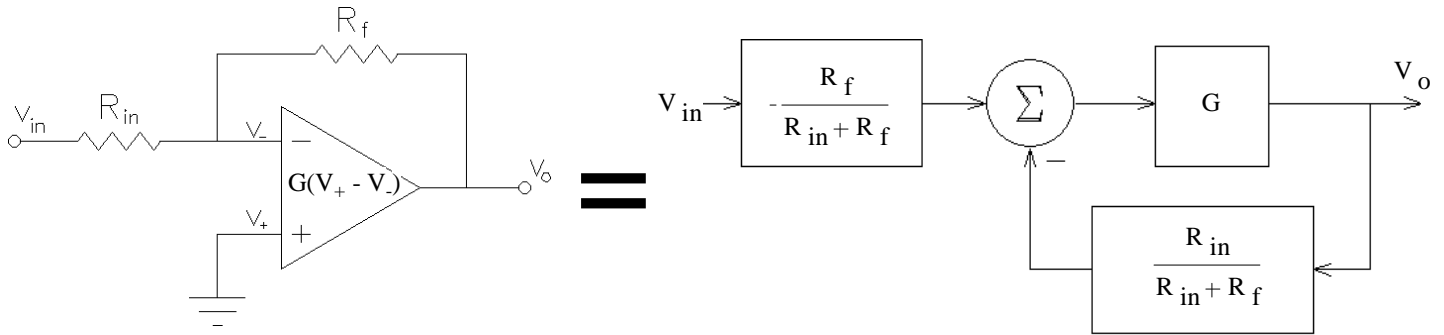
Recall from previous classes that op-amps are used with lots of negative feedback. In homework 4, you showed that common op-amp circuits could be looked at as feedback loops.



$$\frac{V_o}{V_{in}} = \frac{A}{1 + A \cdot B} = \frac{G}{1 + \frac{R_1}{R_1 + R_f} \cdot G} = \frac{G \cdot (R_1 + R_f)}{R_1 + R_f + R_1 \cdot G} = \frac{R_1 + R_f}{\frac{R_1 + R_f}{G} + R_1} \approx \frac{R_1 + R_f}{0 + R_1} = 1 + \frac{R_f}{R_1}$$

If G is very large.

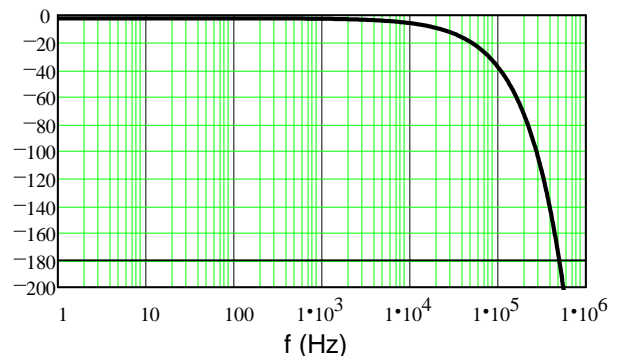
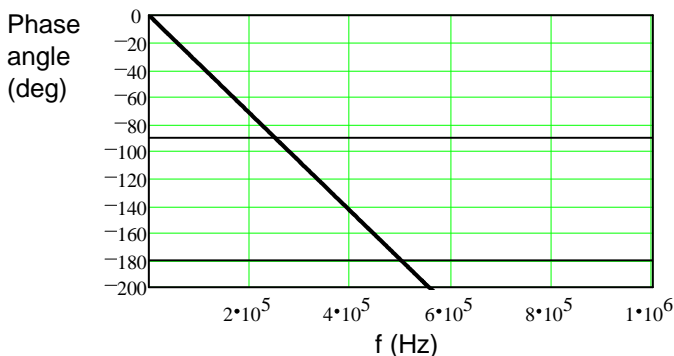
AND



$$\frac{V_o}{V_{in}} = \frac{R_f}{R_{in} + R_f} \cdot \frac{A}{1 + A \cdot B} = \frac{R_f}{R_{in} + R_f} \cdot \frac{G}{1 + \frac{R_{in}}{R_{in} + R_f} \cdot G} = \frac{R_f \cdot G}{R_{in} + R_f + R_{in} \cdot G} = \frac{R_f}{\frac{R_{in} + R_f}{G} + R_{in}} \approx \frac{R_f}{0 + R_{in}} = \frac{R_f}{R_{in}}$$

If G is very large.

But, op-amps are made of real circuitry and therefore have a little delay. Just a delay of: $D := 1 \cdot \mu s$
Would result phase angle plots like those below. Serious problems above 500kHz.



To make sure that op-amp circuits remain stable with feedback, they are compensated with a low-frequency pole so that the gain is less than 0dB where the phase angle falls off to -180° .

ECE 3510 Amplifier Feedback p2

Typical op-amp compensation would look like this for an op-amp with a gain of 100k and $D = 1\mu s$.

$$G := 100000$$

Adequate compensation could be a pole at 2Hz (4π rad/sec).

Op-amp transfer function: $\frac{G \cdot 4 \cdot \pi}{s + 4 \cdot \pi}$

$G \cdot 2 \cdot \text{Hz}$ is the gain-bandwidth product (GBW).
(in Hz)

For the noninverting amp:

$$\frac{V_o}{V_{in}} = \frac{\frac{G \cdot 4 \cdot \pi}{s + 2}}{1 + \frac{R_1}{R_1 + R_f} \cdot \frac{G \cdot 4 \cdot \pi}{s + 4 \cdot \pi}}$$

$$= \frac{G \cdot 4 \cdot \pi}{s + 4 \cdot \pi + \frac{R_1}{R_1 + R_f} \cdot G \cdot 4 \cdot \pi}$$

new pole is at: $4 \cdot \pi + \frac{R_1}{R_1 + R_f} \cdot G \cdot 4 \cdot \pi \approx \frac{R_1}{R_1 + R_f} \cdot G \cdot 4 \cdot \pi$ rad/sec

For the inverting amp:

$$\frac{V_o}{V_{in}} = -\frac{R_f}{R_{in} + R_f} \cdot \frac{\frac{G \cdot 4 \cdot \pi}{s + 4 \cdot \pi}}{1 + \frac{R_{in}}{R_{in} + R_f} \cdot \frac{G \cdot 4 \cdot \pi}{s + 4 \cdot \pi}} = -\frac{R_f}{R_{in} + R_f} \cdot \frac{G \cdot 4 \cdot \pi}{s + 4 \cdot \pi + \frac{R_{in}}{R_{in} + R_f} \cdot G \cdot 4 \cdot \pi}$$

new pole is at: $4 \cdot \pi + \frac{R_{in}}{R_{in} + R_f} \cdot G \cdot (4 \cdot \pi) \approx \frac{R_{in}}{R_{in} + R_f} \cdot G \cdot (4 \cdot \pi)$ rad/sec = $\frac{\text{GBW}}{\text{Closed-loop gain}}$ (in Hz)

The pole is also the 3dB roll-off point of the amplifier, as long as it's just a single pole.

And, The upshot is:

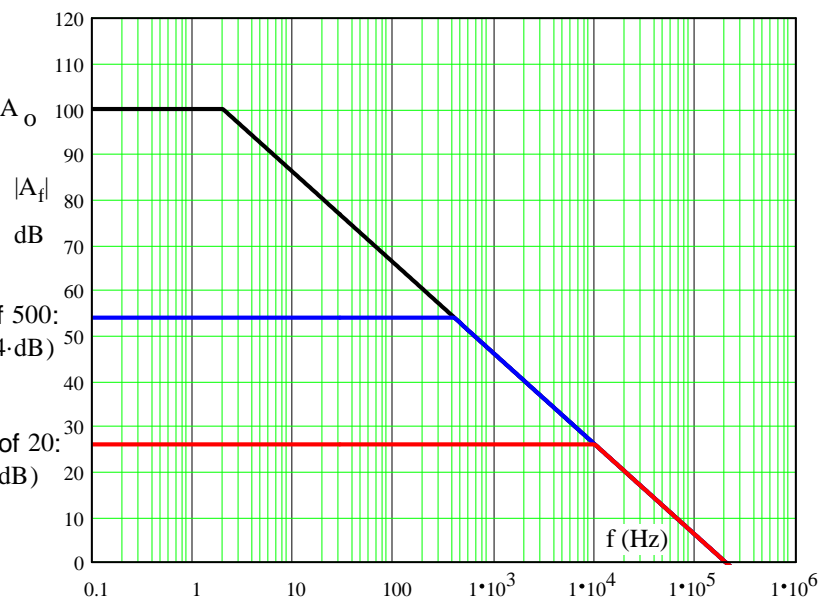
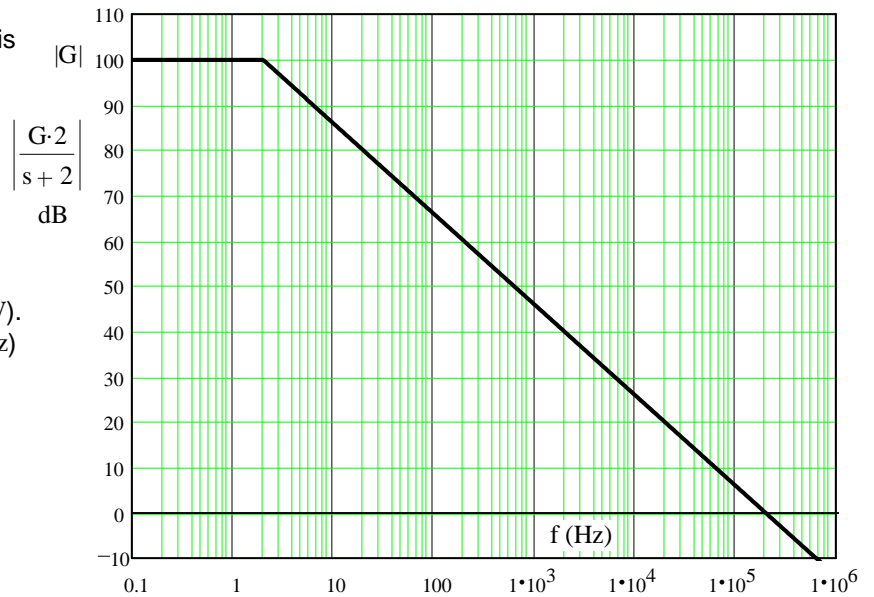
If your op-amp circuit has a closed-loop gain of "g", then the bandwidth should be about:

Closed-loop bandwidth: $\frac{\text{GBW}}{g}$ (in Hz)

Example closed-loop frequency responses:

for a closed-loop gain of 500:
(54-dB)

for a closed-loop gain of 20:
(26-dB)



If you want a higher frequency response, either buy a better op-amp, with a higher gain-bandwidth product, or use more stages. However, if you use two stages with the same the 3dB roll-off point, be aware that the overall roll-off will be 6dB at that frequency and that the 3dB roll-off point will be lower by about 36%.

And about 50% lower for three stages with same 3dB roll-off point.

Effects of Negative Feedback on Linear Amplifiers

In general, you trade gain for other improvements, like bandwidth.

Gain

Closed-loop gain (Gain with feedback) = $A_f = \frac{A_o}{1 + A_o \cdot B} = \frac{\text{Open-loop gain}}{\text{Amount of feedback}}$

If $A_o \cdot B$ is significantly > 1 , then: $A_f \approx \frac{1}{B}$ B = the feedback factor, generally less than 1, often a lot less.
 This is a big difference between the control systems we have been studying and feedback used with amplifiers. Control systems generally use feedback closer to 1.

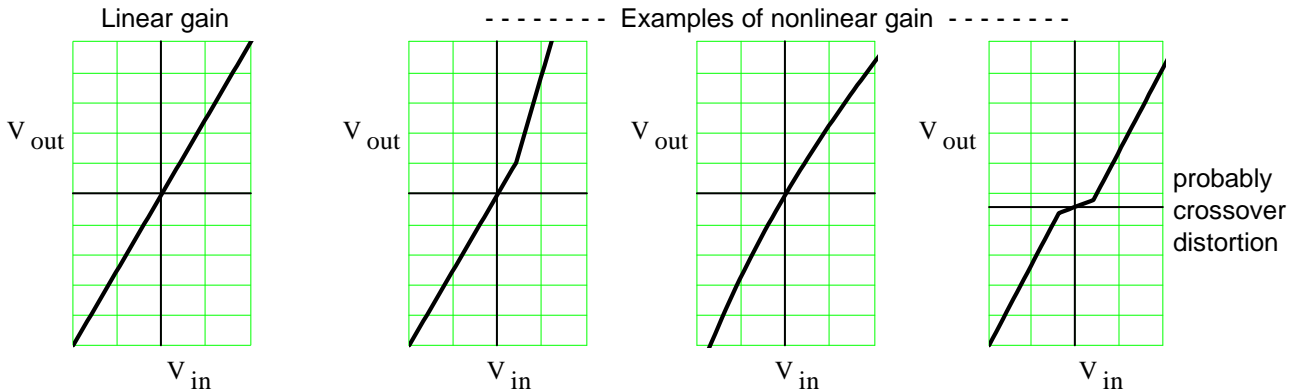
Gain is stabilized, even if the the open-loop gain (A_o) in not consistent.

Possible inconsistencies:

Gain may be be inconsistent from one device to the next due to manufacturing differences.

Gain may be be inconsistent over a range of signal frequencies, as seen above..

Gain may be be inconsistent over a range of signal amplitudes and/or output magnitudes (nonlinear gain).



Bandwidth extension

The high-frequency side of this has already been illustrated as it pertains to op amps, but it also works for low frequencies.

in general: If the open-loop amplifier has the following transfer function: $\frac{A_o \cdot s}{(s + \omega_L) \cdot \left(1 + \frac{s}{\omega_H}\right)}$ A_o = Midband gain
 ω_L = Low-frequency 3dB roll-off point
 ω_H = High-frequency 3dB roll-off point

With feedback

High-frequency 3dB roll-off point = $\omega_{Hf} = \omega_H \cdot (1 + A_o \cdot B)$ OR $f_{Hf} = f_H \cdot (1 + A_o \cdot B)$

Low-frequency 3dB roll-off point = $\omega_{Lf} = \frac{\omega_L}{1 + A_o \cdot B}$ OR $f_{Lf} = \frac{f_L}{1 + A_o \cdot B}$

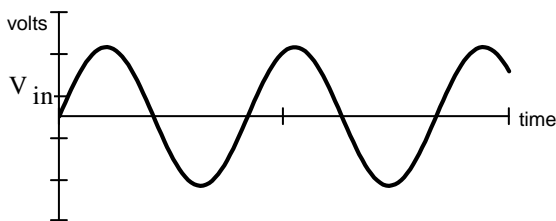
resulting closed-loop transfer function: $\frac{A_f \cdot s}{(s + \omega_{Lf}) \cdot \left(1 + \frac{s}{\omega_{Hf}}\right)}$ A_f = Midband gain
 ω_{Lf} = Low-frequency 3dB roll-off point
 ω_{Hf} = High-frequency 3dB roll-off point

Noise Reduction (increase Signal to Noise (S/N) ratio)

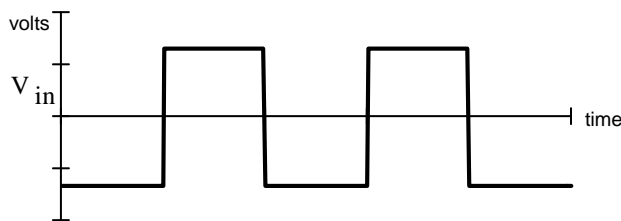
Unwanted additions to the signal, like hiss or hum, are called noise. Every amplifier stage adds some noise. Feedback can reduce noise as long as it isn't added in at the input to the first stage, where it looks just like a part of the signal. The later in the amplifier the noise is added, the more effective feedback is at reducing it.

Reduce Signal Distortion (Total Harmonic Distortion, THD)

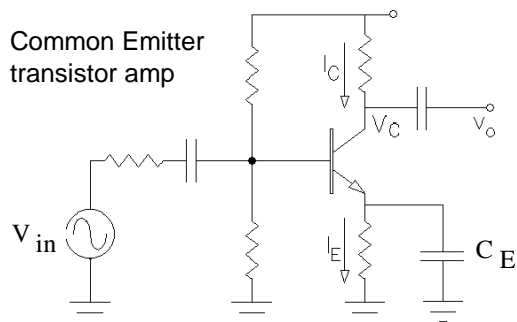
Signal distortion is usually caused by either nonlinear gain, like those shown earlier, or by the frequency response limitations which distort the relationship of the fundamental and harmonics of a signal. Noise is also a form of distortion.



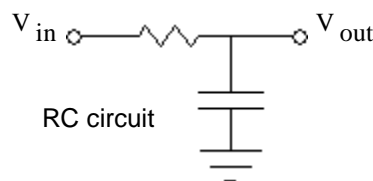
Distortion caused by nonlinear gain



Distortion caused by limited frequency response



Common Emitter transistor amp

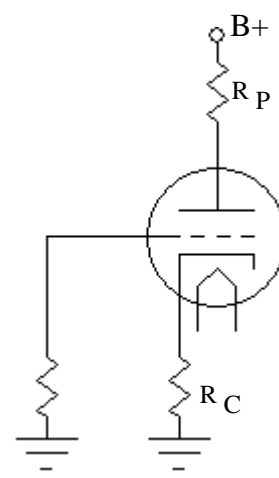
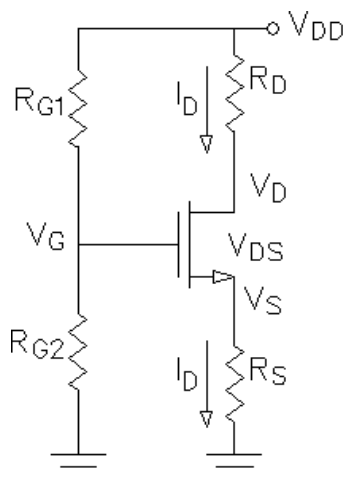
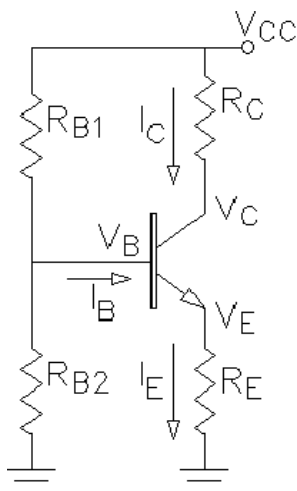


RC circuit

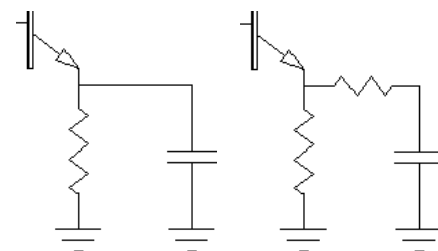
Stabilizing the gain also makes the gain more linear. Extending the bandwidth lets more harmonics through. Feedback improves both.

Bias Stabilization

This type of feedback is part of an individual stage of an amplifier, rather than around the entire amplifier, so, in a way, it doesn't fit the discussion here. The discrete devices we use for amplification generally need to be biased into a conduction state before a signal is applied which creates current and voltage variations about the bias points. (Exceptions are class B and class D power amplifiers.)



The purpose of R_E , R_S and R_C in each of these amplifier stages is the same. They provide negative feedback so as to stabilize the current flowing down through the device. The DC component of that current is the bias current. They all affect the signal as well, increasing the input impedance, reducing distortion and limiting the gain to about R_C/R_E , R_D/R_S , and R_P/R_C respectively. Placing a bypass capacitor in parallel with this resistor can remove the feedback at signal frequencies, but you lose the other benefits. Partial bypass can be a compromise.

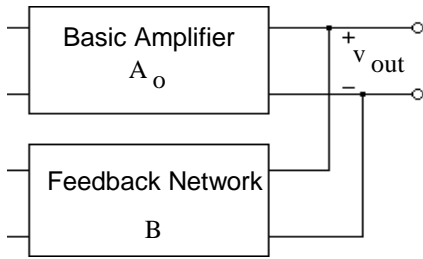


Bypass

Partial bypass

Output Impedance

For an amplifier with voltage feedback, the output impedance is reduced. By "voltage feedback", I mean that the feedback signal is a sample of the output voltage. Also called "shunt" feedback. (See the four feedback topologies.)

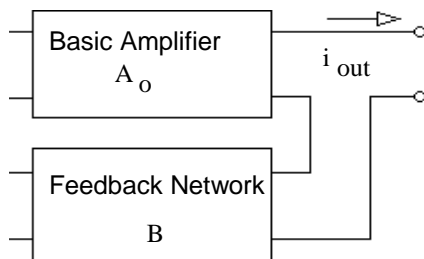


Ideally:

$$Z_{outf} = \frac{Z_{out}}{1 + A_o \cdot B}$$

If the desired output of your amp is voltage, then a lower output impedance is exactly what you want.

For an amplifier with current feedback, the output impedance is increased. By "current feedback", I mean that the feedback signal is a sample of the output current. Also called "series" feedback. (See the four feedback topologies.)



Ideally:

$$Z_{outf} = Z_{out} \cdot (1 + A_o \cdot B)$$

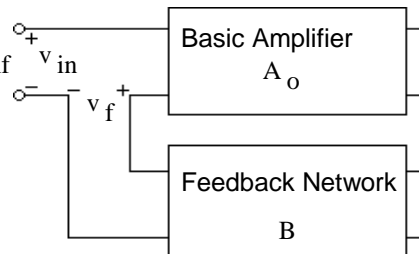
If the desired output of your amp is current, then a higher output impedance is exactly what you want.

Input Impedance

For an amplifier with voltage mixing, the input impedance is increased. By "voltage mixing", I mean that the feedback signal is voltage combined in series with the input voltage. Also called "series" mixing. (See the four feedback topologies.)

Ideally:
$$Z_{inf} \cdot (1 + A_o \cdot B) = Z_{inf}$$

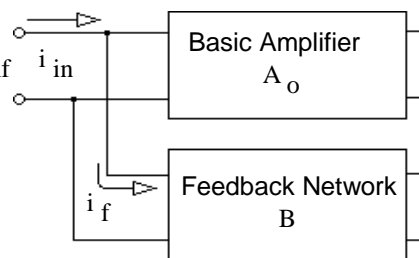
If the desired input of your amp is voltage, then a higher input impedance is exactly what you want.



For an amplifier with current mixing, the input impedance is reduced. By "current mixing", I mean that the feedback signal is a current reducing the input current. Also called "shunt" mixing. (See the four feedback topologies.)

Ideally:
$$\frac{Z_{inf}}{1 + A_o \cdot B} = Z_{inf}$$

If the desired input of your amp is current, then a lower input impedance is exactly what you want.



Assumptions to get ideal answers:

1. Feedback networks have ideal input and output resistances.
2. Loads are ideal for the type of output. $R_L = \infty$ for voltage outputs. $R_L = 0$ for current outputs.

Otherwise improvements in output and input impedances will be less than ideal.

To Find the Input Impedance

1. Draw out the full innards of all the boxes in the appropriate topology shown below, including all the non-ideal characteristics you want to include.
2. For topologies a) and d), below, connect nothing to the output, unless you want to see the the effect of the load on the input impedance. Even then, it's easier to work it out without a load first and then add the effect of the load in later.
For topologies b) and c), below, connect a short to the output, unless you want to see the the effect of the load. Again, it's easier to work without a load first.

3. For topologies a) and c), below, connect an ideal voltage source to the input and find the input current. Use whatever circuit analysis methods you like.

For topologies b) and d), below, connect an ideal current source to the input and find the input voltage. Use whatever circuit analysis methods you like.

4.
$$Z_{inf} = \frac{V_{in}}{i_{in}}$$

To Find the Output Impedance

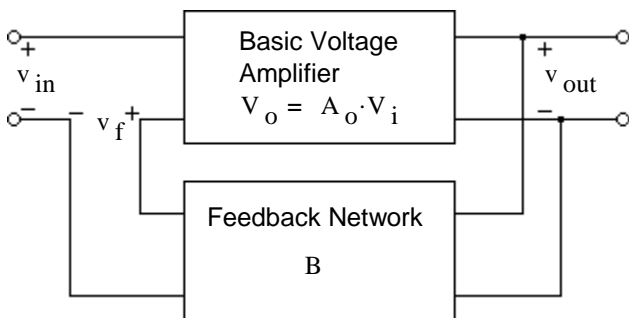
1. Draw out the full innards of all the boxes in the appropriate topology shown below, including all the non-ideal characteristics you want to include.
2. For topologies a) and c), below, connect a short at the input.
For topologies b) and d), below, connect nothing to the input.

3. For topologies a) and d), below, connect an ideal voltage source to the output and find the current flowing back into the output.

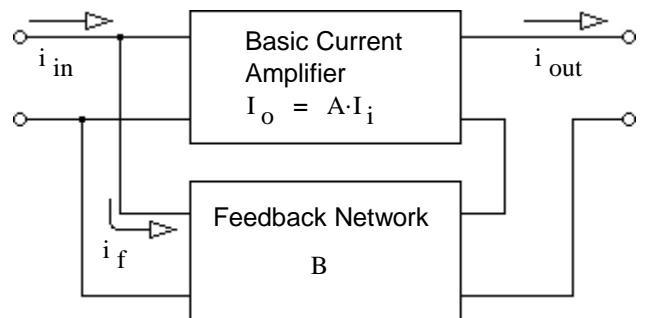
For topologies b) and c), below, connect an ideal current source to the output to force current back into the output. Find the voltage at the output.

4.
$$Z_{inf} = \frac{V_{out}}{-i_{out}}$$

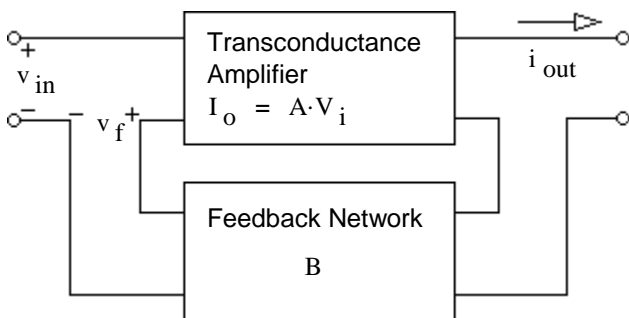
The Four Feedback Topologies



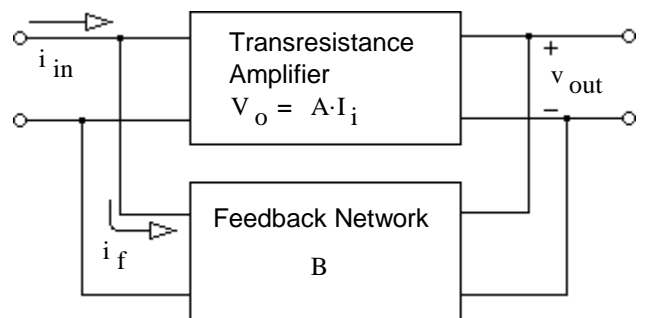
a) Voltage-mixing voltage-sampling (series-shunt)



b) Current-mixing current-sampling (shunt-series)



c) Voltage-mixing current-sampling (series-series)



d) Current-mixing voltage-sampling (shunt-shunt)

Some examples of the different topologies

a) Voltage-mixing voltage-sampling (series-shunt)

R_e is the AC signal resistance from emitter to ground

Input impedance: $R_i = R_{B1} \parallel R_{B2} \parallel \beta \cdot (r_e + R_e)$

Output impedance: $R_o = R_C \parallel r_o$ Often neglected

AC collector resistance: $r_c = R_C \parallel R_L \parallel r_o$

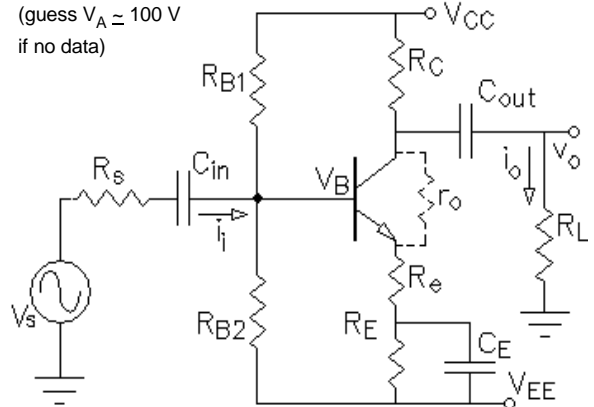
Voltage gain: $A_v = \frac{v_o}{v_b} = \frac{r_c}{r_e + R_e}$

OR: $\frac{v_o}{v_s} = \frac{R_i}{R_s + R_i} \cdot \frac{r_c}{r_e + R_e}$

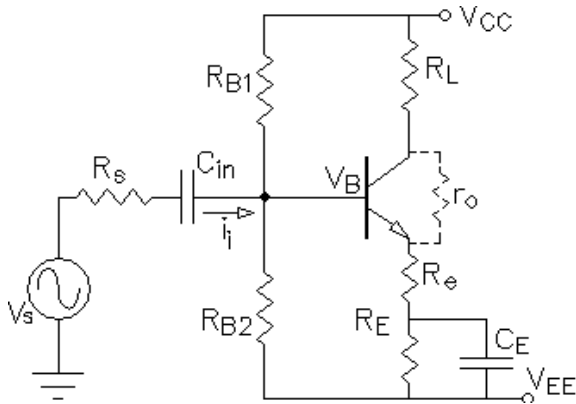
Current gain: $A_i = \frac{i_o}{i_i} = \frac{r_c}{r_e + R_e} \cdot \frac{R_i}{R_L} = A_v \cdot \frac{R_i}{R_L}$

R_E is the DC resistance from emitter to ground

$r_o = \frac{V_A}{I_C}$ Early voltage. (guess $V_A \approx 100$ V if no data)



c) Voltage-mixing current-sampling (series-series)

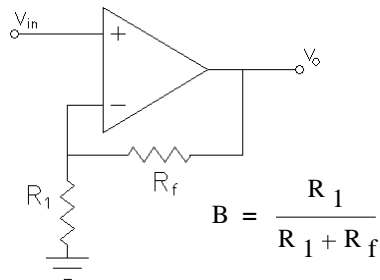


Input impedance: $R_i = R_{B1} \parallel R_{B2} \parallel \beta \cdot (r_e + R_e)$

Output impedance: $r_o = \frac{V_A}{I_C}$ Early voltage. (guess $V_A \approx 100$ V if no data)

Transconductance $g = \frac{1}{r_e + R_e}$

a) Voltage-mixing voltage-sampling (series-shunt)



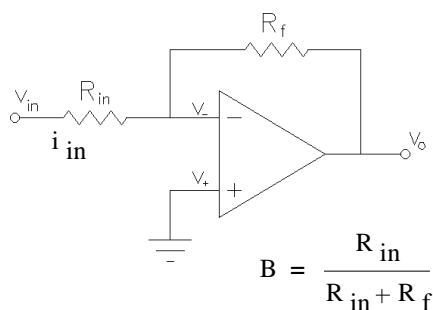
Input impedance: see op-amp data for R_i

Output impedance: $R_{outf} = \frac{R_{out}}{B}$ up to current limits of the op amp

Voltage gain: $1 + \frac{R_f}{R_1}$

Feedback signal mixing is done within the op amp.

d) Current-mixing voltage-sampling (shunt-shunt)



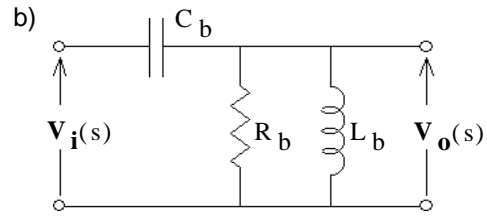
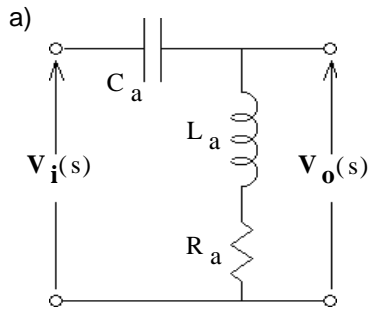
Input impedance: R_{in} for voltage input

Input impedance: ≈ 0 for current input if you remove R_{in}

Output impedance: $R_{outf} = \frac{R_{out}}{B}$ up to current limits of the op amp

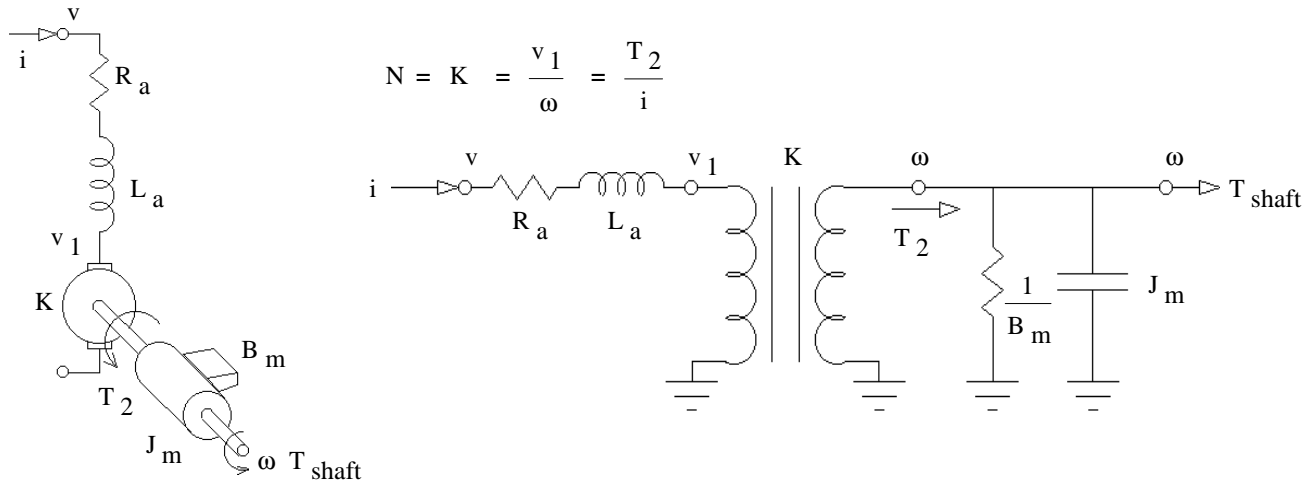
Voltage gain: $-\frac{R_f}{R_1}$

1. Find the input impedance ($Z_{in}(s)$) and output impedance ($Z_{out}(s)$) of each of the circuits below.



2. Now hook circuit b), above to the output of circuit a). Find the input impedance ($Z_{in}(s)$) and output impedance ($Z_{out}(s)$) of the combination circuit.

3. The following is a model of a DC motor. Find the input and output impedances.



Answers

1. a) $Z_{in} = \frac{1}{C_a \cdot s} + R_a + L_a \cdot s$

$$Z_{out} = \frac{\frac{1}{C_a} \cdot s + \frac{R_a}{L_a \cdot C_a}}{s^2 + \frac{R_a}{L_a} \cdot s + \frac{1}{L_a \cdot C_a}}$$

b) $Z_{in} = \frac{1}{C_b \cdot s} + \frac{1}{\frac{1}{R_b} + \frac{1}{L_b \cdot s}}$

$$Z_{out} = \frac{\frac{1}{C_b} \cdot s}{s^2 + \frac{1}{R_b \cdot C_b} \cdot s + \frac{1}{L_b \cdot C_b}}$$

2. $Z_{in} = \frac{1}{C_a \cdot s} + \frac{1}{\frac{1}{R_a + L_a \cdot s} + \frac{1}{\frac{1}{C_b \cdot s} + \frac{1}{\frac{1}{R_b} + \frac{1}{L_b \cdot s}}}}$

$$Z_{out} = \frac{1}{\frac{1}{\frac{1}{C_a \cdot s} + \frac{1}{R_a + L_a \cdot s}} + \frac{1}{\frac{1}{C_b \cdot s} + \frac{1}{R_b + L_b \cdot s}}}$$

3. $Z_{in} = R_a + L_a \cdot s + \frac{K^2}{B_m + J_m \cdot s}$

$$Z_{out} = \frac{1}{\frac{K^2}{R_a + L_a \cdot s} + B_m + J_m \cdot s}$$

Find your textbook from your electronics class (ECE 2280 here at the U). Find the chapter or section which covers feedback in amplifiers. Read the sections covering bandwidth or frequency response, noise reduction, distortion reduction and gain reduction.

1. Show that the low-frequency 3dB roll-off point = $\omega_{Lf} = \frac{\omega_L}{1 + A_o \cdot B}$

where:

A_o = Midband gain of basic amplifier

B = the feedback factor

ω_L = Low-frequency 3dB roll-off point of basic amplifier

ω_{Lf} = Low-frequency 3dB roll-off point with feedback

Note: To do this, you'll take the basic open-loop amplifier transfer function and use it to write the closed-loop transfer function. Unfortunately the result is pretty messy and it can be hard to see what you can reasonably leave out to approximate the closed-loop transfer function.

A much easier approach is to pretend the basic amplifier doesn't have a high-frequency roll-off and eliminate the high-frequency pole from it's transfer function before you write the closed-loop transfer function. This is reasonable to do because the high-frequency roll-off will have very little effect on the low-frequency roll-off point.

2. Draw the ideal series-shunt feedback topology. You may leave out the output impedance (or source resistance) of the input voltage and the input impedance of the feedback network. By "leave out" you may consider them to be zero and ∞ , respectively.

3. Show that the input resistance with feedback is: $R_{if} = (1 + A_o \cdot B) \cdot R_i$

where:

A_o = the open-loop amplifier gain

R_i = the open-loop input resistance

B = the feedback factor

4. Show that the output resistance with feedback is: $R_{of} = \frac{R_o}{(1 + A_o \cdot B)}$

R_o = the open-loop output resistance

ECE 3510 homework # M1 Requires action on Tue, 12/6 Due Thur, 12/8

Go to ME Design day in the Union on Thursday, 12/6 sometime from 11:00 to 3:00. See <http://mech.utah.edu/newsroom/design-day/>

Write **several paragraphs** about what you see there. Especially:

1. Note control systems and/or systems with feedback.
2. Tell which senior project most impressed you and why.
3. Observe at least part of one of the competitions (main mechatronics robot competition, 1:00 - 3:00) and write at least one paragraph about it (suggest improvements).