

**Problem 1 – (35 points)**

**SOLUTION**

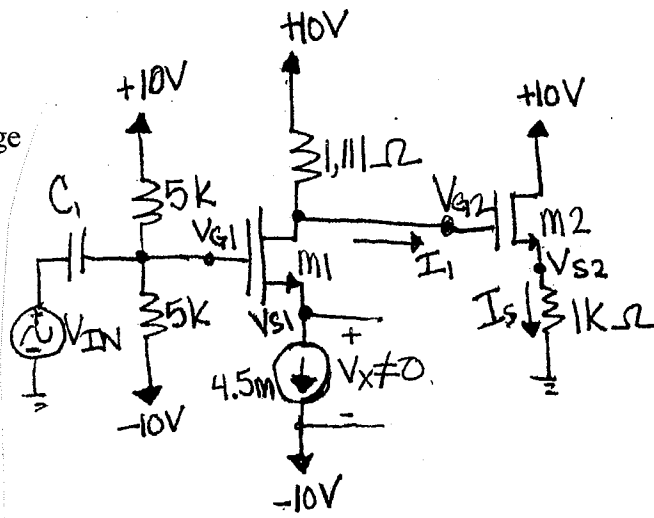
Use:  $V_t = 2V$

$k_n'(W/L) = 1mA/V^2$

$\lambda = 0$

$V_{IN} = 5 + 0.005\sin(20t)$

For DC analysis, assume that the capacitors act as an open. The current source is not ideal and has a voltage drop across it.



(a) Solve for the DC currents:

- a.  $I_1 = 0$
- b.  $I_s = 1.35mA$

(b) Solve for the DC voltages:

- a.  $V_{G2} = 5V$
- b.  $V_{S2} = 1.35$
- c.  $V_{S1} = -5V$

(c) Verify that transistor M2 is saturated.

(d) State the DC bias point for transistor M1.

(e) Assuming that the transistor amplification is  $V_{S2}/V_{IN} = -4V/V$ . Assume the input frequency is operating within the circuits operating range. What is the **PEAK** value seen at  $V_{S2}$  using the  $V_{IN}$  value stated above.

$V_{G1} = 0V$

$I_{S1} = 4.5m = \frac{1}{2}(1m)[V_{GS} - 2]^2$

$2 \pm \sqrt{9} = V_{GS}$

$V_{GS} = 2 \pm 3 = 5, -X$  Not on

$V_{S1} = -5V$

$V_{G2} = 10 - 1,111(4.5m) \approx +5V$

$\frac{V_{S2}}{1k} = \frac{1}{2}(1m) \cdot \frac{[5 - V_{S2} - 2]^2}{3 - V_{S2}}$

$2V_{S2} = 9 - 6 \cdot V_{S2} + V_{S2}^2$

$V_{S2}^2 - 8V_{S2} + 9 = 0$

$V_{S2} = \frac{+8 \pm \sqrt{64 - 4(9)}}{2} = \frac{+8 \pm 5.3}{2} = 6.65, 1.35$

$I_s = \frac{V_{S2}}{1k} = 1.35mA$

(c)  $V_{D2} = +10V$

$V_{G2} = +5V$

$V_{S2} = 1.35$

$V_{D2} = +10V > (V_{G2} = +5V) - (V_t = 2)$   
 $10 > 3$

(d)  $V_{GS1} = +5V, I = 4.5mA$

(e)  $\frac{V_{S2}}{V_{IN}} = -4V/V$

$V_{S2} = 1.35 - 4(5m)\sin(20t)$

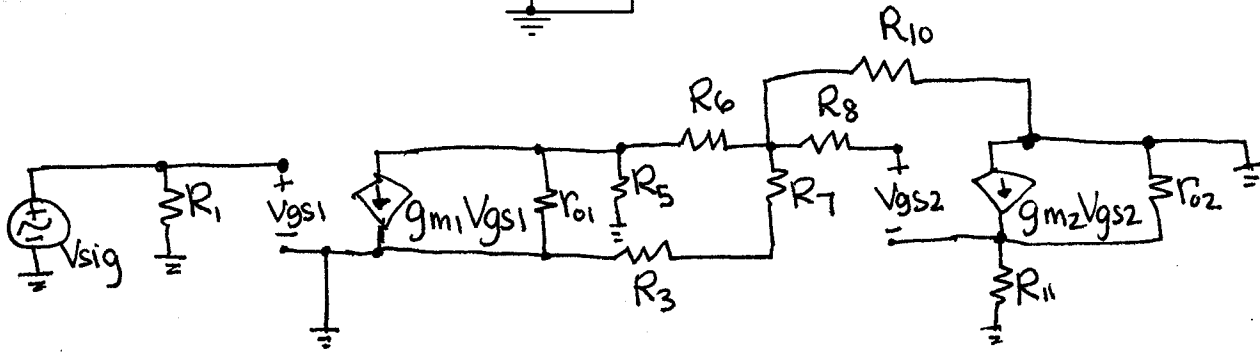
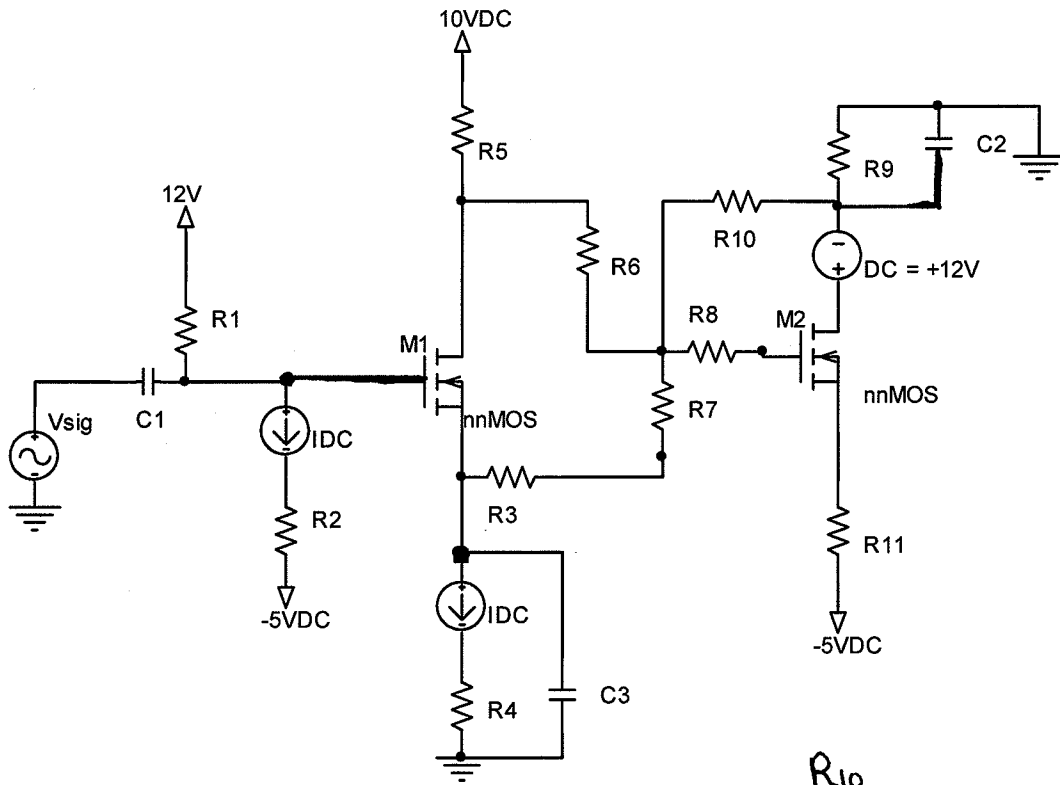
$V_{S2\text{ peak}} = 1.35 + 20m = 1.37V$

$V_{GS2} = 5 - 6.65 = -1.65 \times \text{off } (V_{GS} < V_t)$



**Problem 3 – (12 points)**

For the circuit shown below, **draw** the AC small-signal equivalent circuit (use hybrid- $\pi$  or model T). Make sure that everything is labeled in terms of the transistor number. (e.g.  $g_{m1}$ ,  $v_{gs2}$ ,  $r_{o1}$ , etc.).  $\lambda \neq 0$  for all transistors. (i.e. draw the small-signal with  $r_o$  included).  $v_{sig} = 0.005 \sin(20t)$  AC.

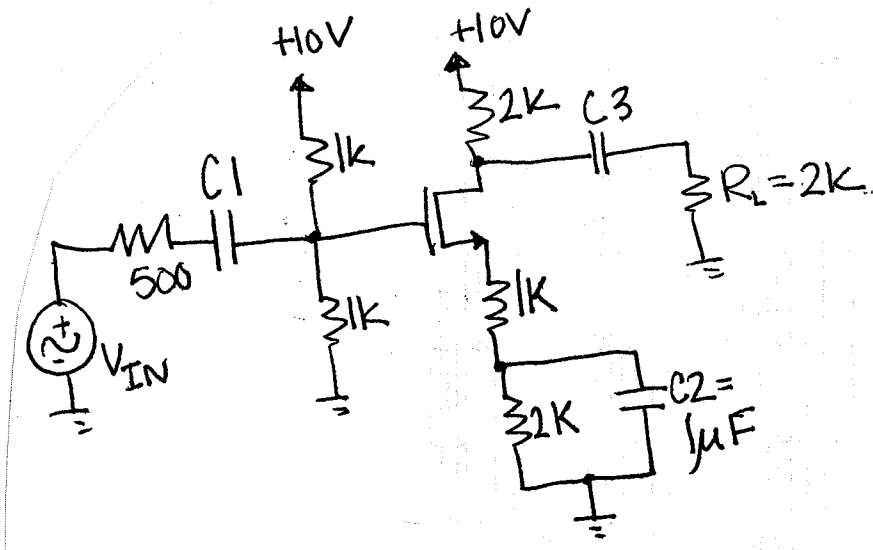


**Problem 4 – (13 points)**

Use:  $g_m=1\text{mA/V}$ ,  $\lambda=0$ , and  $C_{gs}=C_{gd}=5\text{pF}$ .

Assume that  $C_2$  yields that largest pole value for the external capacitors.

What is the operating range for the amplifier below (in Hz)?



Low:

$$C_2: \frac{1}{C_2 \cdot R_{eq}} = \frac{1}{\mu \cdot (2k \parallel (\frac{1}{g_m} + 1k))} = \frac{1}{\mu \cdot (2k \parallel 1k + 1k)} = 1k \frac{\text{rad}}{\text{sec}}$$

High:

$$C_{eq} = C_{gs} + C_{gd}(1 + g_m \cdot R_D) = 5p + 5p(1 + 1m \cdot 1k) = 15pF$$

$\downarrow$   
 $2k \parallel 2k$

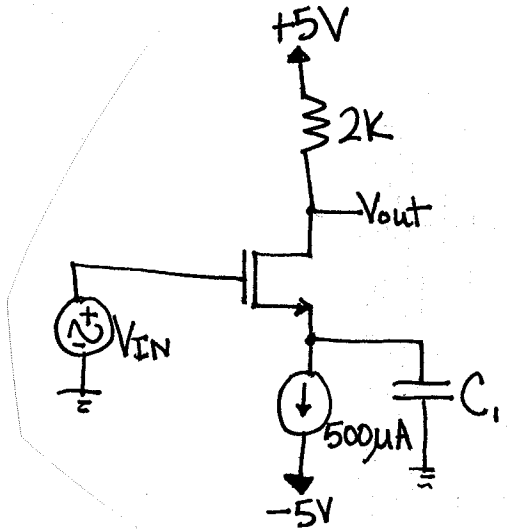
$$\omega_H = \frac{1}{C_{eq} \cdot R_{sig}} = \frac{1}{15p \cdot (1k \parallel 1k \parallel 500)} = \frac{1}{15p \cdot 250} \approx 267M \frac{\text{rad}}{\text{sec}}$$

159 Hz to 43 MHz

**Problem 5 – (5 points)**

$V_T=2V, \lambda=0, k_n'(W/L)=1mA/V^2$ . Does this circuit operate as a **linear** AC amplifier? If so, what is the gain,  $\frac{V_o}{V_{sig}}$ , of the following circuit? If not, explain why.

$V_{IN} = 5 + \sin(\omega t)$ . (assume that  $\omega$  is in the operating range of the circuit). ~~If not, explain why.~~



$$V_G = +5V$$

$$500\mu = \frac{1}{2}(1m)(V_{GS} - 2)^2$$

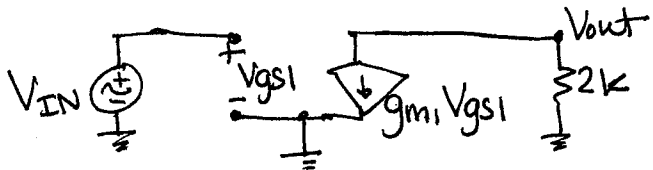
$$\pm\sqrt{1} + 2 = V_{GS}$$

$$V_{GS} = +3, +1 \leftarrow \text{off}$$

$$V_D = 5 - 2k(500\mu) = 5 - 1 = +4V$$

$$V_D = +4 > (V_G - V_T) = +5 - 2 = +3$$

$$\therefore \text{SATURATED}$$



$$V_{out} = -g_{m1} V_{gs1} \cdot 2k, \quad V_{gs1} = V_{IN}$$

$$g_{m1} = 1m(3 - 2) = 1m$$

$$\frac{V_{out}}{V_{IN}} = -1m(2k) = -2V/V$$

~~$V_{IN, total} =$~~

$$V_{out, total} = +4 - 2\sin(\omega t)$$

$$V_{peak} = +6V (V_{IN} = 4V) \rightarrow +6 > (V_G - V_T) = +2 \checkmark$$

$$V_{min} = +2V (V_{IN} = +6V) \rightarrow +2 < (V_G - V_T) = +4 \times \text{NOT SATURATED}$$

$\therefore$  NOT LINEAR