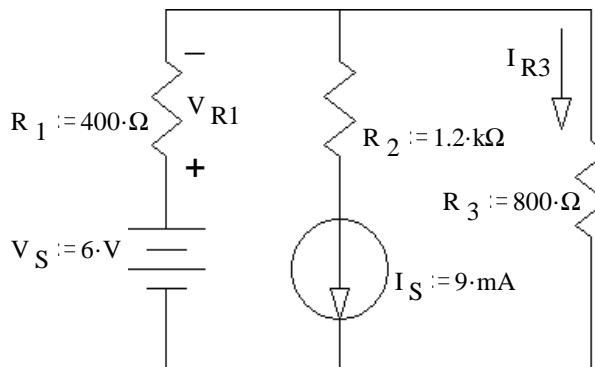


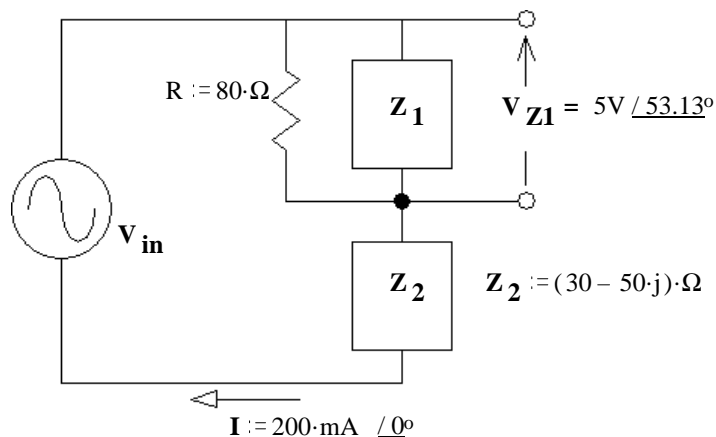
Closed Book, Closed notes except preprinted blue sheet, Calculators OK. Show all work to receive credit. Circle answers, show units, and round off reasonably

1. (17 pts) Use the method of superposition to find the voltage across  $R_1$  ( $V_{R1}$ ) and the current through  $R_3$  ( $I_{R3}$ ).  
Be sure to clearly show and **circle** your intermediate results.



2. (20 pts)

a) Find  $V_{in}$ .

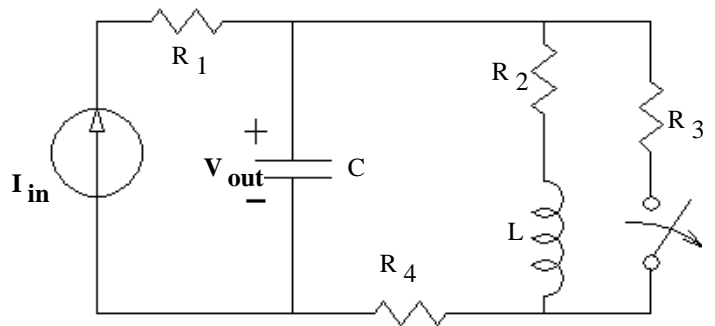


b) Find  $Z_1$  in polar form.

3. (17 pts) a) Find the s-type transfer function of the circuit shown.  $I_{in}$  is the input and  $V_{out}$  is the "output".

You MUST show work to get credit. Simplify your expression for  $H(s)$  so that it is a ratio of simple polynomials just like my examples.

a)  $H(s) = ?$

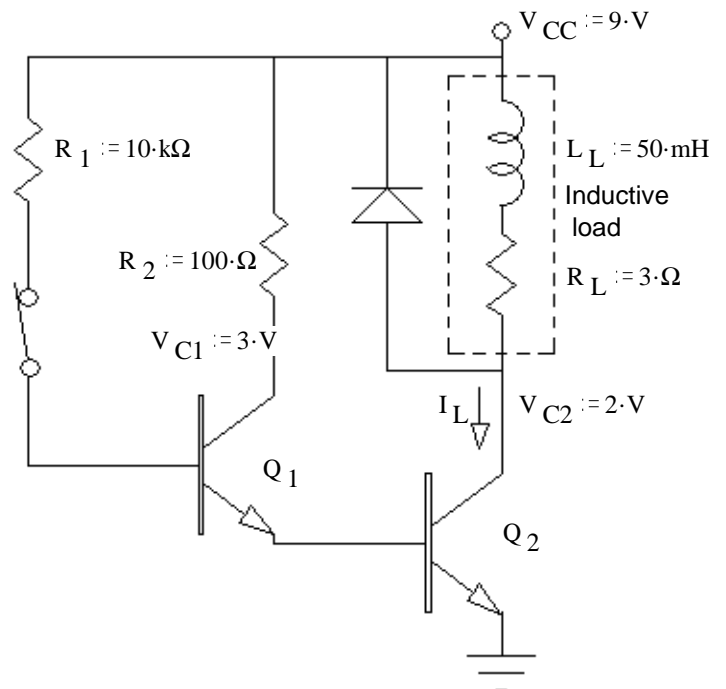


- b) Does the transfer function have any zeros? If yes, express them in terms of the circuit parts.

4. (34 pts) A couple of transistors are used to control the current flow through an inductive load.

a) You measure the voltage at each collector (referenced to ground) as shown on the drawing. Find the power dissipated by transistor  $Q_2$ .

$P_{Q2} = ?$



- b) Find the  $\beta$  of transistor  $Q_2$ .  $\beta_2 = ?$

You may assume that the emitter current of  $Q_1$  is approximately equal to the collector current of  $Q_1$ .

4, Continued c) Find the  $\beta$  of transistor  $Q_1$ .  $\beta_1 = ?$

d) Find the minimum  $\beta$  for transistor  $Q_1$  to be in saturation.  $\beta_{1\min} = ?$

You replace  $Q_1$  with a different transistor so that  $\beta_1 > \beta_{1\min}$ . Use this from now on.

e) Find the new load current.  $I_L = ?$  Be sure to explicitly check any assumption you make about the state of  $Q_2$ .

f) Find the power dissipated in transistor  $Q_2$ .  $P_{Q2} = ?$

g) The diode in this circuit conducts a significant current: (circle one)

A) never.

D) always.

B) when the switch first closes.

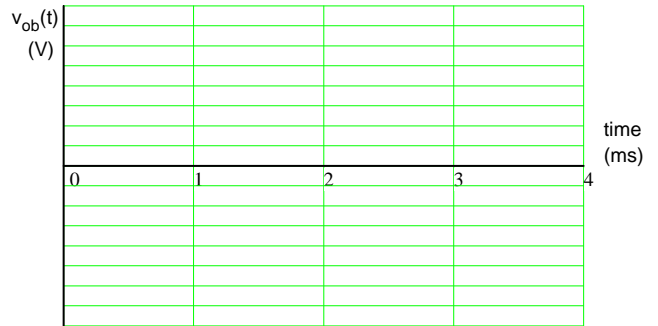
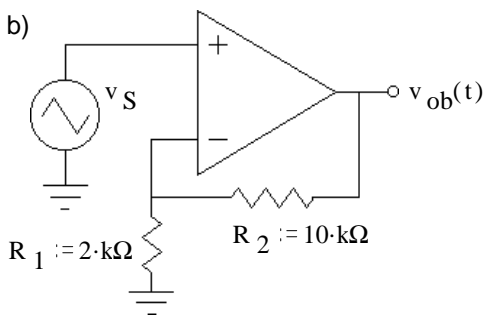
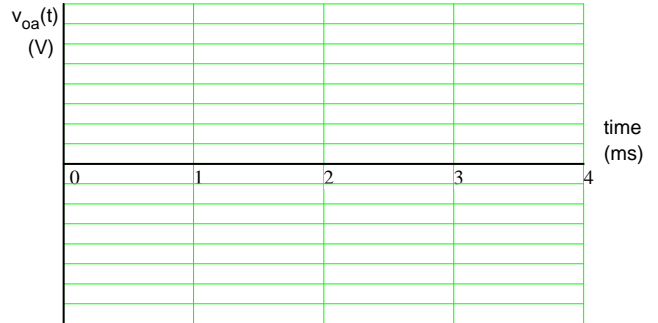
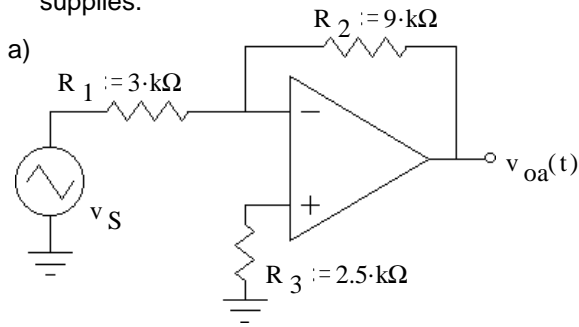
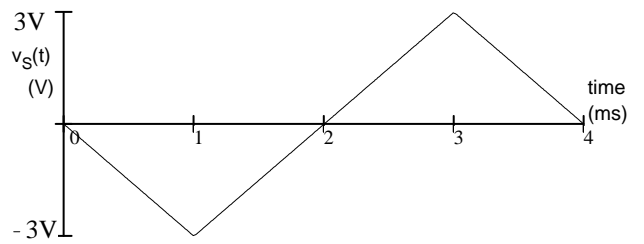
E) when the switch first opens.

C) whenever the switch is closed.

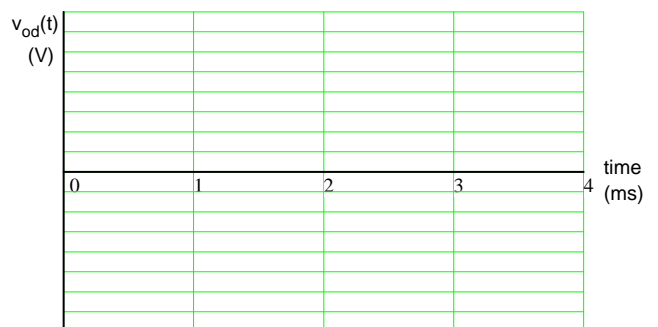
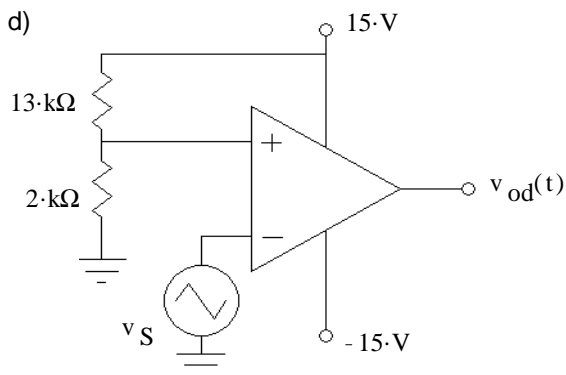
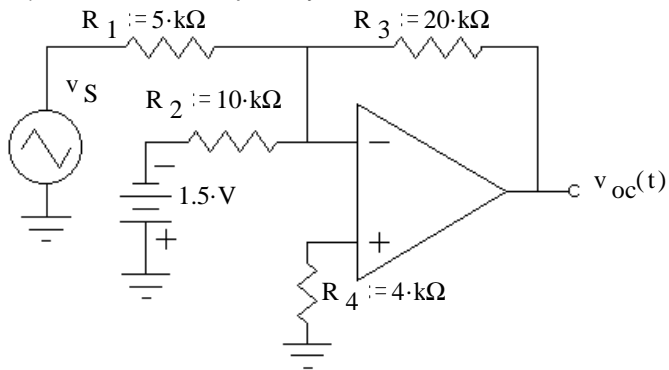
F) whenever the switch is open.

h) What is the maximum diode current you expect when the switch is cycled. (Answer 0 if it never conducts.)

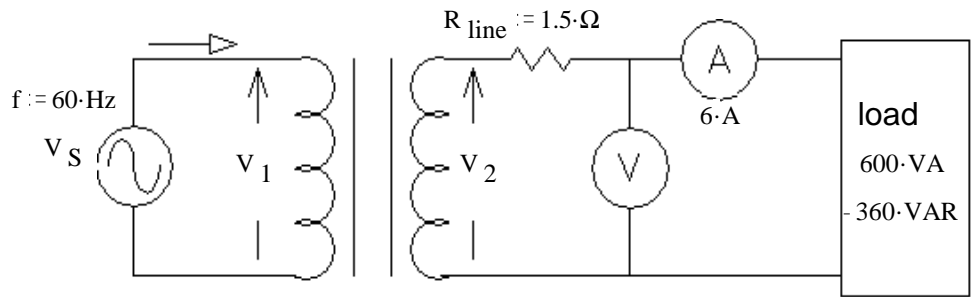
5. (32 pts) The same input signal (at right) is connected to several op-amp circuits below. Sketch the output waveform for each circuit. Clearly label important voltage levels on each output. If I can't easily make out what your peak values are, I'll assume you don't know. Don't forget to show inversions. All op-amps are powered by  $\pm 15\text{ V}$  power supplies.



c) Please note the polarity of the 1.5-V DC source.



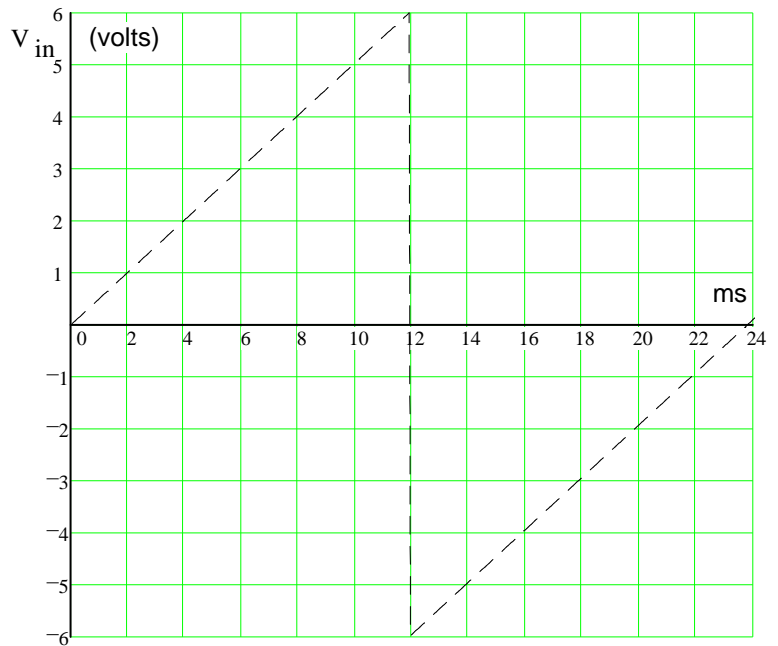
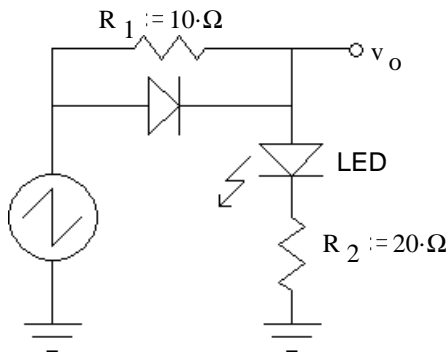
6. (36 pts) A load is connected as shown. The load uses 600 VA and -360 VAR. The RMS ammeter measures 6 A. Find the following: (Be sure to use correct units for each value.)
- a) The real power used by the load.  $P_{\text{load}} = ?$



- b) The load's power factor.  $\text{pf}_{\text{load}} = ?$
- c) The power factor is: i) leading ii) lagging (circle one)
- d) What does the RMS voltmeter measure?
- e) The load consists of two parts in series. Draw the parts in the box above and find the values.
- f) How much power does  $R_{\text{line}}$  waste?  $P_{R_{\text{line}}} = ?$
- g) How much power does the source provide?  $P_S = ?$
- h) What is the secondary voltage?  $|\mathbf{V}_2| = ?$  Hint: Remember, you can't add magnitudes of complex numbers.
- i) The transformer shown in the circuit is ideal. It is rated at 800/200 V, 1 kVA, 60 Hz. Find  $V_1$ .  $|\mathbf{V}_1| = ?$
- j) Is this transformer operating within its ratings? Show your evidence.
- k) The load box cannot be opened. Add (draw it) another component to the circuit above which can correct the power factor (make  $\text{pf} = 1$ ). Show the correct component in the correct place and find its value. This component should not affect the real power consumption of the load.

- l)  $V_L$  will i) decrease ii) stay the same iii) increase when this part is added.

7. (24 pts) A voltage waveform (dotted line) is applied to the circuit shown. Accurately draw the output waveform ( $v_o$ ) you expect to see. Label important times and voltage levels.



Explicitly show important times, values and units

**Answers**

1.  $4.4 \cdot V$   $2 \cdot mA$       2. a)  $10.82V / -33.69^\circ$       b)  $29.4\Omega / 70.2^\circ$

3. a)  $\frac{1}{C} \cdot s + \frac{R_2 + R_4}{L \cdot C}$       b) yes,  $-\frac{R_2 + R_4}{L}$

$$s^2 + \frac{R_2 + R_4}{L} \cdot s + \frac{1}{L \cdot C}$$

4. a)  $4.67 \cdot W$       b)  $38.9$   
 c)  $78.95$       d)  $106.6$   
 e)  $2.93 \cdot A$       f)  $0.587 \cdot W$   
 g) E)      h)  $2.93 \cdot A$

6. a)  $480 \cdot W$       b)  $0.8$       c) i)  
 d)  $100 \cdot V$       e)  $13.3 \cdot \Omega$        $265 \cdot \mu F$   
 f)  $54 \cdot W$       g)  $534 \cdot W$       h)  $107 \cdot 3 \cdot V$   
 i)  $322 \cdot V$       j) No, too much current  
 k) Add a  $73.7 \cdot mH$  inductor in parallel with load.      l) iii

