## ECE2210 Final given: Spring 12

Ideal ammeter reads 15 mA .

1. $(20 \mathrm{pts})$ Find the values below. Show your work.
a) $\mathrm{R}_{5}=$ ?
b) $\mathrm{V}_{\mathrm{S}}=$ ?
c) $\mathrm{P}_{\mathrm{S}}=$ ?

2. (15 pts) a) Find the s-type transfer function of the circuit shown. Consider $\mathbf{I}_{\text {in }}$ as the input and $\mathbf{V}_{\mathbf{C}}$ as the "output". You MUST show work to get credit. Simplify your expression for $\mathrm{H}(\mathrm{s})$ so that the denominator is a simple polynomial with no coefficient before the highest-order s term in the denominator.
b) How many zeroes does this transfer function have? If it has 1 or more, express them (probably in terms of $\mathrm{R}_{1}, \mathrm{R}_{2}, \mathrm{R}_{3}, \mathrm{~L}$ and C ).

c) How many poles does this transfer function have?
3. (26 pts) a) The switch has been open for a long time and is closed (as shown) at time $\mathrm{t}=0$.
a) Find the initial and final conditions and write the full expression for $\mathrm{v}_{\mathrm{C}}(\mathrm{t})$, including all the constants that you find.
b) What is $v_{C}$ when $t=1.5 \tau$ ? $\quad v_{C}(1.5 \cdot \tau)=$ ?
c) At time $t=1.5 \tau$ the switch is opened again. Find the complete expression for $\mathrm{v}_{\mathrm{C}}(\mathrm{t})$, where $\mathrm{t}^{\prime}$ starts when the switch opens.


## ECE2210 Final given: Spring 12 p2

4. (30 pts) A transformer is rated at $500 \mathrm{~V} / 200 \mathrm{~V}$, 0.8 kVA . Assume the transformer is ideal and all voltages and currents are RMS.
a) The primary has 250 turns of wire. How many turns does the secondary have?
b) The the load voltage, $\quad \mathbf{V}_{\mathbf{L}}=120 \cdot \mathrm{~V}$

How big is the source voltage $\left(\left|\mathbf{V}_{\mathbf{S}}\right|\right)$ ?

c) The source supplies 600 VA of apparent power at a power factor of $85 \%$. Find the primary current, $\mathbf{I}_{\mathbf{1}}$ (magnitude and angle).
d) Find the secondary load, $\mathbf{Z}_{\mathbf{L}}$ (magnitude and angle). e) What is the load as seen by $\mathbf{V}_{\mathbf{S}}$ ? (magnitude and angle)
f) How much average power does the load dissipate?
g) How much average power does the power source $\left(\mathbf{V}_{\mathbf{S}}\right)$ supply?
h) Is this transformer operating within its ratings?

How do you know? (Specifically show a values which are or are not within a correct range.)
i) Using the given load voltage and power factor, what is the smallest load impedance magnitude that you could hook to this transformer and still operate within its ratings? $\quad\left|\mathbf{Z}_{\mathbf{L m i n}}\right|=$ ?
j) $\mathbf{Z}_{\mathbf{L}}$ is that found in part d). Add a component to the secondary side of the transformer so as to minimize the currents. Do not change the real power delivered to the load. Draw the part on the drawing and find its value.
k) Is this transformer operating within its ratings with this new part? How do you know? (Specifically show a values which are or are not within a correct range.)
5. (18 pts) The same input signal (at right) is connected to several op-amp circuits.
a) Sketch the output waveform for this circuit. Clearly label important voltage levels on the output. If I can't easily make out what your peak values are, I'll assume you don't know. Don't forget to show inversions.




## ECE2210 Final given: Spring 12 p3

5. b) Devise an op-amp circuit which will output the waveform shown below given the input waveform shown at right. Choose the power supplies and use whatever passive parts you need.
You may not use any other batteries, input signals, or power supplies beyond the two that power the op amp.



6. (26 pts) A couple of transistors are used to control the current flow through an inductive load. The switch has been closed, as shown, for a long time.
a) Assume both transistors are in saturation.

Find the minimum $\beta$ for transistor $Q_{2}$.

$$
\beta_{2 \min }=?
$$


b) Find the minimum $\beta$ for transistor $Q_{1}$ to be in saturation. $\quad \beta_{1 \min }=$ ?
c) Something is wrong. Transistor $\mathrm{Q}_{2}$ is getting too hot. You measure the current through the load and find that $\mathrm{I}_{\mathrm{L}}=800 \mathrm{~mA}$. How much power is being dissipated in transistor $\mathrm{Q}_{2}$ ?
d) Next you measure the voltage at the collector of $\mathrm{Q}_{1}$ and find that $\mathrm{V}_{\mathrm{C} 1}=3.2 \mathrm{~V}$ with respect to ground. Find the actual $\beta \mathrm{s}$ of both transistors and tell me what's wrong.

You replace the faulty component and everything is back to the way is was in part a)
e) The diode in this circuit conducts a significant current: (circle one)
A) never.
D) always.
B) when the switch closes.
E) when the switch opens.
C) whenever the switch is closed.
F) whenever the switch is open.
f) What is the maximum diode current you expect when the switch is cycled. (Answer 0 if it never conducts.)

Use constant-voltage-drop models for the diodes and LEDs on this exam. ECE2210
7. (27 pts) Assume that diodes $\mathrm{D}_{1}, \mathrm{D}_{2}$ and $\mathrm{D}_{4}$ DO conduct. Assume that diode $\mathrm{D}_{3}$ does NOT conduct.
a) Stick with these assumptions even if your answers come out absurd. Find the following and anything else you need in order to check the assumptions:
$\mathrm{I}_{\mathrm{R} 1}=$ $\qquad$
$\mathrm{V}_{\mathrm{R} 4}=$ $\qquad$
${ }^{\mathrm{I}} \mathrm{D}_{4}=$ $\qquad$
$\mathrm{I}_{\mathrm{R} 2}=$ $\qquad$

b) Based on the numbers above, was the assumption about $\mathrm{D}_{1}$ correct? yes no (circle one) How do you know? (Specifically show a value which is or is not within a correct range.)
c) Was the assumption about $\mathrm{D}_{2}$ correct? yes no How do you know? (Show a value \& range.)
d) Was the assumption about $\mathrm{D}_{3}$ correct? yes no

How do you know? (Show a value \& range.)
e) Was the assumption about $\mathrm{D}_{4}$ correct? yes no

How do you know? (Show a value \& range.)
f) Based on your answers to parts b), c), d) \& e), Circle one:
i) The real $\mathrm{I}_{\mathrm{D} 4}<\mathrm{I}_{\mathrm{D} 4}$ calculated in part a. Justify your answer.
ii) The real $\mathrm{I}_{\mathrm{D} 4}=\mathrm{I}_{\mathrm{D} 4}$ calculated in part a.
iii) The real $\mathrm{I}_{\mathrm{D} 4}>\mathrm{I}_{\mathrm{D} 4}$ calculated in part a.
8. (18 pts) A voltage waveform (dotted line) is applied to the circuit shown. Accurately draw the output waveform ( $\mathrm{v}_{\mathrm{o}}$ ) you expect to see. Label important times and voltage levels.


If you're not specific about your times and voltages, I'll assume you don't know!


## ECE2210 Final given: Spring 12

9. Do you want your grade and scores posted on the Internet?

If your answer is yes, then provide some sort of alias:
otherwise, leave blank
The grades will be posted on line in pdf form in alphabetical order under the alias that you provide here. I will not post grades under your real name. It will show the homework, lab, and exam scores of everyone who answers here.

## Answers

1. a) $300 \cdot \Omega$
b) $6.6 \cdot \mathrm{~V}$
c) $106 \cdot \mathrm{~mW}$
2. a) $\mathrm{s}^{2}+\left(\frac{1}{\mathrm{R}_{3} \cdot \mathrm{C}}+\frac{\mathrm{R}_{2}}{\mathrm{~L}}\right) \cdot \mathrm{s}+\frac{1}{\mathrm{~L} \cdot \mathrm{C}} \cdot\left(\frac{\mathrm{R}_{2}}{\mathrm{R}_{3}}+1\right)$
b) $1 \quad-\frac{\mathrm{R}_{2}}{\mathrm{~L}}$
c) 2
$\frac{1}{\mathrm{C}} \cdot \mathrm{s}+\frac{\mathrm{R}_{2}}{\mathrm{~L} \cdot \mathrm{C}}$
3. a) $9 \cdot V+5 \cdot V \cdot e^{-\frac{t}{90 \cdot \mu \mathrm{~s}}}$
b) $10.12 \cdot \mathrm{~V}$
c) $14 \cdot \mathrm{~V}-3.88 \cdot \mathrm{~V} \cdot \mathrm{e}^{-\frac{1}{140 \cdot \mu \mathrm{~s}}}$
4. a) 100
b) $300 \cdot \mathrm{~V}$
c) $2 \mathrm{~A} / 31.8^{\circ}$
d) $24 \Omega /-31.8^{\circ}$
e) $150 \Omega /-31.8^{\circ}$
f) $510 \cdot \mathrm{~W}$
g) $510 \cdot \mathrm{~W}$
h) NO
i) $30 \cdot \Omega$
j) Add a 121 mH inductor in parallel with the load
k) Still NO
5. a)

b)

6. a) 28.4
b) 96.2
c) $2.24 \cdot \mathrm{~W}$
d) $\beta_{2}:=32$
$\beta_{1}:=47.2$ TOO LOW !
e) E)
7. a) $25 \cdot \mathrm{~mA} \quad 2.6 \cdot \mathrm{~V} \quad 52 \cdot \mathrm{~mA} \quad 53 \cdot \mathrm{~mA} \quad 80 \cdot \mathrm{~mA}$
b) yes $\quad \mathrm{I}_{\mathrm{D} 1}:=80 \cdot \mathrm{~mA}>0$
c) yes $\quad \mathrm{I}_{\mathrm{D} 2}:=52 \cdot \mathrm{~mA}>0$
d) no $\quad \mathrm{V}_{\mathrm{D} 3}:=0.94 \cdot \mathrm{~V} \quad>0.7 \mathrm{~V}$
e) yes $\mathrm{I}_{\mathrm{D} 4}:=52 \cdot \mathrm{~mA}>0$
f) ii)
$\mathrm{V}_{\mathrm{D} 4}$ doesn't depend on the current through $\mathrm{D}_{3}$
8. 



