Diodes Notes

ECE 2210

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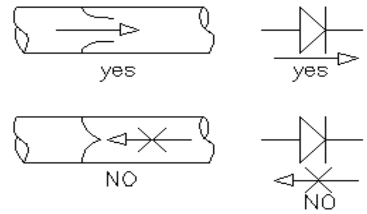
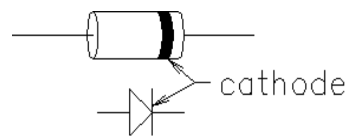
4/8/03,
2/27/07

Diodes are basically electrical check valves. They allow current to flow freely in one direction, but not the other. Check valves require a small forward pressure to open the valve. Similarly, a diode requires a small forward voltage (bias) to "turn on". This is called the forward voltage drop. There are many different types of diodes, but the two that you are most likely to see are silicon diodes and light-emitting diodes (LEDs). These two have forward voltage drops of about 0.7V and 2V respectively.



Mechanical check valve   Diode

silicon diode   LED



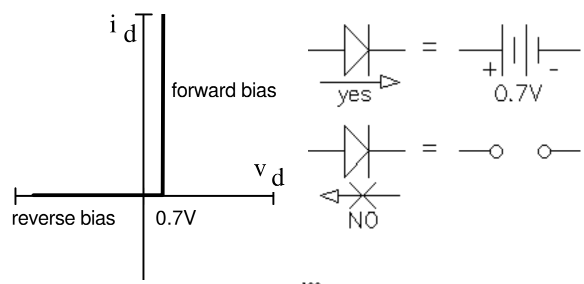
The electrical symbol for a diode looks like an arrow which shows the forward current direction and a small perpendicular line. The two sides of a diode are called the "anode" and the "cathode" (these names come from vacuum tubes). Most small diodes come in cylindrical packages with a band on one end that corresponds to the small perpendicular line, and shows the polarity, see the picture. Normal diodes are rated by the average forward current and the peak reverse voltage that they can handle. Diodes with significant current ratings are known as "rectifier" or "power" diodes. (Rectification is the process of making AC into DC.) Big power diodes come in a variety of packages designed to be attached to heat sinks. Small diodes are known as "signal" diodes because they're designed to handle small signals rather than power.

Diodes are nonlinear parts

So far in this class we've only worked with linear parts. The diode is definitely NOT linear, but it can be modeled as linear in its two regions of operation. If it's forward biased, it can be replaced by battery of 0.7V (2V for LEDs) which opposes the current flow. Otherwise it can be replaced by an open circuit. These are "models" of the actual diode. If you're not sure of the diode's state in a circuit, guess. Then replace it with the appropriate model and analyze the circuit. If you guessed the open, then the voltage across the diode model should come out less than +0.7V (2V for LEDs). If you guessed the battery, then the current through the diode model should come out in the direction of the diode's arrow. If your guess doesn't work out right, then you'll have to try the other option. In a circuit with multiple diodes (say "n" diodes), there will be 2^n possible states, all of which may have to be tried until you find the right one. Try to guess right the first time.

Constant-voltage-drop model

This is the most common diode model and is the only one we'll use in this class. It gives quite accurate results in most cases.



1 Assume the diode is operating in one of the linear regions (make an educated guess).

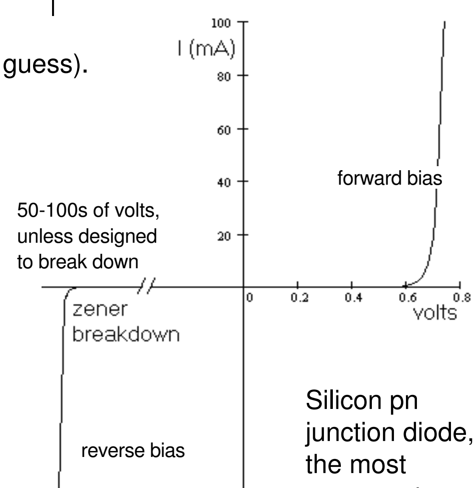
2 Analyze circuit with a linear model od the diode.

3 Check to see if the diode was really in the assumed region.

4 Repeat if necessary.

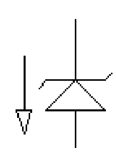
Actual diode curve

The characteristics of real diodes are actually more complicated than the constant-voltage-drop model. The forward voltage drop is not quite constant at any current and the diode "leaks" a little current when the voltage is in the reverse direction. If the reverse voltage is large enough, the diode will "breakdown" and let lots of current flow in the reverse direction. A mechanical check valve will show similar characteristics. Breakdown does not harm the diode as long as it isn't overheated.



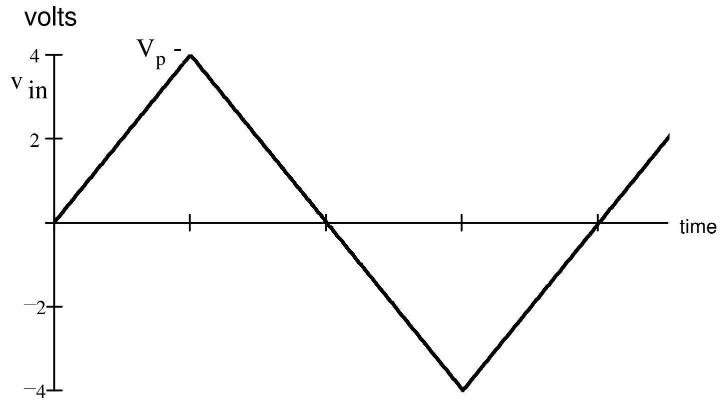
Silicon pn junction diode, the most common type.

Zener diodes are special diodes designed to operate in the reverse breakdown region. Since the reverse breakdown voltage across a diode is very constant for a large range of current, it can be used as a voltage reference or regulator. Zener diodes are also used for over-voltage protection. In the forward direction zeners work the same as regular diodes.



V_Z = zener voltage

I recommend that you try some of the DC analysis in the Diode Circuit Examples handout before you proceed here.



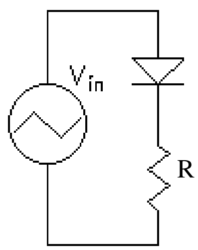
Diodes in AC Circuits

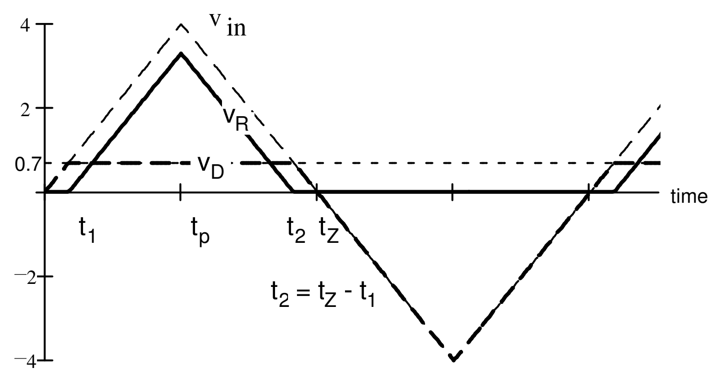
Diodes are often used to manipulate AC waveforms. We'll start with some triangular waveforms to get the general idea.

Diode doesn't conduct until v_{in} reaches 0.7V, so 0.7V is a dividing line between the two models of the diode.

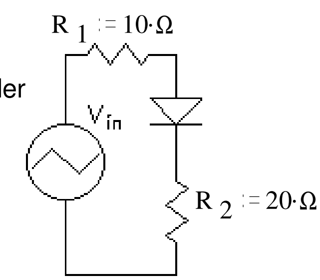
$$\text{slope} = \frac{0.7 \cdot V}{t_1} = \frac{V_p}{t_p}$$

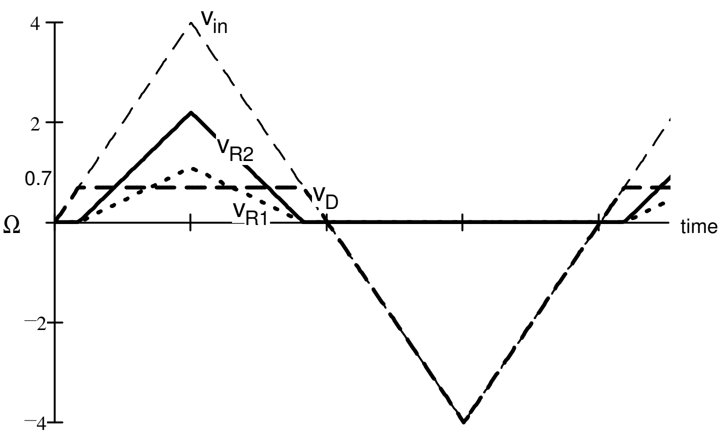
$$t_1 = \frac{0.7 \cdot V}{V_p} \cdot t_p$$





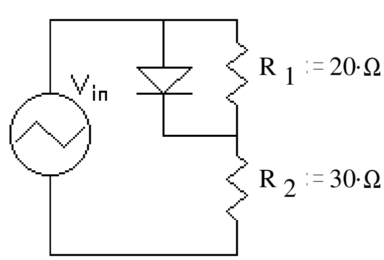
When the diode conducts, you're left with a voltage divider

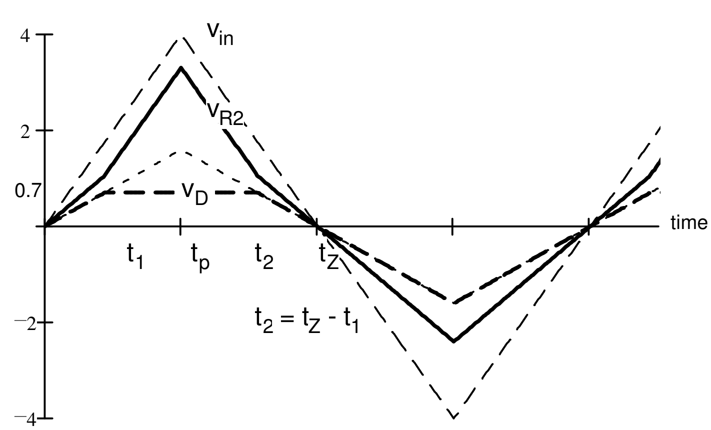




$$V_{R2peak} = \left(V_p - 0.7 \cdot V\right) \cdot \frac{R_2}{R_1 + R_2}$$

Sometimes it's helpful to figure out what the voltage across the diode would be if it never conducted (light dotted line).





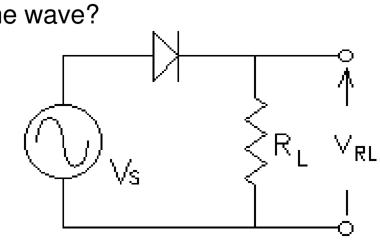
$$t_1 = \frac{0.7 \cdot V}{V_p \cdot \frac{R_1}{R_1 + R_2}} \cdot t_p$$

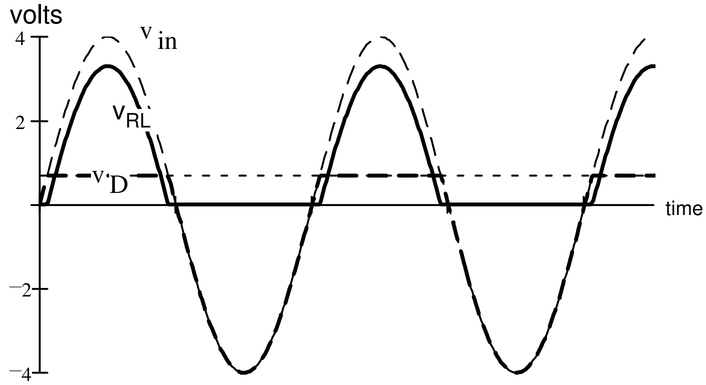
Rectifier Circuits & Power Supplies

Half-wave rectification

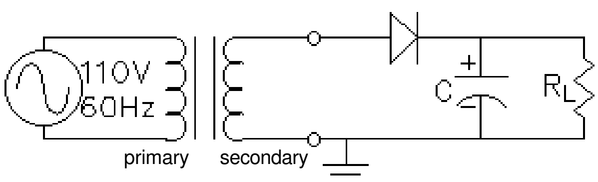
What if the input is a sine wave?

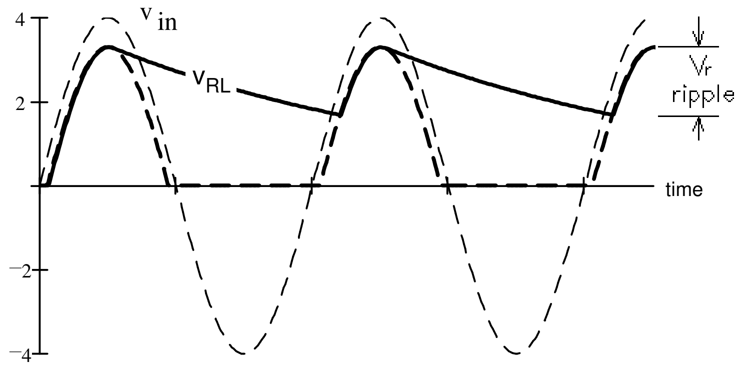
V_{RL} is now DC, although a bit bumpy. Some things are better if they' re bumpy, but not roads and not DC voltages.





Rectification is the process of making DC from AC. Usually the AC is derived from the AC wall outlet (often through a transformer) and the DC is needed for electronic circuitry modeled by R_L here.

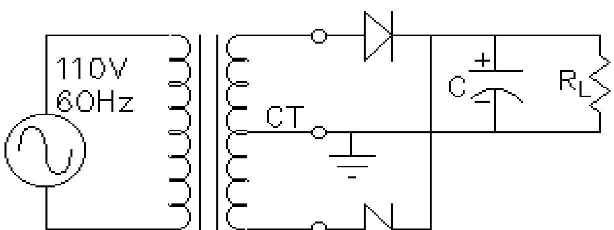


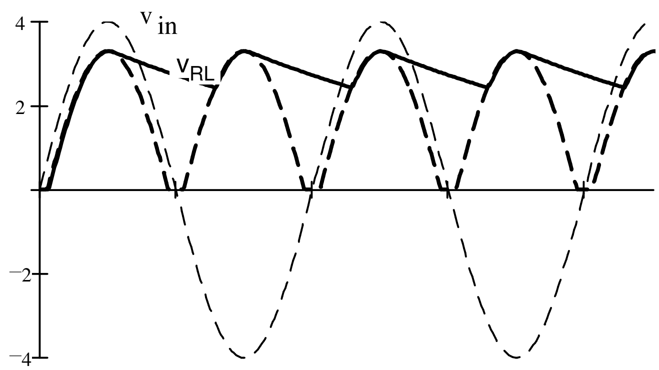


A "filter" capacitor (usually a big electrolytic) helps smooth out the bumps, although it sure looks like we could a bit bigger one here. The remaining bumpiness is called "ripple", V_r is peak-to-peak ripple

Full-wave rectification

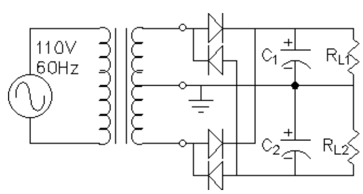
The "center tap" in the secondary of this transformer makes it easy to get full-wave rectification.



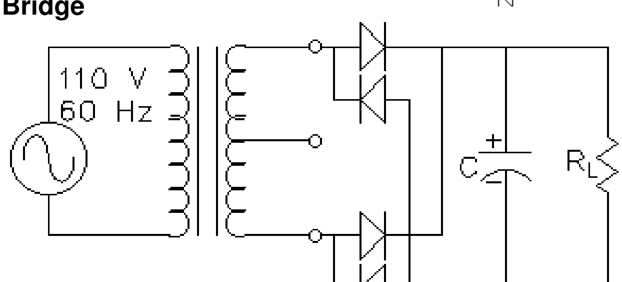


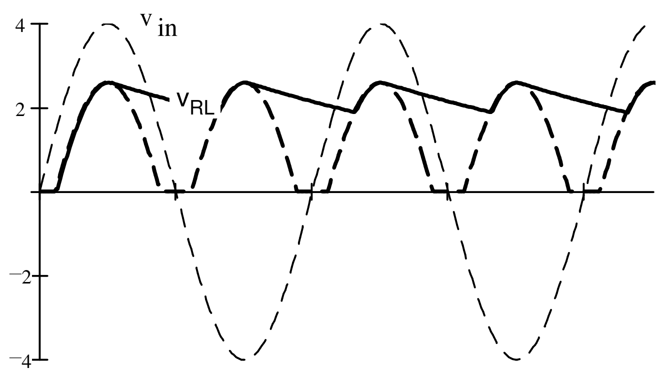
ripple is less

The center-tap transformer is also good for making $\pm$ supplies



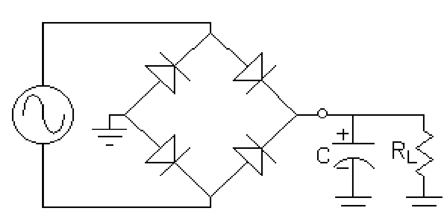
Bridge





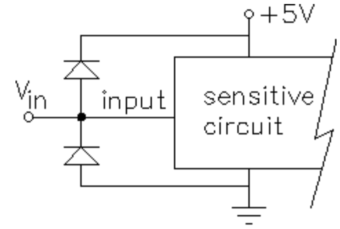
A "bridge" circuit or "bridge rectifier" can give you full-wave rectification without a center-tap transformer, but now you loose another "diode drop"

Bridge rectifiers are often drawn like this:



Other Useful Diode Circuits

Simple limiter circuits can be made with diodes.
A common input protection to protect circuit from excessive input voltages such as static electricity.



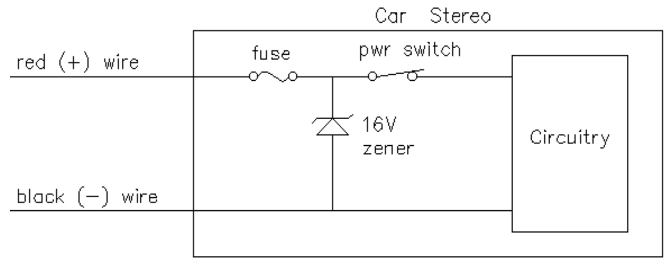
The input to the box marked "sensitive circuit" can' t get higher than the positive supply + 0.7V or lower than the negative supply - 0.7V.

Put a fuse in the V_{in} line and the diodes can make it blow, providing what' s known as "crowbar" protection.

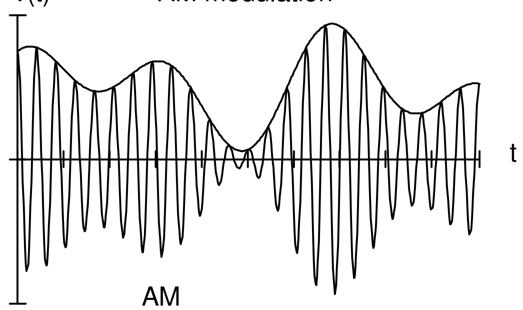
Another example of crowbar protection:

If the input voltage goes above 16 V. the fuse will blow, protecting the circuitry.

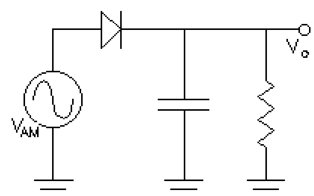
Or, If the input voltage is hooked up backwards the fuse will blow, protecting the circuitry.



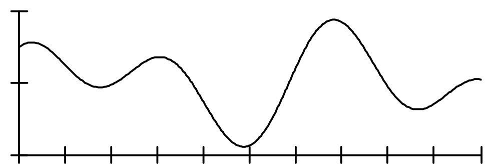
AM detector



A simple rectifier circuit



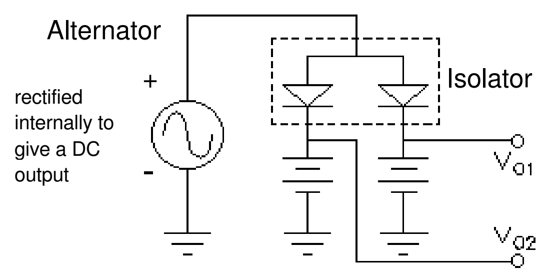
Returns the modulation signal



And a coupling capacitor can remove the DC

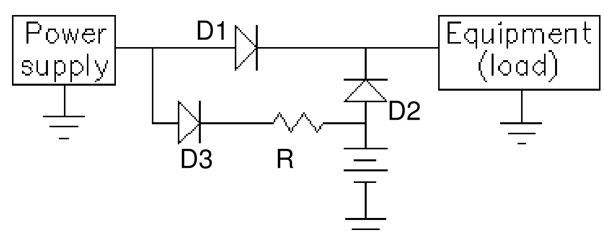
Battery Isolator

Like you might find in an RV. One alternator is used to charge two batteries. When the alternator is **not** charging, the batteries, the circuits they are hooked to should be isolated from one another. If not, then one battery might discharge through the second, especially if second is bad. Also, you wouldn' t want the accessories in the RV to drain the starting battery, or your uncle George from South Dakota might never leave your driveway.



Battery Backup Power

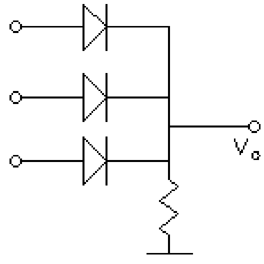
Normally the power supply powers the load through D1. However, if it fails, the load will remain powered by the battery through D2. Finally, D3 and R may be added to keep the battery charged when the power supply is working. These sorts of circuits are popular in hospitals.



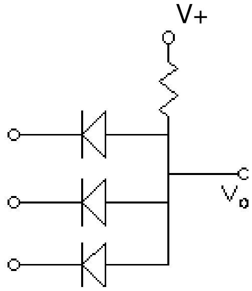
Diode Logic Circuits

Actually, both of the previous circuits are logic circuits as well.

"OR" gate

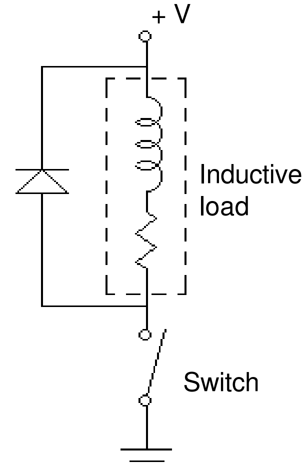


"AND" gate



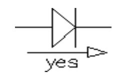
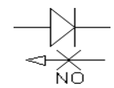
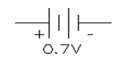
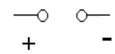
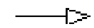
"Flyback" Diode

Every time the switch opens the inductor current continues to flow through the diode for a moment. If the diode weren' t there, then the current would arc across the switch.



ECE 2210 Diode Circuit Examples

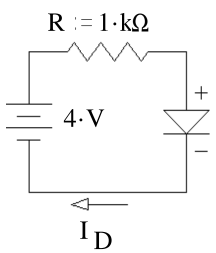
Basic diode circuit analysis

	conducting		not conducting
1 Make an educated guess about each diode' s state.		or	
2 Replace each diode with the appropriate model:			
3 Redraw and analyze circuit.			
4 Make sure that each diode is actually in the state you assumed:	 current		$V_d < 0.7V$ Check

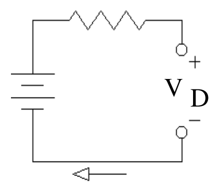
Note: 0.7V is for silicon junction diodes & will be different for other types. (2V for LED)

If any of your guesses don' t work out right, then you' ll have to start over with new guesses. In a circuit with n diodes there will be 2^n possible states, all of which may have to be tried until you find the right one. Try to guess right the first time.

Ex1



Try reverse-biased, non-conducting model

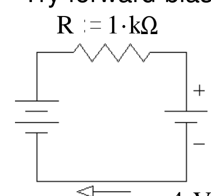


$V_D := 4 \cdot V \quad > 0.7V$

$I_D := 0$

Doesn' t work, diode must be forward biased.

Try forward-biased, conducting model

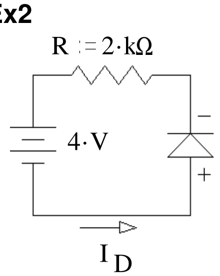


$V_D := 0.7 \cdot V$

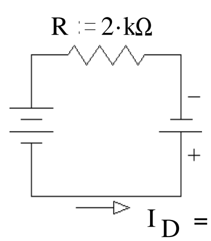
$$I_D = \frac{4 \cdot V - 0.7 \cdot V}{1 \cdot k\Omega} = 3.3 \cdot mA \quad > 0$$

The current is in the forward direction, confirming the assumption.

Ex2



Try forward-biased, conducting model

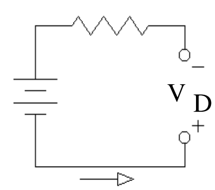


$V_D := 0.7 \cdot V$

$$I_D = \frac{-4 \cdot V - 0.7 \cdot V}{1 \cdot k\Omega} = -4.7 \cdot mA < 0$$

Doesn' t work, diode must be reverse biased.

Try reverse-biased, non-conducting model



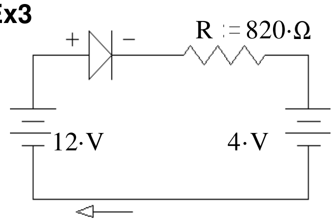
Check the diode voltage

$V_D := -4 \cdot V \quad < 0.7V$

Confirms diode is reverse biased

$I_D := 0 \cdot mA$

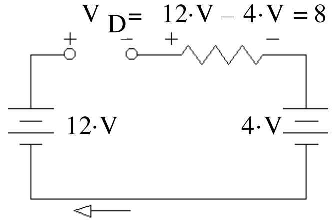
Ex3



Try reverse-biased, non-conducting model

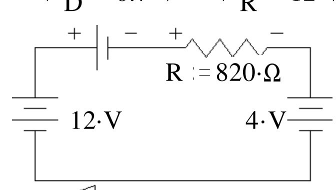
Doesn' t work, diode must be forward biased.

$V_D = 12 \cdot V - 4 \cdot V = 8 \cdot V \quad > 0.7V$



Try forward-biased, conducting model

$V_D := 0.7 \cdot V \qquad V_R := 12 \cdot V - 4 \cdot V - V_D$



$V_R = 7.3 \cdot V$

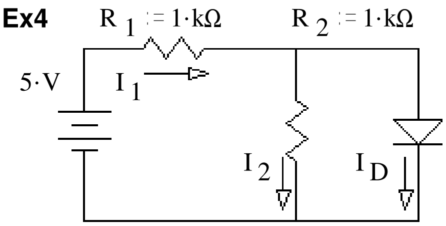
Check the diode current

$$I = \frac{V_R}{R} = 8.9 \cdot mA \quad > 0$$

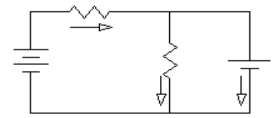
Confirms diode is forward biased

In each of these examples, my first guess was pretty stupid. I did that intentionally to show the process. I expect that you can make better guess and thus save yourself some work.

Ex4



Assume diode conducts:
Analyze



$V_D := 0.7\cdot V \;=\; V_{R2}$

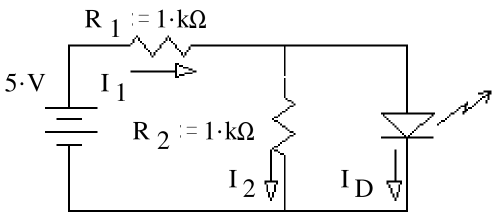
$V_{R2} := V_D \qquad I_2 := \frac{V_{R2}}{R_2} \qquad I_2 = 0.7\cdot mA$

$V_{R1} := 5\cdot V - V_D \qquad V_{R1} = 4.3\cdot V \qquad I_1 := \frac{V_{R1}}{R_1} \qquad I_1 = 4.3\cdot mA$

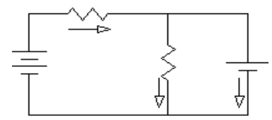
We assumed conducting (assuming a voltage), so check the current.

$I_D := I_1 - I_2 \qquad I_D = 3.6\cdot mA \quad > 0$, so assumption was correct

Ex5 Now with an LED



Assume diode conducts



$V_D := 2\cdot V \;=\; V_{R2}$

Analyze

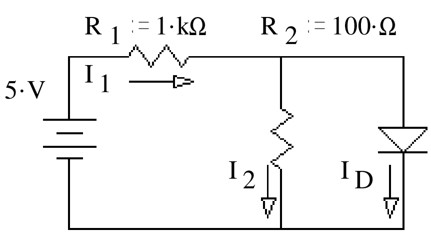
$V_{R2} := V_D \qquad I_2 := \frac{V_{R2}}{R_2} \qquad I_2 = 2\cdot mA$

$V_{R1} := 5\cdot V - V_D \qquad V_{R1} = 3\cdot V \qquad I_1 := \frac{V_{R1}}{R_1} \qquad I_1 = 3\cdot mA$

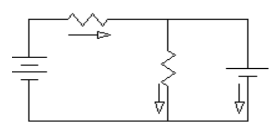
We assumed conducting (assuming a voltage), so check the current.

$I_D := I_1 - I_2 \qquad I_D = 1\cdot mA \quad > 0$, so assumption was correct, but the current is probably too small to create noticeable light

Ex6 Regular diode, but smaller R_1



Assume diode conducts



$V_D := 0.7\cdot V = \; V_{R2}$

Analyze

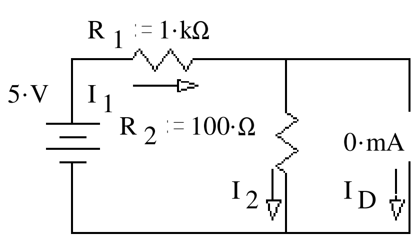
$V_{R2} := V_D \qquad I_2 := \frac{V_{R2}}{R_2} \qquad I_2 = 7\cdot mA$

$V_{R1} := 5\cdot V - V_D \qquad V_{R1} = 4.3\cdot V \qquad I_1 := \frac{V_{R1}}{R_1} \qquad I_1 = 4.3\cdot mA$

We assumed conducting (assuming a voltage), so check the current.

$I_D := I_1 - I_2 \qquad I_D = -2.7\cdot mA \quad < 0$, so assumption was **WRONG !**

Assume diode does not conduct $\quad I_D := 0\cdot mA$



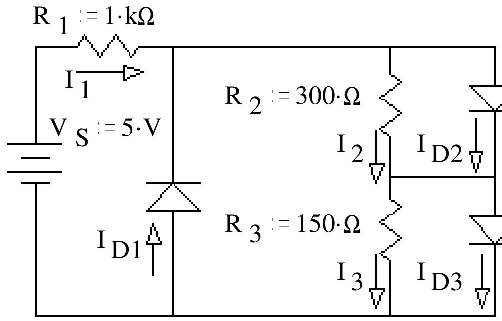
Analyze $\qquad I_1 := \frac{5\cdot V}{R_1 + R_2} \qquad I_2 := I_1$

We assumed not conducting (assuming a current), so check the voltage.

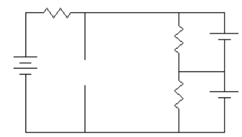
$V_{R2} := I_2\cdot R_2 \qquad V_{R2} = 0.455\cdot V \;< 0.7V$, so assumption was correct

Actually, this final check isn' t necessary, since first assumption didn' t work, so this one had to.

Ex7



You can safety say that diode D_1 doesn' t conduct without rechecking later because no supply is even trying to make current flow through that diode the right way.



$V_{D2} := 0.7\cdot V$ $\quad V_{D3} := 0.7\cdot V$

Assume both D_2 and D_3 conduct.

Analyze $\quad V_{R1} := V_S - V_{D2} - V_{D2}$

$V_{R1} = 3.6\cdot V$

$I_1 := \dfrac{V_{R1}}{R_1}$ $\qquad I_1 = 3.6\cdot mA$

$I_2 := \dfrac{V_{D2}}{R_2}$ $\qquad I_2 = 2.333\cdot mA$

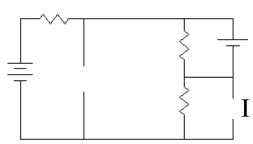
$I_3 := \dfrac{V_{D3}}{R_3}$ $\qquad I_3 = 4.667\cdot mA$

We assumed D_1 & D_2 conduct (assumed a voltage), so check currents.

$I_{D2} := I_1 - I_2$ $\qquad I_{D2} = 1.267\cdot mA$ $\quad > 0$, so assumption OK

$I_{D3} := I_1 - I_3$ $\qquad I_{D3} = -1.067\cdot mA$ $\quad < 0$, so assumption **wrong**

Assume D_2 conducts and D_3 doesn' t.



$V_{D2} := 0.7\cdot V$

$I_{D3} := 0\cdot mA$

Analyze $\quad I_2 := \dfrac{V_{D2}}{R_2}$ $\qquad I_2 = 2.333\cdot mA$

$I_1 := \dfrac{V_S - V_{D2}}{R_1 + R_3}$ $\qquad I_1 = 3.739\cdot mA$

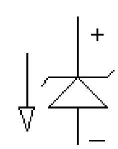
Assumed D_2 conducts, so check D_2 current. $\quad I_{D2} := I_1 - I_2$ $\qquad I_{D2} = 1.406\cdot mA$ $\quad > 0$, so assumption OK

Assumed D_3 doesn' t conduct, so check D_3 voltage. $\quad V_{R3} := I_1\cdot R_3$ $\qquad V_{R3} = 0.561\cdot V$ $\quad < 0.7V$, so OK

Once you find one case that works, you don' t have to try any others.

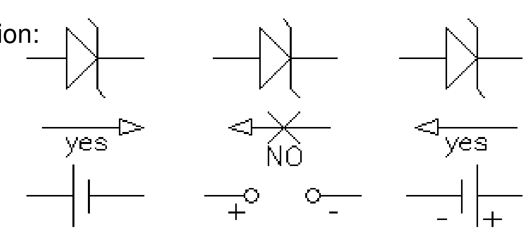
Zener Diodes

Zener diodes are special diodes designed to operate in the reverse breakdown region. Since the reverse breakdown voltage across the diode is very constant for a large range of current, it can be used as a voltage reference or regulator. Diodes are not harmed by operating in this region as long as their power rating isn' t exceeded. In the forward direction zeners work the same as regular diodes.



V_Z = zener voltage

Now there are three possible regions of operation:



$V_D = 0.7V$ $\qquad 0.7V > V_D > V_Z$ $\qquad V_Z$

Same basic diode circuit analysis

1 Make an educated guess about each diode' s state.

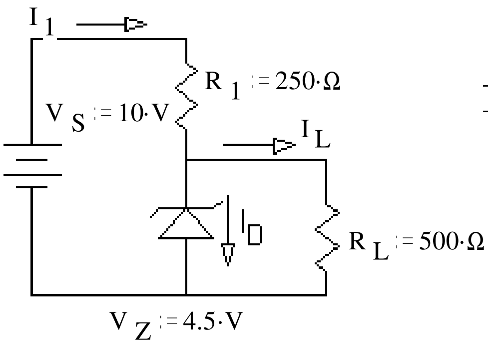
2 Replace each diode with the appropriate model:

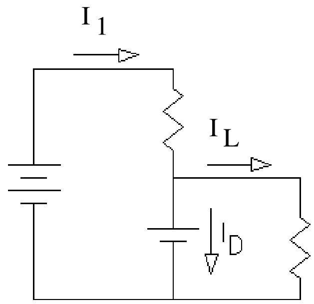
3 Redraw and analyze circuit.

4 Make sure that each diode is actually in the state you assumed:

Zener Diode Circuit Examples

Ex1 Typical shunt regulator circuit:





$V_Z := 4.5\cdot V$ $\quad R_L := 500\cdot\Omega$

Assume conducting in breakdown region

$V_D := V_Z$

$I_L := \frac{V_Z}{R_L}$ $\quad I_L = 9\cdot mA$

$I_1 := \frac{V_S - V_Z}{R_1}$ $\quad I_1 = 22\cdot mA$

Assumed a conducting region, so check the current to see if the current flows in the direction shown.

$I_D := I_1 - I_L$ $\quad I_D = 13\cdot mA$ > 0, so assumption OK

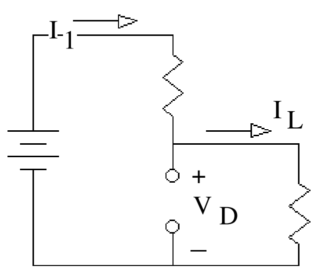
Ex2 What if R_L is smaller? $\quad R_L := 150\cdot\Omega$

Assume conducting in breakdown region $\quad V_D := V_Z$ $\quad I_L := \frac{V_Z}{R_L}$ $\quad I_L = 30\cdot mA$

$I_1 := \frac{V_S - V_Z}{R_1}$ $\quad I_1 = 22\cdot mA$ $\quad I_D := I_1 - I_L$ $\quad I_D = -8\cdot mA$ < 0, so assumption is **WRONG !**

Circuit "falls out of regulation"

Assume not conducting



$I_L = I_1 := \frac{V_S}{R_1 + R_L}$ $\quad I_1 = 25\cdot mA$

Assumed a non-conducting region, so check the voltage to see if it' s in the right range.

$V_D := \frac{R_L}{R_1 + R_L}\cdot V_S$ $\quad V_D = 3.75\cdot V$ $< V_Z = 4.5\cdot V$

so this assumption is OK

Ex3 What if V_S is smaller instead of R_L? $\quad V_S := 6\cdot V$ $\quad R_L := 500\cdot\Omega$

Assume conducting in breakdown region $\quad V_D := V_Z$ $\quad I_L := \frac{V_Z}{R_L}$ $\quad I_L = 9\cdot mA$

$I_1 := \frac{V_S - V_Z}{R_1}$ $\quad I_1 = 6\cdot mA$ $\quad I_D := I_1 - I_L$ $\quad I_D = -3\cdot mA$ < 0, so assumption is **WRONG !**

Circuit "falls out of regulation"

Assume not conducting $\quad I_L = I_1 := \frac{V_S}{R_1 + R_L}$ $\quad I_1 = 8\cdot mA$

Assumed a non-conducting region, so check the voltage to see if it' s in the right range. $\quad V_D := \frac{R_L}{R_1 + R_L}\cdot V_S$ $\quad V_D = 4\cdot V$ $< V_Z = 4.5\cdot V$

so this assumption is OK

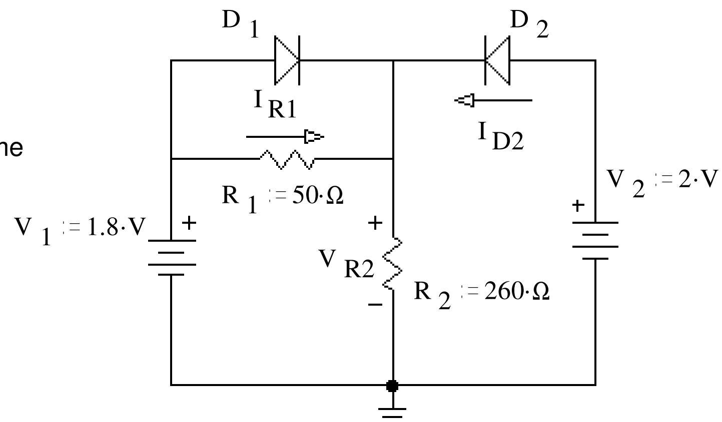
Exam-type Diode Circuit Examples

On an exam, I usually tell you what assumptions to make about the diodes, then you can show that you know how to analyze the circuit and test those assumptions. Since everyone starts with the same assumptions, everyone should do the same work.

In the circuit shown, use the constant-voltage-drop model for the silicon diode.

a) Assume that diode D_1 does NOT conduct.
Assume that diode D_2 does conduct.

Find V_{R2}, V_{R1}, I_{R1}, & I_{D2}, based on these assumptions. Stick with these assumptions even if your answers come out absurd. Hint: think in nodal voltages.



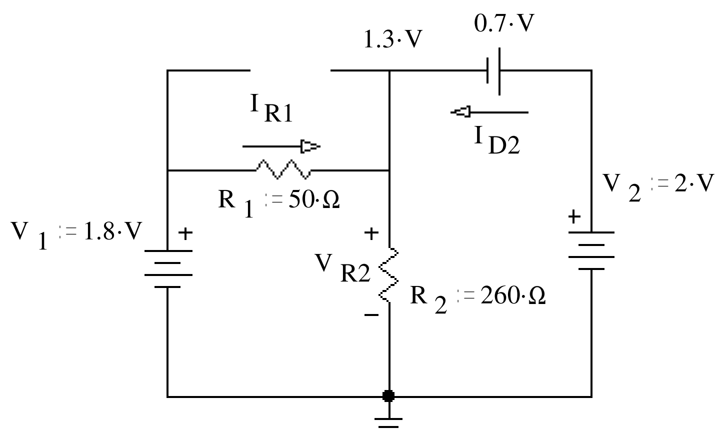
$V_{R2} =$ ________

$V_{R1} =$ ________

$I_{R1} =$ ________

$I_{D2} =$ ________

Solution to a)



$V_{R2} := V_2 - 0.7 \cdot V$ $\quad V_{R2} = 1.3 \cdot V$

$V_{R1} := V_1 - V_{R2}$ $\quad V_{R1} = 0.5 \cdot V$

$I_{R1} := \frac{V_{R1}}{R_1}$ $\quad I_{R1} = 10 \cdot mA$

$I_{R2} := \frac{V_{R2}}{R_2}$ $\quad I_{R2} = 5 \cdot mA$

$I_{D2} := I_{R2} - I_{R1}$ $\quad I_{D2} = -5 \cdot mA$

b) Based on your numbers above, does it look like the assumption about D_1 was correct? yes no (circle one)
How do you know? (Specifically show a value which is or is not within a correct range.)

yes $\quad V_{D1} = V_{R1} = 0.5 \cdot V < 0.7V$

c) Based on your numbers above, does it look like the assumption about D_2 was correct? yes no (circle one)
How do you know?

no $\quad I_{D2} = -5 \cdot mA < 0$

d) Based on your answers to b) and c), which (if any) of the following was not correctly calculated in part a.

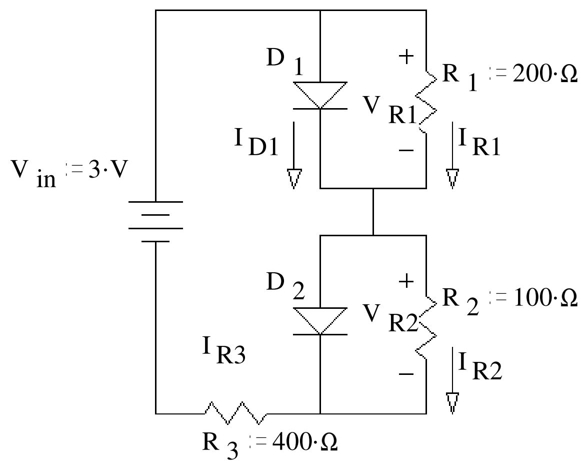
V_{R2} $\quad V_{R1}$ $\quad I_{R1}$ $\quad I_{D2}$

(circle any number of answers)

Circle all in this case

Assume that diode D_1 is conducting and that diode D_2 is not conducting.

a) Find V_{R1}, I_{R1}, I_{R3}, I_{D1}, V_{R2} based on these assumptions.
Do not recalculate if you find the assumptions are wrong.



$V_{R1} =$ ________

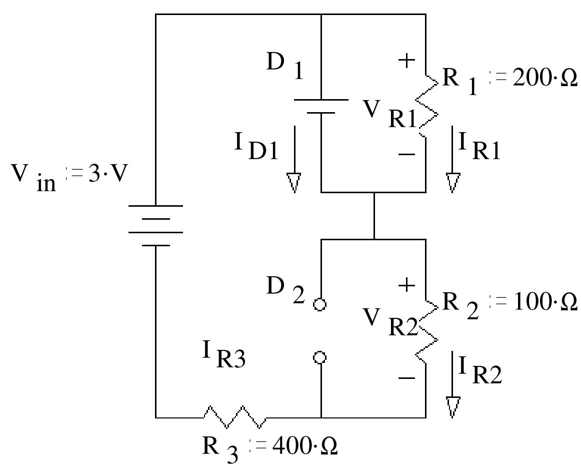
$I_{R1} =$ ________

$I_{R3} =$ ________

$I_{D1} =$ ________

$V_{R2} =$ ________

Solution:



$V_{R1} := 0.7 \cdot V$

$I_{R1} := \frac{V_{R1}}{R_1}$ $\qquad I_{R1} = 3.5 \cdot mA$

$I_{R3} := \frac{V_{in} - 0.7 \cdot V}{R_2 + R_3}$ $\qquad I_{R3} = 4.6 \cdot mA$

$I_{D1} := I_{R3} - I_{R1}$ $\qquad I_{D1} = 1.1 \cdot mA$

$I_{R2} := I_{R3}$

$V_{R2} := I_{R2} \cdot R_2$ $\qquad V_{R2} = 0.46 \cdot V$

(circle one)

b) Was the assumption about D_1 correct? yes no

How do you know? (Specifically show a value which is or is not within a correct range.)

yes $I_{R2} = 4.6 \cdot mA > 0$

c) Was the assumption about D_2 correct? yes no

(circle one)

How do you know?

yes $V_{D2} = V_{R2} = 0.46 \cdot V < 0.7V$

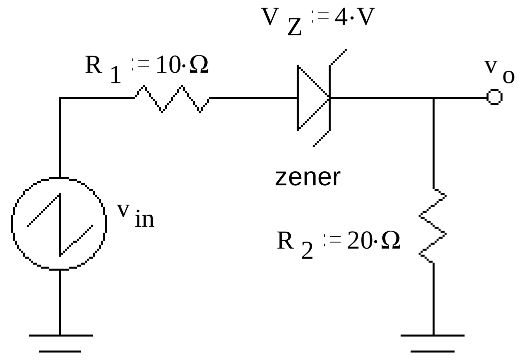
d) Based on your answers to b) and c), which (if any) of the following was not correctly calculated in part a.

V_{R1} $\quad I_{R1}$ $\quad I_{R3}$ $\quad I_{D2}$ $\quad V_{R2}$

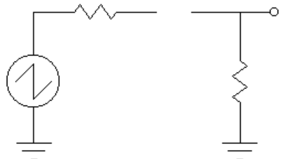
(circle any number of answers)

Circle none in this case

A voltage waveform (dotted line) is applied to the circuit shown.
Accurately draw the output waveform (v_o) you expect to see.
Label important times **and** voltage levels.



If diode doesn't conduct:

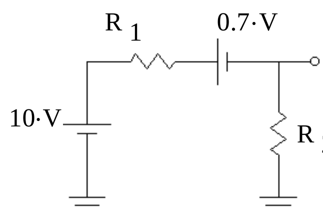


$v_o := 0$

Positive half

Diode conducts at: $0.7 \cdot V$ input at time: $\frac{0.7 \cdot V}{10 \cdot V} \cdot 10 \cdot ms = 0.7 \cdot ms$

Maximum:

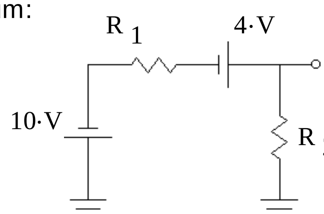


$v_o := (10 \cdot V - 0.7 \cdot V) \cdot \frac{R_2}{R_1 + R_2}$ $v_o = 6.2 \cdot V$

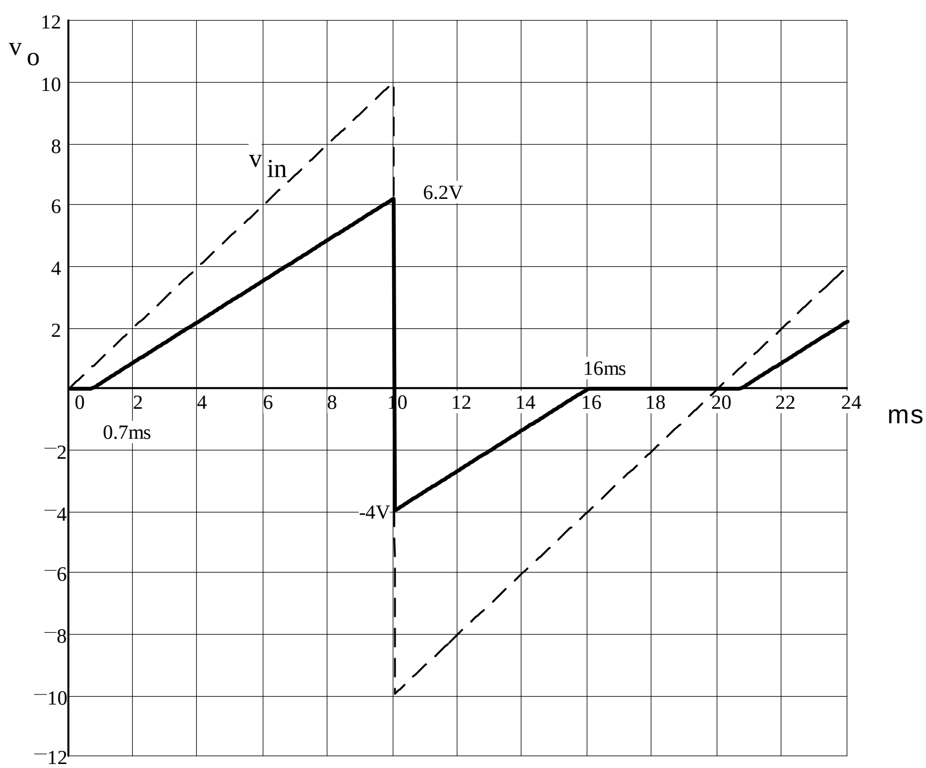
Negative half

Diode conducts at: $-4 \cdot V$ input at time: $20 \cdot ms - \frac{4 \cdot V}{10 \cdot V} \cdot 10 \cdot ms = 16 \cdot ms$

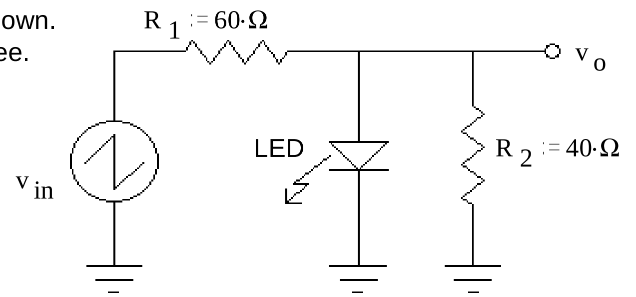
Maximum:



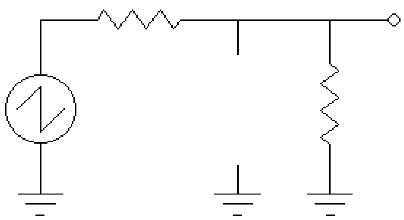
$v_o := -(10 \cdot V - 4 \cdot V) \cdot \frac{R_2}{R_1 + R_2}$ $v_o = -4 \cdot V$



A voltage waveform (dotted line) is applied to the circuit shown. <u>Accurately</u> draw the output waveform (v_o) you expect to see. Label important times **<u>and</u>** voltage levels.



If diode doesn't conduct:

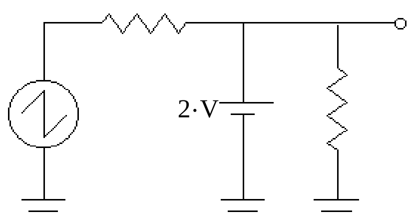


$$v_o = \frac{R_2}{R_1 + R_2}\cdot v_{in}$$

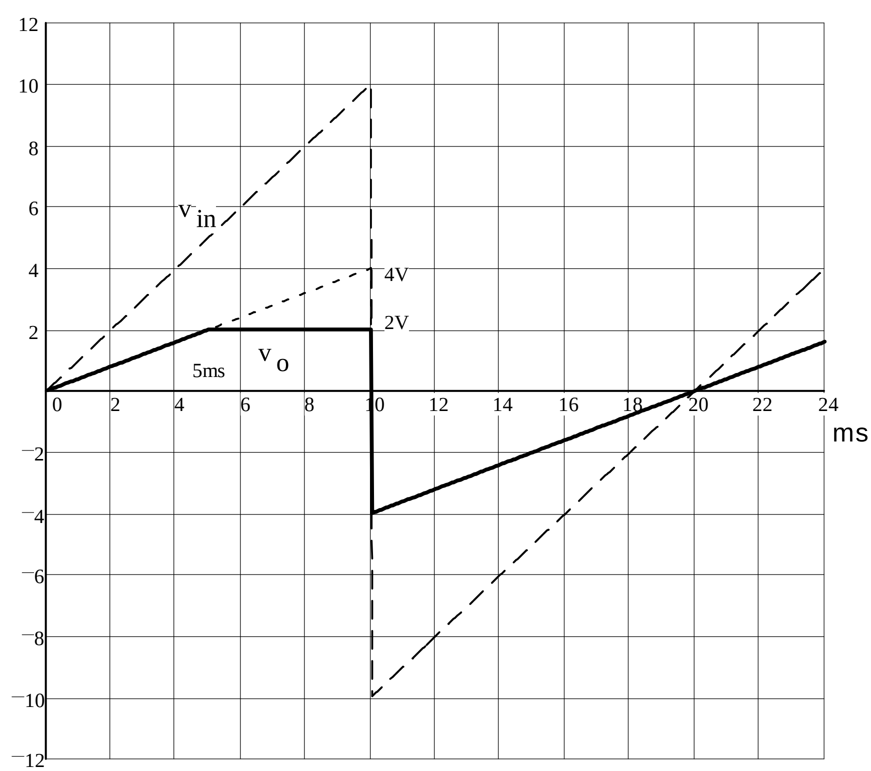
$$\frac{R_2}{R_1 + R_2}\cdot 10\cdot V = 4\cdot V$$

When: $v_{in} := \frac{R_1 + R_2}{R_2}\cdot 2\cdot V$ $v_{in} = 5\cdot V$ at: $5\cdot ms$ Diode begins to conduct

When diode conducts:



$v_o = 2\cdot V$



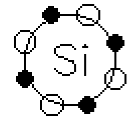
Diode Physics (The simple version)   EE 2210

A.Stolp 3/22/01, rev, 2/25/16

FYI Only, You don't need to know this

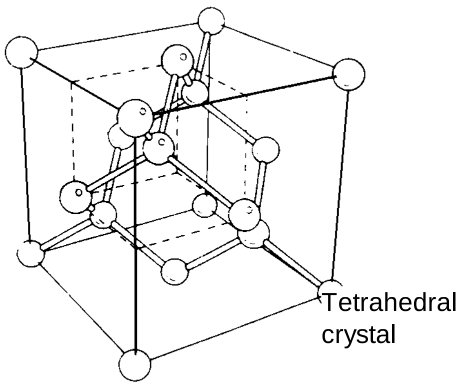
Silicon atoms

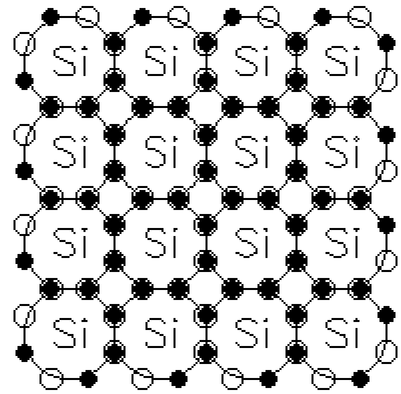
Silicon atoms each have 4 valence electrons (electrons in their outermost shell). That leaves 4 spaces in the outer shell of 8. This makes silicon a very reactive chemical, like carbon, which has the same valence configuration.



Silicon crystals

Each atom covalently bonds with four neighboring atoms to form a tetrahedral crystal, which we'll represent in 2D.

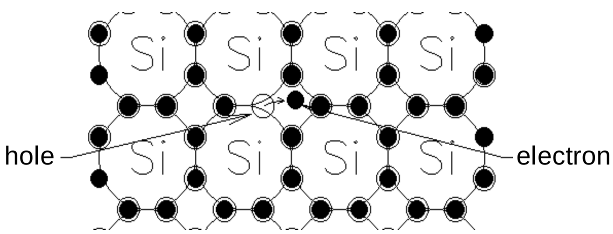


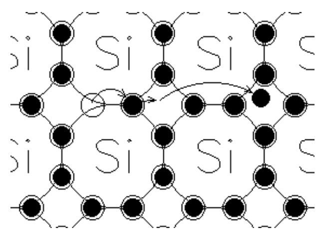


2-dimensional representation

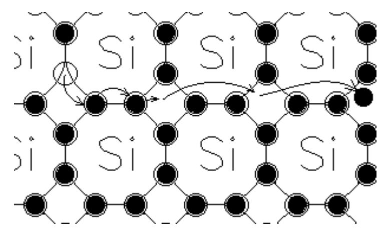
In the pure, "intrinsic" crystal, practically all the electrons are used in bonds and all the spaces are filled, which leaves almost no electrons free to move and thus no way to make current flow.

By the effects of heat, light and/or large electric fields, a few electrons do break free of the bonds and become "free" carriers. That is, they're free to move about crystal and "carry" an electrical current.





carrier movement



more carrier movement

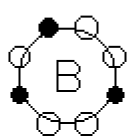
Interestingly, the space that was vacated by the electron also acts like a carrier. This pseudo-carrier is called a "hole" and it acts like a positively charged carrier.

Unless there's a lot of heat or light, the intrinsic silicon is still a very bad conductor.
Silicon is considered a semiconductor.

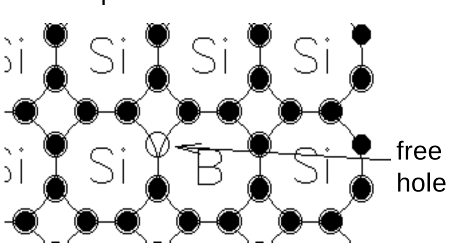
Doping

p-type

Some atoms, like boron and aluminum naturally have 3 valence electrons in their outer shells.

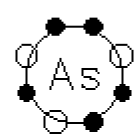


If you replace some of the silicon atoms in a crystal with boron there won't be quite enough electrons to fill the crystalline bond structure and unfilled spaces will act just like free holes. This "doped" silicon crystal is now called an p-type semiconductor. The p refers to the "extra" "positive" carriers.

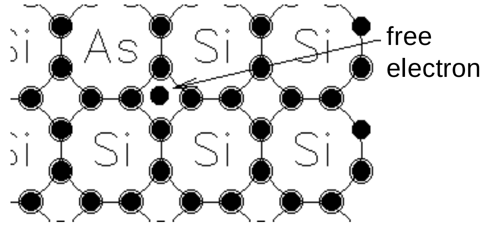


n-type

Some atoms, like arsenic and phosphorus naturally have 5 valence electrons in their outer shells.

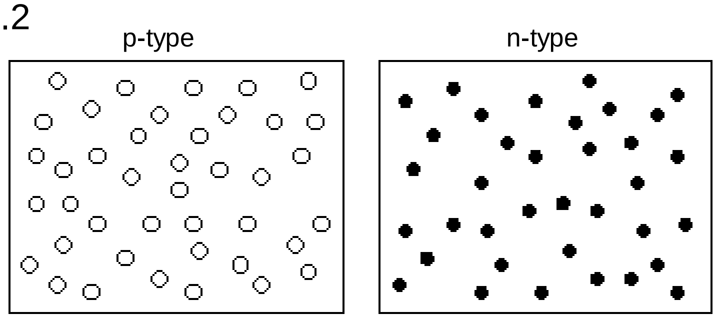


If you replace some of the silicon atoms in a crystal with arsenic the 5th electron doesn't fit into the crystalline bond structure and is therefore free to roam about and be a carrier. This "doped" silicon crystal is now called an n-type semiconductor. The n refers to the "extra" negative carriers.



Diode Physics (The simple version) p.2

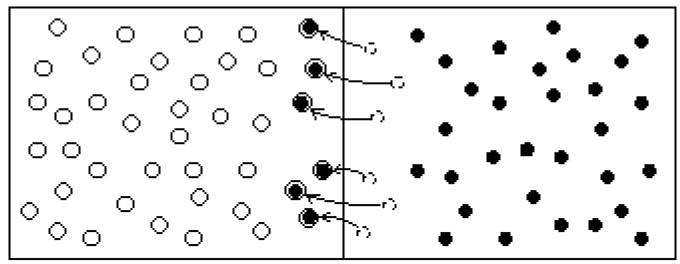
It turns out that the free carriers are the most important things in the semiconductor crystals, so we can simplify the drawings to show only these free carriers.



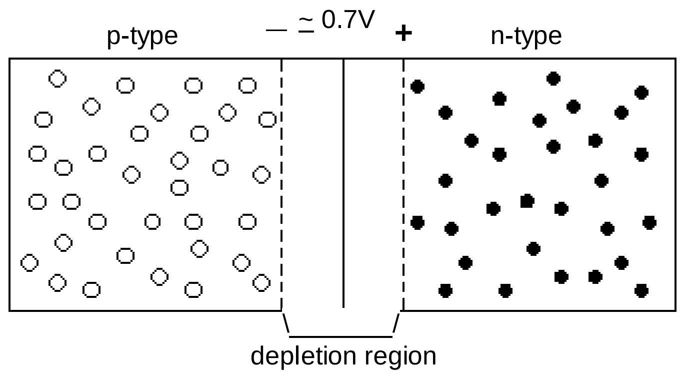
circles represent free holes   dots represent free electrons

PN Junction

When a p-type semiconductor is created next to an n-type, some of the free electrons from the n side will cross over and fill some of the free holes on the p side. This makes the p side negatively charged and leaves the n side positively charged. When the voltage across the junction reaches about 0.7 V the electrons find it too difficult to move against the charge and the process stops.



A region near the junction is now depleted of carriers and (surprise) is called the depletion region.

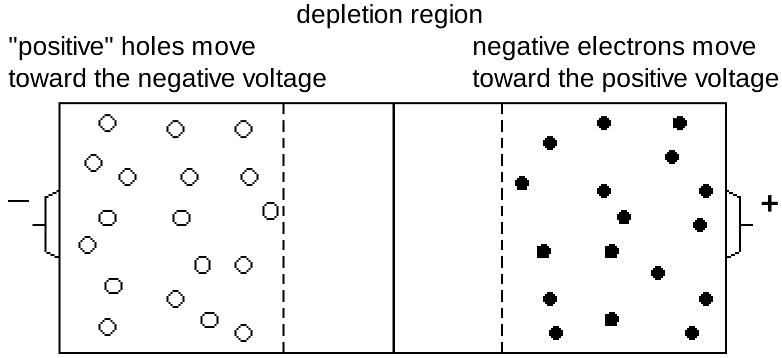


Reverse bias

This pn junction is now a diode. If you place an external voltage across the diode in the reverse bias direction, the depletion region gets bigger and no current flows.

This reverse bias region can be used as a heat or light sensor since the only current flow should be due to a few carriers produced by these effects.

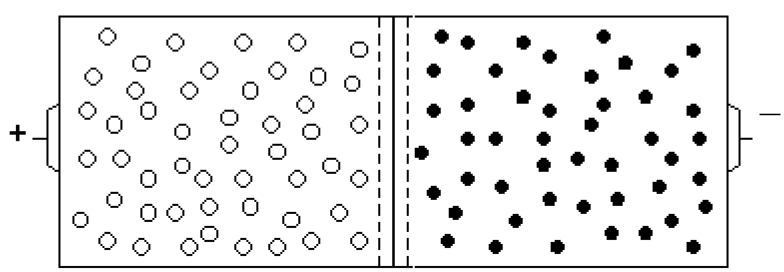
The reverse biased diode can also be used as a voltage variable capacitor since it is essentially an insulator (the depletion region) sandwiched between two conducting regions.



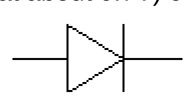
With reverse bias the depletion region gets bigger

Forward bias

If you place an external voltage across the diode in the forward bias direction, the depletion region shrinks until your external voltage reaches about 0.7V. After that the diode conducts freely..



With forward bias the depletion region gets smaller and eventually (at about 0.7V) conducts freely.



Diode

Diode Physics (The simple version) p.2

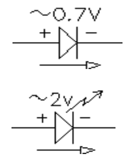
Folder:____ Name:______________________ ECE 2210 hw # DO1 Due: Mon, 4/4/22

Fill in the blanks in the following circuits. For some of the simple calculations, you may simply write down the answer without showing work.

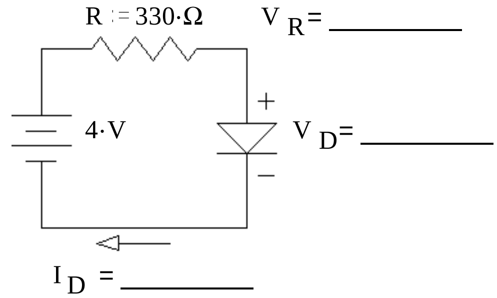
A.Stolp rev b

Assume the diodes are silicon with a 0.7V forward voltage drop:

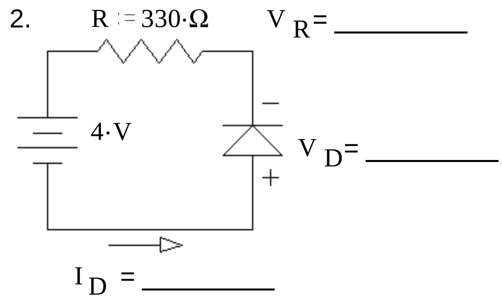
Assume the LEDs have a 2V forward voltage drop:



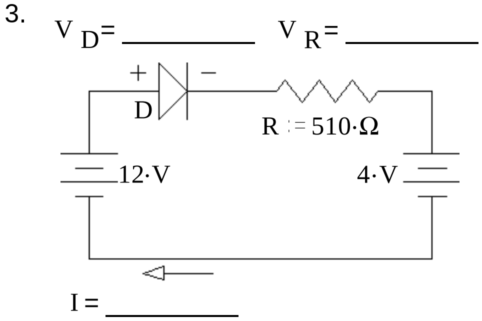
1.



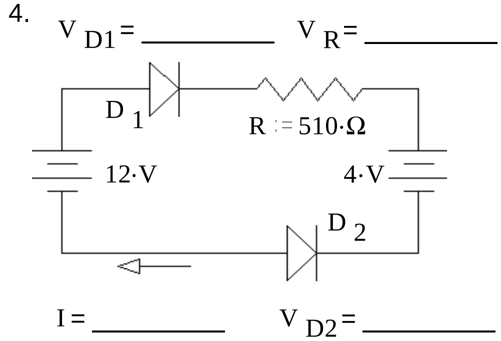
2.



3.

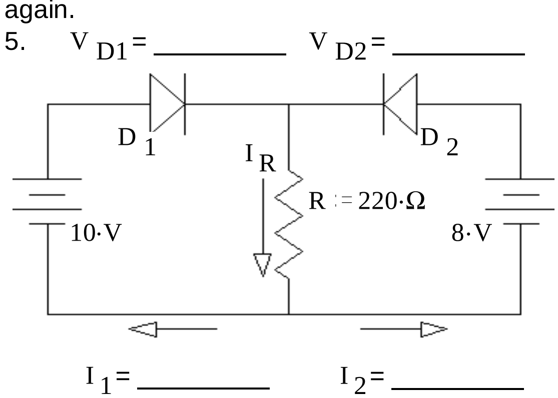


4.



Note: In problems 5 and 6 you'll have to make some assumptions about which diode(s) is/are conducting. Work the problem with those assumptions and see if you arrive at impossible answers. If so, change your assumptions and try again.

5.

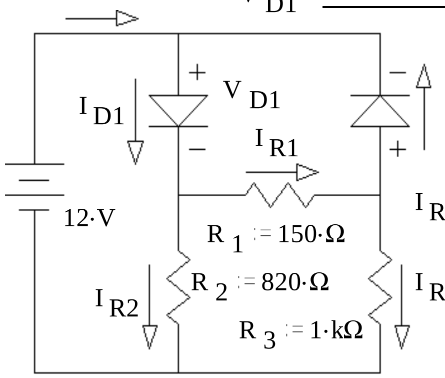


There are four possible assumptions.

1. Neither diode conducts.
2. Only D_1 conducts.
3. Only D_2 conducts.
4. Both diodes conduct.

NOTE: You don't have to try all four possibilities. As soon as you find one that works, that's the answer. So make your best guess first.

6. I_T = __________ V_{D1} = __________



V_{D2} = __________

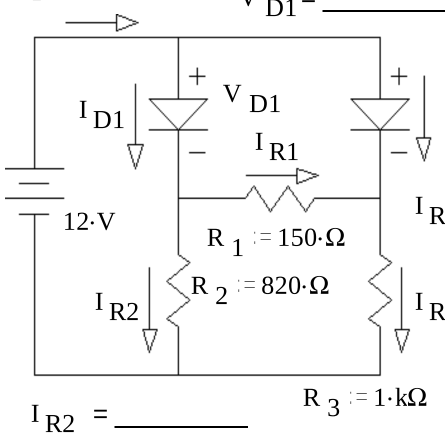
I_{D2} = __________

I_{R1} = __________

I_{R3} = __________

I_{R2} = __________

7. I_T = __________ V_{D1} = __________



V_{D2} = __________

I_{D2} = __________

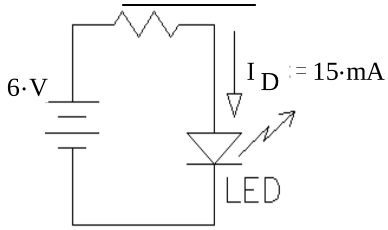
I_{R1} = __________

I_{R3} = __________

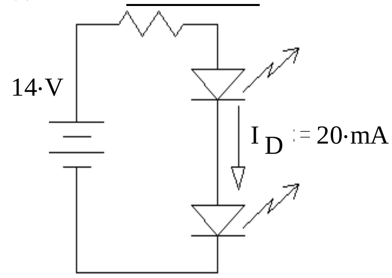
I_{R2} = __________

8. V_R = __________

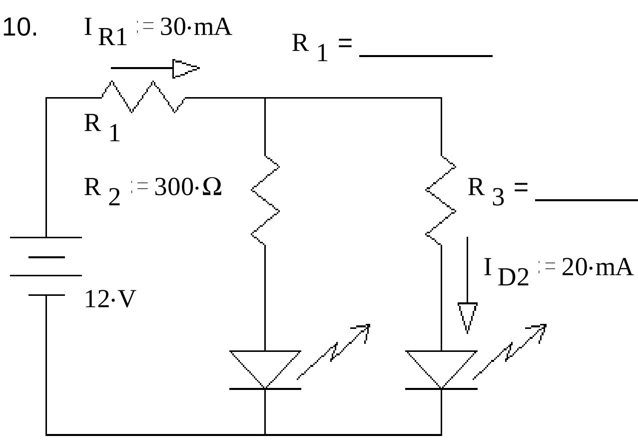
R = __________



9. R = __________



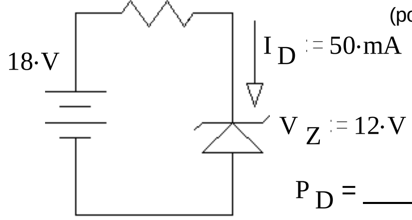
10. $I_{R1} := 30\cdot mA$ R_1 = __________



R_3 = __________

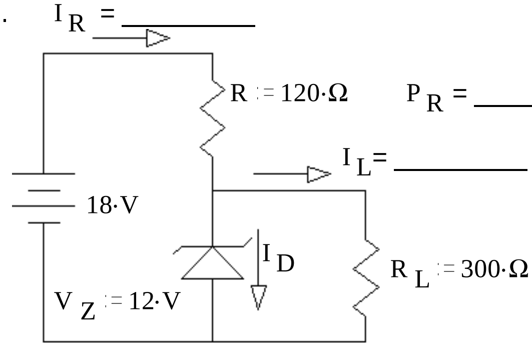
11. V_R = ________

R = ________ P_R = ________ (power)



P_D = ________

12. I_R = ________

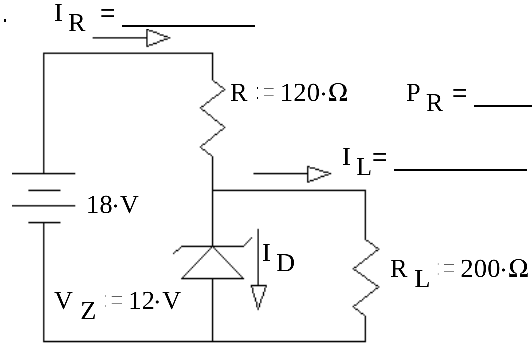


P_R = ________

I_L = ________

I_D = ________ P_D = ________

13. I_R = ________



P_R = ________

I_L = ________

I_D = ________ P_D = ________

Warning: If I_D turns out negative, it is actually 0 and you must recalculate everything else.

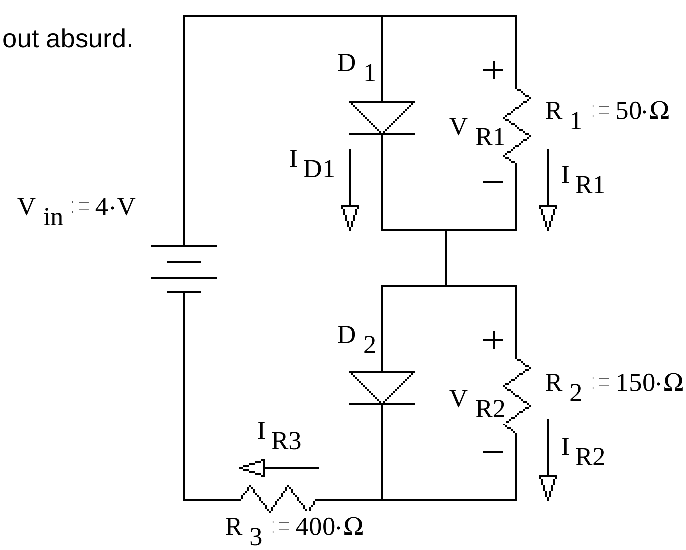
You will need more paper for the next two problems, add a sheet or two.

14. Assume that diode D_1 does conduct. Assume that diode D_2 does NOT conduct.

a) Find V_{R1}, I_{R1}, I_{R3}, I_{D1}, V_{R2} based on these assumptions.
Stick with these assumptions even if your answers come out absurd.

V_{R1} = ? I_{R1} = ? I_{R3} = ? I_{D1} = ?

V_{R2} = ?



b) Was the assumption about D_1 correct? yes or no

How do you know? (Specifically show a value which is or is not within a correct range.)

c) Was the assumption about D_2 correct? yes or no

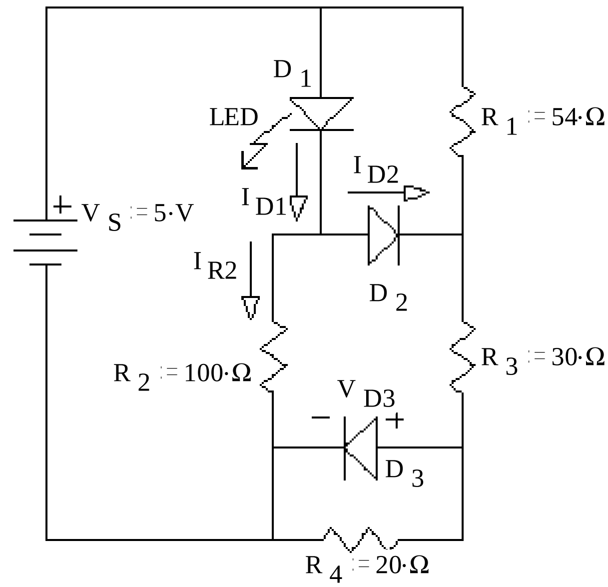
How do you know?

15. Assume that diodes D_1 and D_2 **DO** conduct.

Assume that diode D_3 does **NOT** conduct.

a) Find I_{R2}, I_{D2}, I_{D1}, & V_{D3} based on these assumptions. Stick with these assumptions even if your answers come out absurd.

I_{R2} = ? I_{D2} = ? I_{D1} = ? V_{D3} = ?



b) Based on the numbers above, was the assumption about D_1 correct? yes no
How do you know? (Show a value & range.)

c) Was the assumption about D_2 correct? yes no
How do you know? (Show a value & range.)

d) Was the assumption about D_3 correct? yes no
How do you know? (Show a value & range.)

e) Based on your answers to parts b), c) & e):

i) The ***real*** $I_{R2} < I_{R2}$ calculated in part a.

ii) The ***real*** $I_{R2} = I_{R2}$ calculated in part a.

iii) The ***real*** $I_{R2} > I_{R2}$ calculated in part a.

You do not need to justify your answer.

<u>Answers</u>

1 $V_D := 0.7\cdot V$ $V_R := 3.3\cdot V$ $I_D := 10\cdot mA$

2. $I_D := 0\cdot mA$ $V_D := -4\cdot V$ $V_R := 0\cdot V$

3. $V_D := 0.7\cdot V$ $V_R := 7.3\cdot V$ $I := 14.3\cdot mA$

4. $I := 0\cdot mA$ $V_{D2} := -8\cdot V$ $V_{D1} := 0\cdot V$ $V_R := 0\cdot V$

5. $V_{D1} := 0.7\cdot V$ $V_{D2} := -1.3\cdot V$ $I_1 := 42.3\cdot mA$ $I_2 := 0\cdot mA$

6. $I_{D2} := 0\cdot mA$ $V_{D1} := 0.7\cdot V$ $I_{R2} := 13.8\cdot mA$ $I_{R1} = I_{R3} := 9.83\cdot mA$ $V_{D2} := -2.17\cdot V$ $I_{D1} = I_T := 23.6\cdot mA$

7. $V_{D1} := 0.7\cdot V$ $V_{D2} := 0.7\cdot V$ $I_{R1} := 0\cdot mA$ $I_{R2} := 13.8\cdot mA = I_{D1}$ $I_{R3} := 11.3\cdot mA = I_{D2}$ $I_T := 25.1\cdot mA$

8. $V_R := 4\cdot V$ $R := 267\cdot\Omega$

9. $R := 500\cdot\Omega$

10. $R_1 := 233\cdot\Omega$ $R_3 := 150\cdot\Omega$

11. $V_R := 6\cdot V$ $I_D := 50\cdot mA$ $R := 120\cdot\Omega$ $P_R := 0.3\cdot W$ $P_D := 0.6\cdot W$

12. $I_L := 40\cdot mA$ $I_R := 50\cdot mA$ $I_D := 10\cdot mA$ $P_R := 0.3\cdot W$ $P_D := 0.12\cdot W$

13. $I_D := 0\cdot mA$ $I_L = I_R := 56.3\cdot mA$ $V_L := 11.3\cdot V$ $P_R := 0.38\cdot W$ $P_D := 0\cdot W$

14. a) $V_{R1} := 0.7\cdot V$ $I_{R1} := 14\cdot mA$ $I_{R3} := 6\cdot mA$ $I_{D1} := -8\cdot mA$ $V_{R2} := 0.9\cdot V$ b) no $I_{D1} = -8\cdot mA < 0$

c) no $V_{D2} = V_{R2} = 0.9\cdot V > 0.7V$

15. a) $I_{R2} := 30\cdot mA$ $I_{D2} := -4\cdot mA$ $I_{D1} := 26\cdot mA$ $V_{D3} := 0.92\cdot V$

_/ b) yes $I_{D1} := 26\cdot mA > 0$

c) no $I_{D2} := -4\cdot mA < 0$

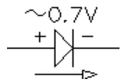
d) no $V_{D3} := 0.92\cdot V > 0.7V$

e) ii)

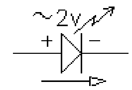
Folder:____ Name:________________________ ECE 2210 hw # DO2 Due: Fri, 4/8/22

A.Stolp
rev a

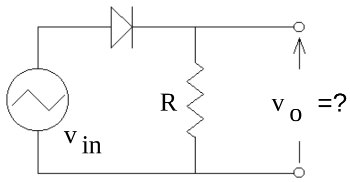
Assume the diodes are silicon with a 0.7V forward voltage drop:

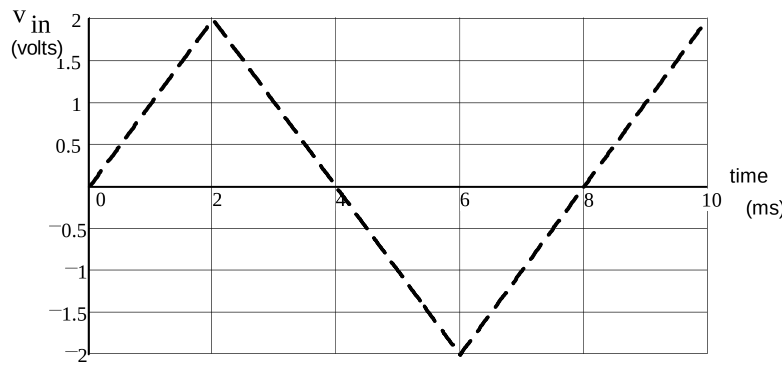


Assume the LEDs have a 2V forward voltage drop:

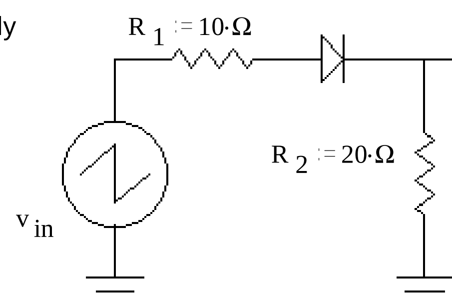


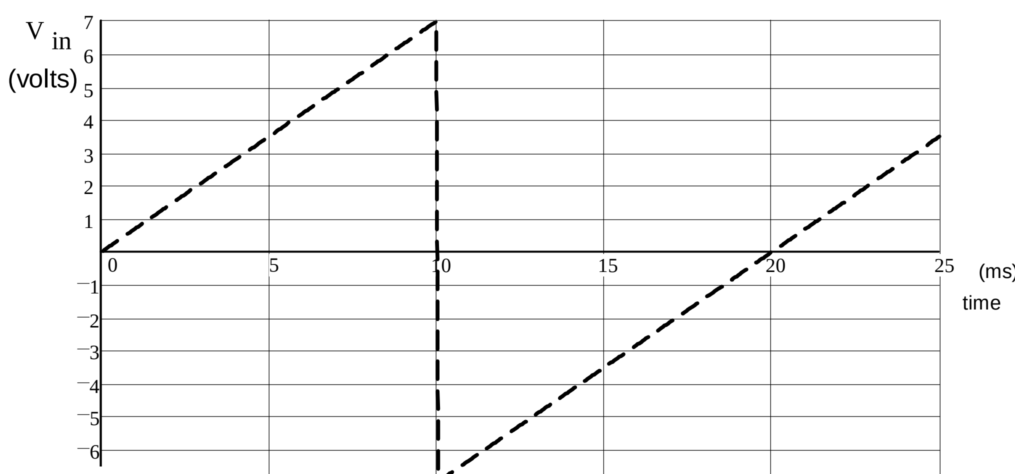
1. The input voltage to the circuit below is shown at right (dotted line). Show the output voltage across the resistor. Make it accurate and label the important voltages **and** times. You can draw your answer right on my drawing, that's why the input is shown as a dotted line.





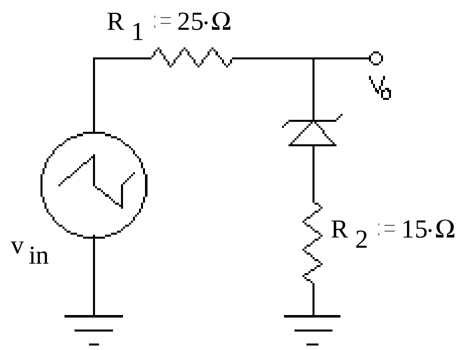
2. The voltage waveform shown (dotted line) is applied to the circuit. Accurately draw the output voltage you expect to see across the 20 Ω resistor.
Label the important voltages **and** times.

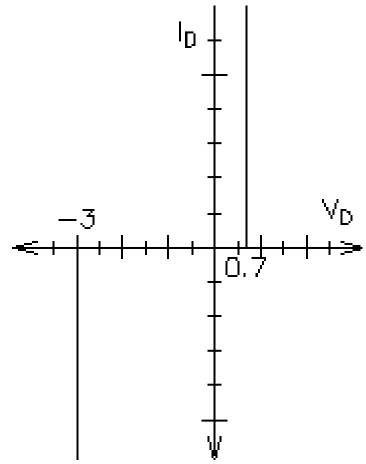


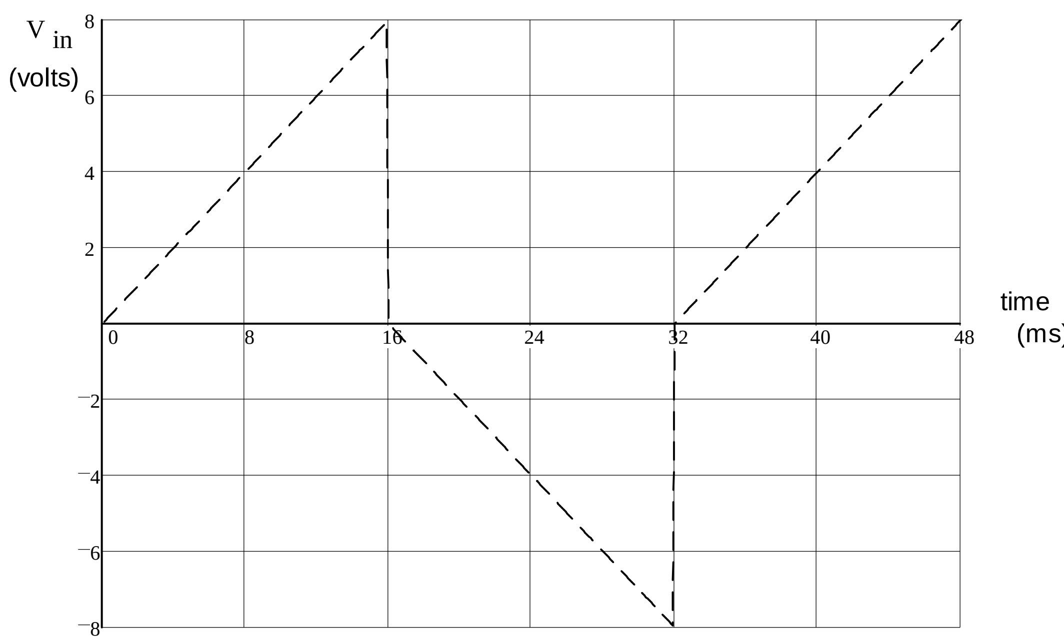


ECE 2210 homework # DO2, p2

3. The voltage waveform shown below is applied to the circuit shown. Accurately draw the output voltage (v_o) you expect to see. The characteristic curve for the 3-V silicon zener diode is also shown.
Label important times **and** voltage levels.

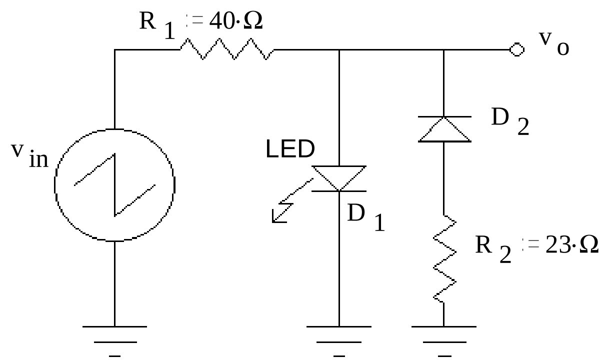


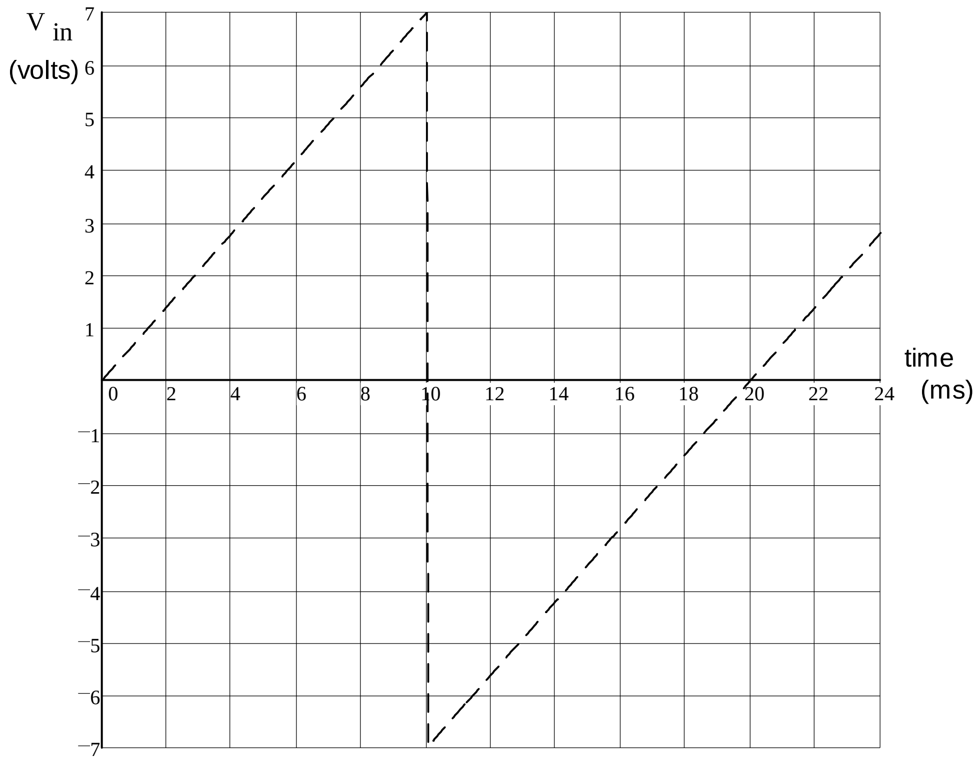




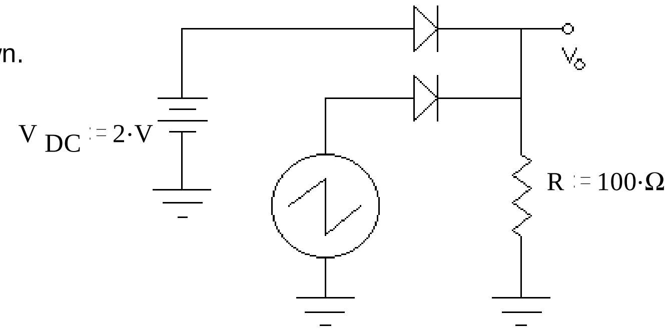
ECE 2210 homework # DO2, p2

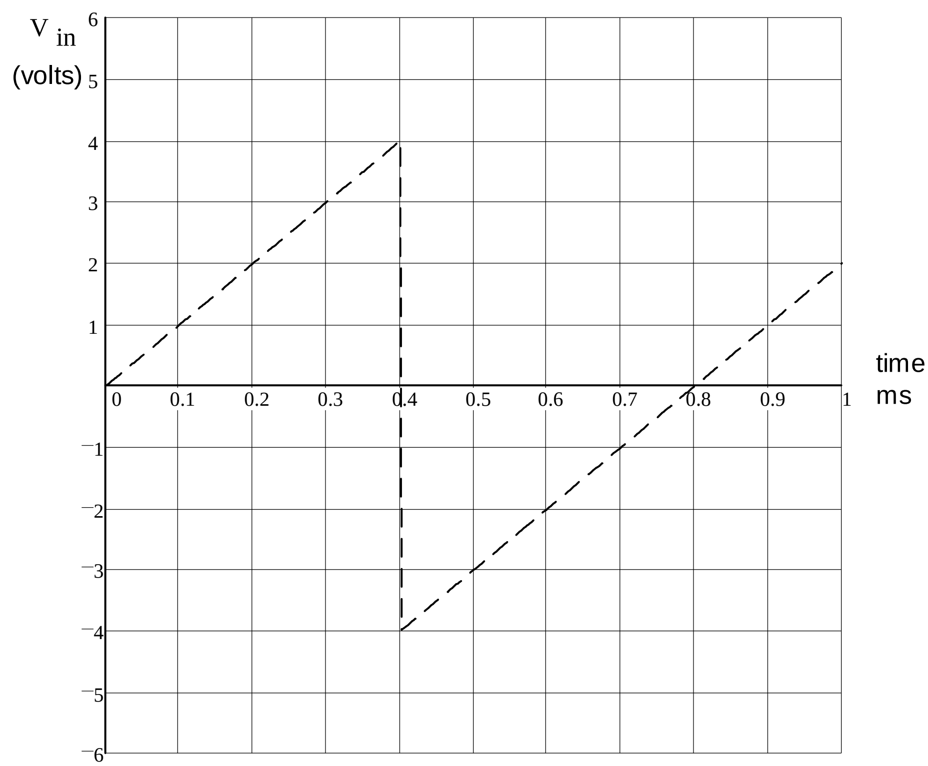
4. A voltage waveform (dotted line) is applied to the circuits shown. Accurately draw the output waveform (v_o) you expect to see. Label important times **and** voltage levels.





5. A voltage waveform (dotted line) is applied to the circuits shown.
   Accurately draw the output waveform (v_o) you expect to see.
   Label important times **and** voltage levels.





Answers

1 Straight lines between the following points: (0ms,0V), (0.7ms,0V), (2ms,1.3V), (3.3ms,0V), (8.7ms,0V), then ramps up as between 0.7ms & 2ms.

2. Straight lines between the following points: (0ms,0V), (1ms,0V), (10ms,4.2V), (10ms,0V), (21ms,0V), then ramps up as between 0.7ms & 10ms.

3. Straight lines between the following points: (0ms,0V), (6ms,3V), (16ms,4.875V), (16ms,0V), (17.4ms,-0.7V), (32ms,-3.438V), (32ms,0V), (38ms,3V), then ramps up as between 6ms & 16ms.

4. Straight lines between the following points: (0ms, 0), (2.86ms, 2V), (10ms, 2V), (10ms, -3V), (19ms, -0.7V), (22.86ms, 2V), flat at 2

5. Straight lines between the following points: (0ms,1.3V), (0.2ms,1.3V), (0.4ms,3.3V), (0.4ms,1.3V), (1ms,1.3V) .