ECE 2210 Frequency Response, Filters & Bode Plots

Frequency Response

In the Capacitors lab you made a "frequency dependent voltage divider" whose output was not the same for all frequencies of input. You made a graph of the output voltage as a function of the input frequency. That was a *frequency response* graph of the circuit. You made similar graphs in the Resonance lab. These graphs help show the relationship of the output to the input as a function of frequency. This relationship is known as the frequency response of the circuit. You may have heard the term used before in connection with speakers or microphones. All electrical and mechanical systems have frequency response characteristics. Sometimes the frequency response can be quite dramatic, like the Tacoma Narrows bridge.

Filter Circuits

A circuit which *passes* some frequencies and *filters out* other frequencies is called (surprise, surprise) a "filter" and this selection and rejection of frequencies is called "filtering". The tone or equalization controls on your stereo are frequency filters. So are the tuners in TVs and radios.

If a filter passes high frequencies and rejects low frequencies, then it is a high-pass filter. Conversely, if it passes low frequencies and rejects high ones, it is a low-pass filter. A filter that passes a range or band of frequencies and rejects frequencies lower or higher than that band, is a band-pass filter. The opposite of this is a band-rejection filter, or if the band is narrow, a notch filter or trap.

Look at the circuit at right. At low frequencies the impedance of the inductor is low and the output voltage is essentially shorted to ground. At high frequencies the impedance of the inductor is high and the output is about the same as the input. This is a high-pass filter. We can determine the relationship between the input and output:

 $\mathbf{V}_{out} = \frac{\mathbf{j} \cdot \boldsymbol{\omega} \cdot \mathbf{L}}{\mathbf{R} + \mathbf{j} \cdot \boldsymbol{\omega} \cdot \mathbf{L}} \cdot \mathbf{V}_{in} \qquad \mathsf{OR:} \qquad \frac{\mathbf{V}_{out}}{\mathbf{V}_{in}} = \frac{\mathbf{j} \cdot \boldsymbol{\omega} \cdot \mathbf{L}}{\mathbf{R} + \mathbf{j} \cdot \boldsymbol{\omega} \cdot \mathbf{L}} = \mathbf{H}(\boldsymbol{\omega})$

$$R$$

$$V_{out}$$

$$L$$

$$Z_{L} = j \cdot \omega \cdot L$$

$$gnd =$$

A *transfer function* is a general term used for any linear system that has an input and an output. It is simply the ratio of output to input. The idea is that if you multiply the input by the transfer function, you get the output.

$\mathbf{H}(\boldsymbol{\omega}) = \frac{j \cdot \boldsymbol{\omega} \cdot \mathbf{L}}{\mathbf{R} + j \cdot \boldsymbol{\omega} \cdot \mathbf{L}}$	At low frequencies:	R >> j·ωL	and	$\mathbf{H}(\boldsymbol{\omega}) \simeq \frac{\mathbf{j} \cdot \boldsymbol{\omega} \cdot \mathbf{L}}{\mathbf{R}}$	output is proportional to frequency
	At high frequencies:	R << j∙ωL	and	$\mathbf{H}(\boldsymbol{\omega}) \simeq \frac{j \cdot \boldsymbol{\omega} \cdot \boldsymbol{L}}{j \cdot \boldsymbol{\omega} \cdot \boldsymbol{L}} = 1$	output is about the same as the input.

Naturally, a plot of the transfer function verses frequency would be a handy thing. You've already made similar plots in the lab. It turns out that these plots are best done on a log-log scale. Unfortunately, they are actually plotted on a semilog scale using a special unit in the vertical axis called the *decibel* (dB) and the log is built into this dB unit. The dB unit doesn't really simplify things, but it is widely used and you'll need to know about it, so here goes.

Decibels

Your ears respond to sound logarithmically, both in frequency and in intensity. Musical octaves are in ratios of two. "A" in the middle octave is 220 Hz, in the next, 440 Hz, then 880 Hz, etc... It takes about ten times as much power for you to sense one sound as twice as loud as another.

10x power $\simeq 2x$ loudnessA bel is such a10x ratio of power.Power ratio expressed in bels = $log\left(\frac{P_2}{P_1}\right)$ belsBell, who did original research in hearing.

It is a logarithmic expression of a unitless ratio (like the magnitude of $H(\omega)$ or gain of an amplifier).

The bel unit is never actually used, instead we use the decibel (dB, 1/10th of a bel).

Power ratio expressed in dB =
$$10 \cdot \log \left(\frac{P_2}{P_1}\right) dB$$

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dB are also used to express voltage and current ratios, which is related to power when squared. P = $\frac{V^2}{R}$ = $I^2 \cdot R$

Voltage ratio expressed in dB =
$$10 \cdot \log \left(\frac{V_2^2}{V_1^2} \right) dB = 20 \cdot \log \left(\frac{V_2}{V_1} \right) dB$$

Current ratio expressed in dB = $20 \cdot \log \left(\frac{I_2}{I_1} \right)$

Some common ratios expressed as dB

$$20 \cdot \log\left(\frac{1}{\sqrt{2}}\right) = -3.01 \cdot dB \qquad 10^{-\frac{3}{20}} = 0.708 \qquad 20 \cdot \log\left(\sqrt{2}\right) = 3.01 \cdot dB \qquad 10^{-\frac{3}{20}} = 1.413$$

$$20 \cdot \log\left(\frac{1}{2}\right) = -6.021 \cdot dB \qquad 10^{-\frac{6}{20}} = 0.501 \qquad 20 \cdot \log(2) = 6.021 \cdot dB \qquad 10^{-\frac{6}{20}} = 1.995$$

$$20 \cdot \log\left(\frac{1}{10}\right) = -20 \cdot dB \qquad 10^{-\frac{20}{20}} = 0.1 \qquad 20 \cdot \log(10) = 20 \cdot dB \qquad 10^{-\frac{20}{20}} = 10$$

$$20 \cdot \log\left(\frac{1}{100}\right) = -40 \cdot dB \qquad 10^{-\frac{40}{20}} = 0.01 \qquad 20 \cdot \log(100) = 40 \cdot dB \qquad 10^{-\frac{40}{20}} = 100$$

These are the most common formulas used

for dB

dB

Other dB-based units

You may have encountered dB as an absolute measure of sound intensity (Sound Pressure Level or SPL). In that case the RMS sound pressure is compared as a ratio to a reference of 2 x 10⁻⁵ Pascals.

dBm is another absolute power scale expressed in dB. Powers are referenced to 1mW.

Volume Units (VU) are dBm with the added spec that the load resistor is 600Ω .

Bode Plots

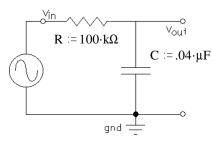
Named after Hendrik W. Bode (bo-dee), bode plots are just frequency response curves made on semilog paper where the horizontal axis is frequency on a log₁₀ scale and the vertical axis is either dB or phase angle. The plots are nothing special, but the method that Bode came up with to make them quickly and easily is special. We aren' t going to bother with the phase-angle plots in this class, but since the bode method of making frequency plots is so simple it's worth our time to see how it's done.

Basically, these are the steps:

- 1. Find the transfer function.
- 2. Analyze the transfer function to find "corner frequencies" and use these to divide the frequency into ranges.
- 3. Simplify and approximate the magnitude of the transfer function in each of these ranges.
- 4. Draw a "straight-line approximation" of the frequency response curve.
- 5. Use a few memorized facts to draw the actual frequency response curve.

The best way to learn the method is by examples.

Ex. 1



$$\frac{\mathbf{V}_{out}}{\mathbf{V}_{in}} = \frac{\frac{1}{j \cdot \omega \cdot C}}{\frac{1}{j \cdot \omega \cdot C} + R} = \frac{1}{1 + R \cdot (j \cdot \omega \cdot C)} = \mathbf{H}(\omega) = \text{The "Transfer Function"}$$

corner frequency is where real = imaginary (in denominator in this case)

$$1 = \omega_{c} \cdot R \cdot C \qquad \omega_{c} := \frac{1}{R \cdot C} \qquad \omega_{c} = 250 \cdot \frac{rad}{sec} \qquad \qquad \text{So...} \quad \mathbf{H}(\omega) := \frac{1}{1 + j \cdot \frac{\omega}{250 \cdot \frac{rad}{sec}}}$$

sec

 ω_c is also called a "pole" frequency

The transfer function is said to have one "pole" at ω_{c}

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To make a straight-line approximation of the magnitude of $\mathbf{H}(\omega)$ we' II approximate $\mathbf{H}(\omega)$ | in two regions, one below the corner frequency, and one above the corner frequency. Keep only the real or only the imaginary part of the denominator, depending on which is greater.

below the corner frequency:
$$\omega < \omega_{c}$$
 $\mathbf{H}(\omega) \simeq \frac{1}{1}$ $|\mathbf{H}(\omega)| \simeq 1$ $20 \cdot \log(1) = 0 \cdot dB$
above the corner frequency: $\omega > \omega_{c}$ $\mathbf{H}(\omega) \simeq \frac{1}{j \cdot \frac{\omega}{250 \cdot \frac{rad}{sec}}}$ $|\mathbf{H}(\omega)| \simeq \frac{1}{\omega} \cdot \left(250 \cdot \frac{rad}{sec}\right)$ inversely proportional to ω .

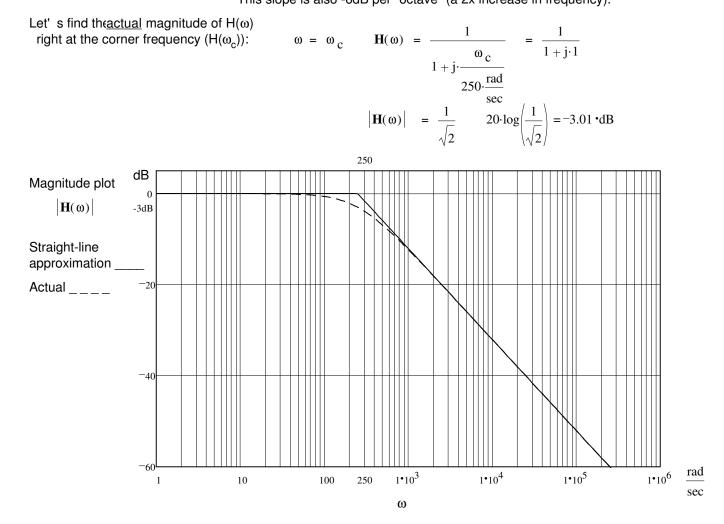
Inverse proportionality is a straight 1 to 1 down slope on a log-log plot, with dB it's a only slightly different. Since 10x corresponds to 20 dB, the line goes down 20 dB for every 10x increase in frequency (called a decade).

That's all you need to make the straight-line approximation shown in the plot below. (If you know the slope)

Try some values above the corner frequency:

$$20 \cdot \log \left[\frac{1}{10 \cdot \omega_{c}} \cdot \left(250 \cdot \frac{\text{rad}}{\text{sec}} \right) \right] = -20 \cdot \text{dB} \qquad 20 \cdot \log \left[\frac{1}{100 \cdot \omega_{c}} \cdot \left(250 \cdot \frac{\text{rad}}{\text{sec}} \right) \right] = -40 \cdot \text{dB}$$

The slope above the corner frequency is -20 dB per "decade". A decade is a 10x increase in frequency. This slope is also -6dB per "octave" (a 2x increase in frequency).



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 $\frac{\mathbf{V}_{\mathbf{R}}}{\mathbf{V}_{\mathbf{S}}} = 50 \cdot \frac{\mathbf{R}}{\frac{1}{j \cdot \boldsymbol{\omega} \cdot \mathbf{C}} + \mathbf{R}} = \frac{50 \cdot (\mathbf{R} \cdot (j \cdot \boldsymbol{\omega} \cdot \mathbf{C}))}{1 + \mathbf{R} \cdot (j \cdot \boldsymbol{\omega} \cdot \mathbf{C})} = \mathbf{H}(\boldsymbol{\omega})$ Transfer function $C = 0.2 \cdot \mu F$ $\overline{\mathbf{V}_{\mathbf{S}}} = 50^{-1}$ $\overline{\mathbf{V}_{\mathbf{S}}} = 50^{-1}$ $\overline{\mathbf{V}_{\mathbf{S}}} = 10 \cdot \mathrm{k\Omega}$ $R := 10 \cdot \mathrm{k\Omega}$ $R := 10 \cdot \mathrm{k\Omega}$ $\mathrm{corner \ frequency \ is \ where \ real = imaginary}$ $1 = \omega_{c} \cdot \mathrm{R} \cdot \mathrm{C} \qquad \omega_{c} := \frac{1}{\mathrm{R} \cdot \mathrm{C}} \qquad \omega_{c} = 500 \cdot \frac{\mathrm{rad}}{\mathrm{sec}}$ has one pole at ω_{c} So... $\mathbf{H}(\omega) := \frac{50 \cdot \mathbf{j} \cdot \frac{\omega}{500 \cdot \frac{\text{rad}}{\text{sec}}}}{1 + \mathbf{j} \cdot \frac{\omega}{500 \cdot \frac{\text{rad}}{\text{sec}}}} = \frac{50 \cdot \mathbf{j} \cdot \omega}{500 \cdot \frac{\text{rad}}{\text{sec}} + \mathbf{j} \cdot \omega}$ OR: $\mathbf{H}(\omega) := \frac{50 \cdot \mathbf{j} \cdot \frac{\omega}{\omega_c}}{1 + \mathbf{j} \cdot \frac{\omega}{\omega_c}}$ $\omega < \omega_{c} \quad \mathbf{H}(\omega) \simeq \frac{50 \cdot \mathbf{j} \cdot \frac{\omega}{500 \cdot \frac{\mathrm{rad}}{\mathrm{sec}}}}{1} = \frac{0.1 \cdot \frac{\mathrm{sec}}{\mathrm{rad}} \cdot \mathbf{j} \cdot \omega}{1} \quad |\mathbf{H}(\omega)| \simeq 0.1 \cdot \frac{\mathrm{sec}}{\mathrm{rad}} \cdot \omega$ Proportional to ω . That' s all we need to know here. This proportionality to ω will result in a +20 dB per decade 50·j·____ slope for all frequencies below the corner frequency 500-<u>rad</u> sec $\omega > \omega_c - H(\omega) \simeq$ $|\mathbf{H}(\boldsymbol{\omega})| \simeq 50$ j._____ω $20 \cdot \log(50) = 33.98 \cdot dB$ The "pass band" $500 \cdot \frac{\text{rad}}{--}$ sec Actual value at the corner frequency $\mathbf{H}(\omega) = \frac{50 \cdot j \cdot \omega}{500 \cdot \frac{\text{rad}}{\cos^2} + j \cdot \omega} = \frac{50 \cdot j}{1 + j \cdot 1} = 25 + 25j \qquad |25 + 25 \cdot j| = 35.355$ $\omega = \omega_{c}$ $20 \cdot \log(35.355) = 30.97 \cdot dB$ 3 dB lower than the magnitude in the pass band 500 Magnitude plot 40 $H(\omega)$ dB 34dB 31dB 30 Straight-line approximation 20 Actual ____ 10 0 -10^{1} rad 1•10³ 1•10⁴ 1•10⁵ 1•10⁶

1

10

100

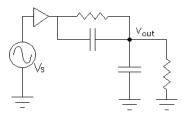
ω

sec

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Ex. 3 The transfer function may already be worked of	out:
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Could come from a circuit like this:



 $\mathbf{H}(\mathbf{f}) := 10 \cdot \frac{1 + \mathbf{j} \cdot \frac{\mathbf{f}}{10 \cdot \mathbf{Hz}}}{1 + \mathbf{j} \cdot \frac{\mathbf{f}}{500 \cdot \mathbf{Hz}}}$

The real and imaginary parts of the numerator are

equal at the one corner frequency (called a "zero")

The real and imaginary parts of the denominator are equal at the other corner frequency (pole)

$$= j \cdot \frac{f_c}{10 \cdot Hz} \qquad f_{c1} := 10 \cdot Hz$$

$$1 = j \cdot \frac{f_c}{500 \cdot Hz} \qquad f_{c2} = 500 \cdot Hz$$

There are now three regions to approximate |H(f)|

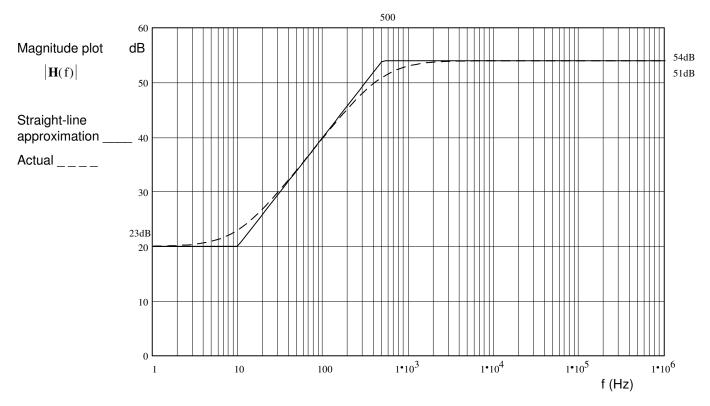
Below the first corner frequency:

 $f < 10 \cdot Hz$ $|H(f)| \simeq |10 \cdot \frac{1}{1}| = 10$ $20 \cdot \log(10) = 20 \cdot dB$ Between the corner frequencies: $10 \cdot \text{Hz} < f < 500 \cdot \text{Hz}$ $|\mathbf{H}(f)| \simeq \left| \begin{array}{c} j \cdot \frac{f}{10} \\ 10 \cdot \frac{1}{10} \\ 1 \end{bmatrix} = f$ proportional to f

Above the second corner frequency: $1000 \cdot \text{Hz} < \text{f}$

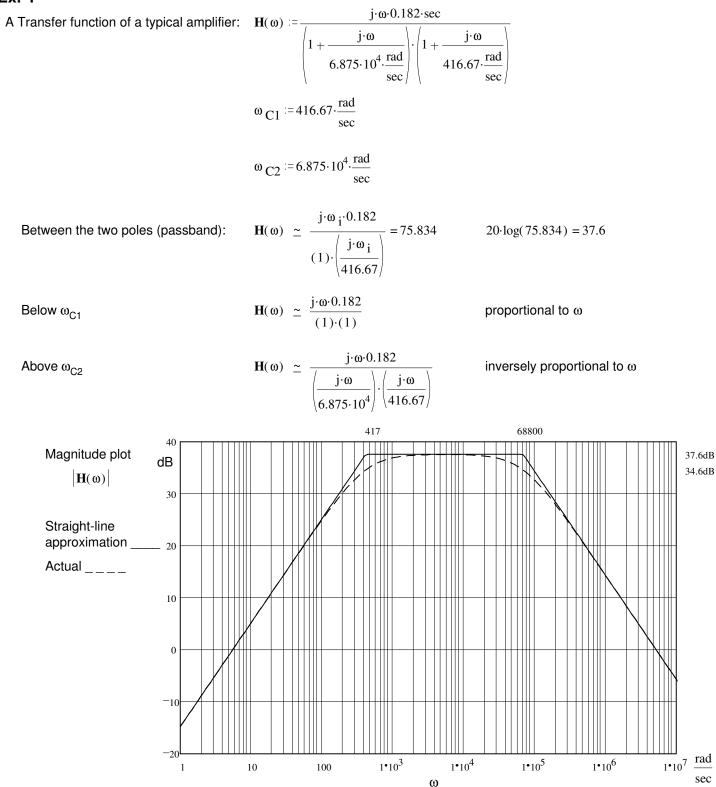
$$\mathbf{H}(f) = \begin{vmatrix} j \cdot \frac{f}{10} \\ 10 \cdot \frac{f}{j \cdot \frac{f}{500}} \end{vmatrix} = 500 \qquad 20 \cdot \log(500) = 53.98 \cdot dB$$

1



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Ex. 4



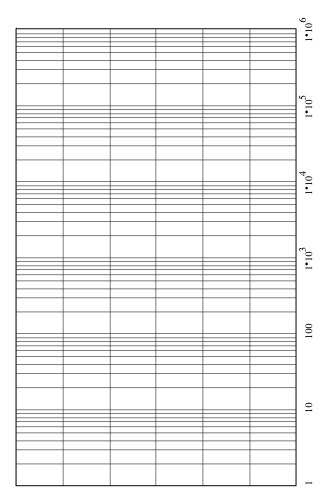
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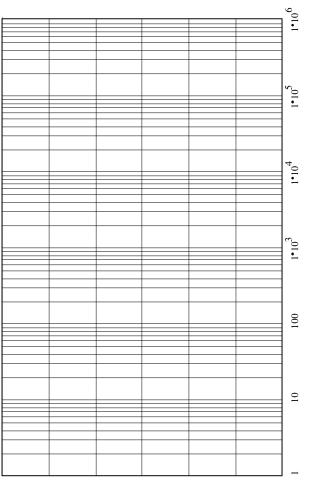
The Bode plots that we' ve covered here are the simplest types and only magnitude plots. This will do for an initial introduction to simple filters, but this coverage *is not* complete.

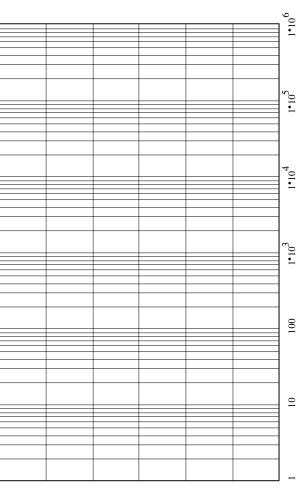
Complete Bode plots also include phase plots which we haven' t looked at at all. Also, if some poles and zeroes are too close to each other they can interact and even result in complex poles.

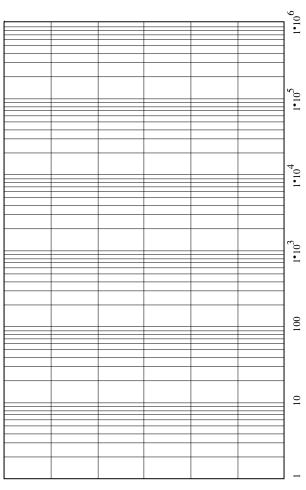
If asked in a future classes if you have "covered" Bode plots, do not make the mistake of saying "yes".

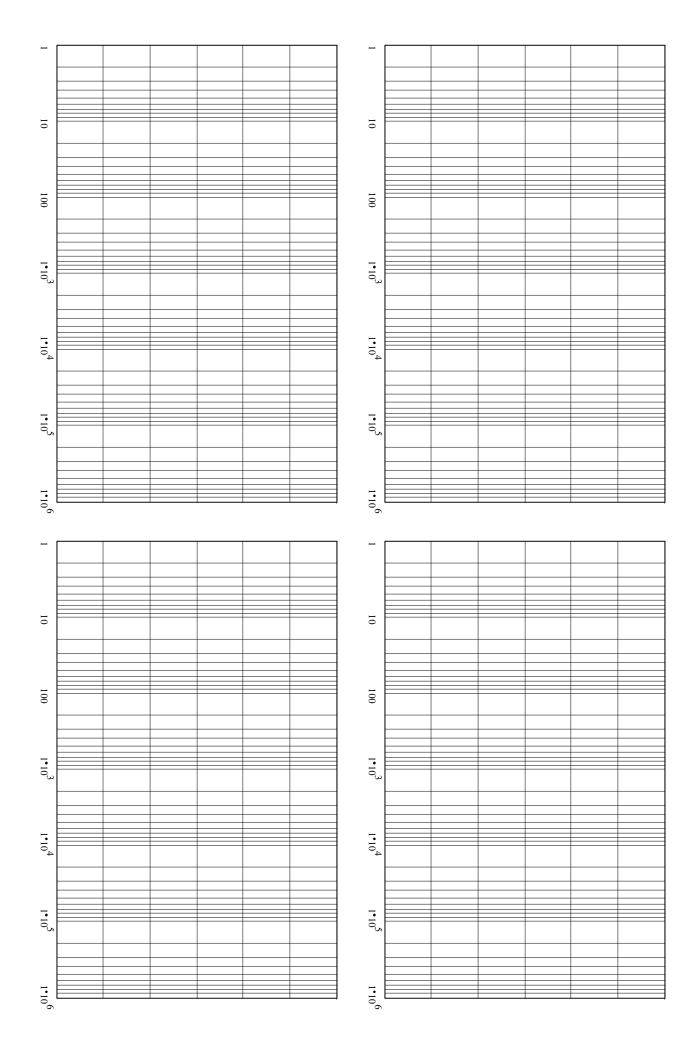
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1.6 Second-Order Transients

A circuit with both a capacitor and an inductor is like a mechanical system with both a mass and a spring. When there are two different types of energy-storage elements, the transient responses can be much more interesting than the simple exponential curves that we've seen so far. Many of these systems can oscillate or "ring" when a transient is applied. When you analyze a circuit with a capacitor and an inductor you get a second-order differential equation, so the transient voltages and currents are called second-order transients.

Series RLC circuit, traditional way: Look at the circuit at right. The same current flows through all three elements (i(t) or just i). That current will begin to flow after time t = 0, when the switch is closed. Using basic circuit laws:

$$V_{in} = v_R + v_L + v_C$$

= $i \cdot R + L \frac{d}{dt} i + \frac{1}{C} \int_{-\infty}^{t} i_C dt$ Making the obvious substitutions.

The next step here would be to differentiate both sides of the equation, but we've been through this before with the RC circuit. If you're a little more clever, there's an easier way.

 $i = i_C = C \cdot \frac{d}{dt} v_C$, to get $V_{in} = R \cdot C \cdot \frac{d}{dt} v_C + L \cdot C \cdot \frac{d^2}{dt^2} v_C + v_C$ Make this substitution instead Rearrange this equation to get $V_{in} = L C \cdot \frac{d^2}{dt^2} v_C + R \cdot C \cdot \frac{d}{dt} v_C + v_C$ and $\frac{V_{in}}{L \cdot C} = \frac{d^2}{dt^2} v_C + \frac{R \cdot C}{L \cdot C} \cdot \frac{d}{dt} v_C + \frac{1}{L \cdot C} \cdot v_C$

This is the classical second-order differential equation and it is solved just like the first-order differential equation, by guessing a solution of the right form and then finding the particulars of that solution.

 $v_{C}(t) = A + B \cdot e^{s \cdot t}$ Standard differential equation answer: Note: It will turn out that there will be two And again: $\frac{d^2}{dt^2} v_C = B \cdot s^2 \cdot e^{s \cdot t}$ $\frac{V_{\text{in}}}{V_{\text{in}}} = \frac{d^2}{dt^2} V_{\text{C}} + \frac{R}{L} \frac{d}{dt} V_{\text{C}} + \frac{1}{L \cdot C} V_{\text{C}}$ Substitute these back into the original equation: = $\mathbf{B} \cdot \mathbf{s}^2 \cdot \mathbf{e}^{\mathbf{s} \cdot \mathbf{t}} + \frac{\mathbf{R}}{\mathbf{L}} \cdot \mathbf{B} \cdot \mathbf{s} \cdot \mathbf{e}^{\mathbf{s} \cdot \mathbf{t}} + \frac{1}{\mathbf{L} \cdot \mathbf{C}} \cdot \left(\mathbf{A} + \mathbf{B} \cdot \mathbf{e}^{\mathbf{s} \cdot \mathbf{t}}\right)$

$$= \mathbf{B} \cdot \mathbf{s}^2 \cdot \mathbf{e}^{\mathbf{s} \cdot \mathbf{t}} + \frac{\mathbf{R}}{\mathbf{L}} \cdot \mathbf{B} \cdot \mathbf{s} \cdot \mathbf{e}^{\mathbf{s} \cdot \mathbf{t}} + \frac{1}{\mathbf{L} \cdot \mathbf{C}} \cdot \mathbf{B} \cdot \mathbf{e}^{\mathbf{s} \cdot \mathbf{t}} + \frac{1}{\mathbf{L} \cdot \mathbf{C}} \cdot \mathbf{A}$$

We can separate this equation into two parts, one which is time dependent and one which is not. Each part must still be an equation.

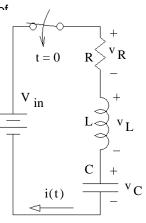
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Time independent (forced) part: $V_{in} = A$, $A = V_{in} = final condition = v_{C}(\infty)$ just like before

Time dependent (transient) part:
$$0 = B \cdot s^2 \cdot e^{s \cdot t} + \frac{R}{L} \cdot B \cdot s \cdot e^{s \cdot t} + \frac{1}{L \cdot C} \cdot B \cdot e^{s \cdot t}$$

to get: $0 = s^2 + \frac{r}{L} \cdot s + \frac{r}{L/C}$ = characteristic equation Divide both sides by B∙e

This equation is important. It is called the characteristic equation and we'll need to find one like it for every second-order circuit that we analyze. Luckily, there's a much easier way to get it, using impedances similar to those we used in phasor analysis. I'll talk about that in the next section, in the meantime, let's continue with this problem.



Once you have the characteristic equation

characteristic equation: $s^2 + \frac{R}{L} \cdot s + \frac{1}{L \cdot C} = 0$

Solutions to the characteristic equation:

$$s_1 = -\frac{R}{2 \cdot L} + \frac{1}{2} \cdot \sqrt{\left(\frac{R}{L}\right)^2 - \frac{4}{L \cdot C}} \qquad s_2 = -\frac{R}{2 \cdot L} - \frac{1}{2} \cdot \sqrt{\left(\frac{R}{L}\right)^2 - \frac{4}{L \cdot C}}$$

This results in three possible types of solutions, depending on what's under the radical, +, -, or 0.

Notice also that there are two s values (s1 and s2) and that leads to two two B's (we'll call them B and D)

Overdamped The part under the radical is +

 $\text{if } \left(\frac{R}{L}\right)^2 - \frac{4}{L \cdot C} > 0 \quad \text{then } s_1 \text{ and } s_2 \text{ are both real and} \quad s_1 \neq s_2 \text{ and our guessed solution } v_C(t) = A + B \cdot e^{s \cdot t}$

will become $v_C(t) = v_C(\infty) + B \cdot e^{s_1 \cdot t} + D \cdot e^{s_2 \cdot t}$ and is simply the combination of two exponentials.

Also both s_1 and s_2 will always be negative (unless you find a negative R, C, or L), meaning the exponential parts will decay with time and are thus transient.

This is the overdamped case, like a class of students on a Monday morning. Pretty dull and soon to be asleep.

Underdamped The part under the radical is -

if
$$\left(\frac{R}{L}\right)^2 - \frac{4}{L \cdot C} < 0$$
 then s_1 and s_2 are both complex and and can be expressed as
 $s_1 = \alpha + j \cdot \omega$ and $s_2 = \alpha - j \cdot \omega$

Well, if you start putting complex numbers in exponentials, what do you get? Euler's equations show that you'll get sines and cosines. In this case its much easier to rephrase the guessed solution like this.

$$v_{\mathbf{C}}(t) = v_{\mathbf{C}}(\infty) + e^{\alpha \cdot t} \left(B_{2} \cdot \cos(\omega \cdot t) + D_{2} \cdot \sin(\omega \cdot t) \right)$$

This form can be derived directly from $v_{C}(t) = A + B \cdot e^{s_{1} \cdot t} + D \cdot e^{s_{2} \cdot t}$

using Euler's equation, $e^{j \cdot \theta} = \cos(\theta) + j \cdot \sin(\theta)$, but we won't bother to here.

In fact, although B_2 and D_2 are <u>not</u> the same as B and D, I'll drop the "2" subscripts because we'll never actually need to convert between these two forms and the extra subscripts just become annoying.

So:
$$v_{C}(t) = v_{C}(\infty) + e^{\alpha \cdot t} (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$$

 α and ω come from the s₁ and s₂ solutions to the characteristic equation. ω is frequency at which the underdamped circuit will "ring" or "oscillate" in response to a transient. α sets the decay rate of that oscillation. Because α will always be negative the e^{at} term insures that the transient ringing dies out in time.

This is the underdamped case, like students on spring break in Fort Lauderdale.

Natural Frequency and the Damping Ratio

These are commonly used terms to describe the underdamped response in a normalized way, similar to the τ used to decribe first-order transient responses.

The "natural frequency" is defined as: $\omega_n = \sqrt{\alpha^2 - \omega^2}$

It is the frequency that the system would oscillate at if there were no damping (R = 0 in our case)

The damping ratio is defined as: $\zeta = \frac{\alpha}{\omega_n}$ (ζ is zeta) Transients p. 1.10 The characteristic equation is solved using the quadratic equation, recall:

if
$$a \cdot x^2 + b \cdot x + c = 0$$

there are two solutions

and

$$x_{1} = \frac{-b + \sqrt{b^{2} - 4 \cdot a \cdot c}}{2 \cdot a}$$

$$x_{2} = \frac{-b - \sqrt{b^{2} - 4 \cdot a \cdot c}}{2 \cdot a}$$

for this case:
$$\omega_n = \frac{1}{\sqrt{L \cdot C}}$$

Critically damped The part under the radical is 0

if $\left(\frac{R}{L}\right)^2 - \frac{4}{L \cdot C} = 0$ then s_1 and s_2 are both real and exactly the same. Now our guessed solution must be

modified to $v_C(t) = v_C(\infty) + B \cdot e^{s_1 \cdot t} + D \cdot t \cdot e^{s_2 \cdot t}$ and can result in a single overshoot.

This is actually a trivial case since it relies on an exact equality which will never happen in reality. The best use of the critically damped case is as a conceptual border between the over- and under-damped cases.

RLC examples

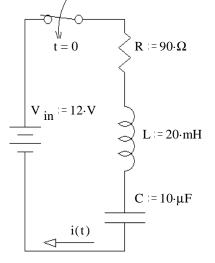
Let's use some component values in the RLC circuit and see what happens.

Overdamped Example

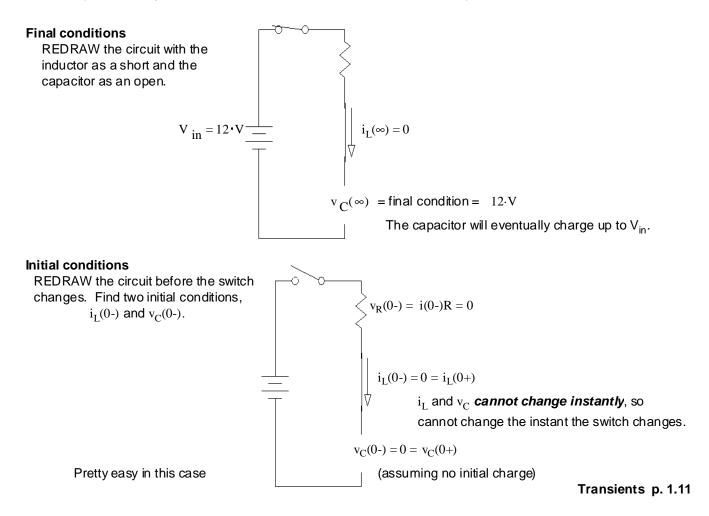
$$\left(\frac{R}{L}\right)^2 - \frac{4}{L\cdot C} > 0 \qquad s_1 \text{ and } s_2 \text{ are real and negative, overdamped.}$$
$$s_1 := -\frac{R}{2\cdot L} + \frac{1}{2} \cdot \sqrt{\left(\frac{R}{L}\right)^2 - \frac{4}{L\cdot C}} \qquad s_1 = -2000 \cdot \sec^{-1}$$

$$s_2 := -\frac{R}{2 \cdot L} - \frac{1}{2} \cdot \sqrt{\left(\frac{R}{L}\right)^2 - \frac{4}{L \cdot C}}$$
 $s_2 = -2500 \cdot \sec^{-1}$

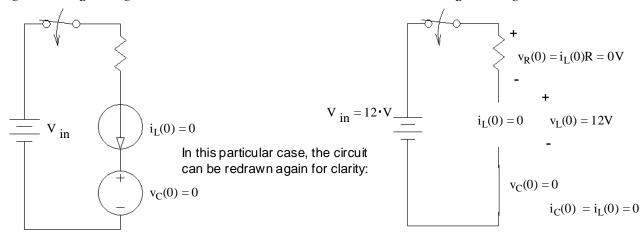
$$v_{C}(t) = v_{C}(\infty) + B \cdot e^{s_{1} \cdot t} + D \cdot e^{s_{2} \cdot t}$$



(As an example, the form is the same for all variables in this circuit)



REDRAW the circuit again just after the switch changes. Show the inductor as a current source of $i_L(0)$ (same as $i_L(0-)$) and the capacitor as a voltage source of $v_C(0)$ (same as $v_C(0-)$). Find two more initial conditions, $v_L(0)$ and $i_C(0)$. Both $v_L(0)$ or $i_C(0)$ can change instantly, so you **must** find them from $i_L(0)$ and $v_C(0)$.



Again, pretty easy in this case

Rearrange the basic equations for inductors and capacitors to find the initial slopes from $v_L(0)$ or $i_C(0)$.

Note: You will need only the first one if you are looking for $i_{T}(t)$.

You will need only the second one if you are looking for $v_{C}(t)$.

You may need both if you are looking for any other variable in the circuit. Other variables can usually be found most easily from $i_L(t)$ and/or $v_C(t)$.

To Find $v_{C}(t)$

At time t = 0 $v_C(0) = v_C(\infty) + B + D = 0$ $0 = 12 \cdot V + B + D$ Rearranging: $D = -12 \cdot V - B$

This equation has two unknowns. The initial slope will give us the needed second equation.

Differentiate the solution:
$$v_{C}(t) = v_{C}(\infty) + B \cdot e^{s_{1} \cdot t} + D \cdot e^{s_{2} \cdot t}$$

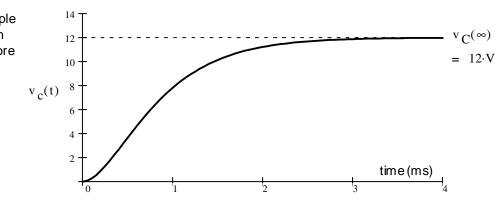
to get: $\frac{d}{dt} v_{C}(t) = 0 + B \cdot s_{1} \cdot e^{s_{1} \cdot t} + D \cdot s_{2} \cdot e^{s_{2} \cdot t}$
At time $t = 0$: $\frac{d}{dt} v_{C}(0) = B \cdot s_{1} + D \cdot s_{2}$
From initial conditions, above: $\frac{d}{dt} v_{C}(0) = \frac{i_{C}(0)}{C} = 0 \cdot \frac{V}{sec}$
Combining: $0 \cdot \frac{V}{sec} = B \cdot s_{1} + D \cdot s_{2}$ The second equation!
Solve simultaneously for B and D: $0 \cdot \frac{V}{sec} = B \cdot s_{1} + (-12 \cdot V - B) \cdot s_{2}$
B = $s_{2} \cdot \frac{12 \cdot V}{(s_{1} - s_{2})} = -60 \cdot V$
Transients p. 1.12
D = $-12 \cdot V - B = -12 \cdot V - -60 \cdot V = 48 \cdot V$

recall the solution:
$$v_{C}(t) = v_{C}(\infty) + B \cdot e^{s_{1} \cdot t} + D \cdot e^{s_{2} \cdot t}$$

 $v_{\mathbf{C}}(t) = 12 \cdot \mathbf{V} - 60 \cdot \mathbf{V} \cdot \mathbf{e}^{-\frac{2000}{\text{sec}} \cdot t} + 48 \cdot \mathbf{V} \cdot \mathbf{e}^{-\frac{2000}{\text{sec}} \cdot t}$

Substitute everything back in back in:

Notice that this is not a simple exponential curve, although admittedly it's not much more interesting.

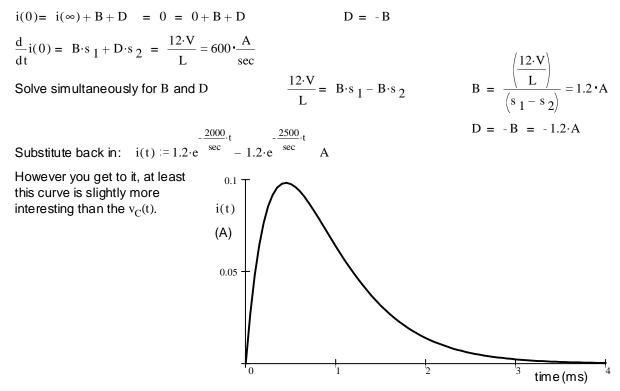


 $-\frac{2500}{\text{sec}} \cdot t$

To Find $i_L(t)$ or $i_R(t)$ or $i_C(t)$ which all the same i(t).

 $i(t) = i(\infty) + B \cdot e^{s \cdot 1 \cdot t} + D \cdot e^{s \cdot 2 \cdot t}$

From final and initial conditions



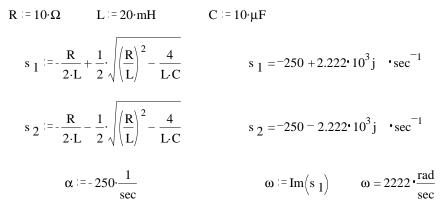
We could have found the same result from $v_{C}(t)$, using that to find $i_{L}(t)$:

$$i_{C}(t) = C \cdot \frac{d}{dt} v_{C}(t) = C \cdot \frac{d}{dt} \left(12 \cdot V - 60 \cdot V \cdot e^{-\frac{2000}{\sec} \cdot t} + 48 \cdot V \cdot e^{-\frac{2500}{\sec} \cdot t} \right)$$

$$= C \cdot (-60 \cdot V) \cdot \left(-\frac{2000}{\sec} \right) \cdot e^{-\frac{2000}{\sec} \cdot t} + C \cdot 48 \cdot V \cdot \left(-\frac{25}{\sec} \right) \cdot e^{-\frac{2500}{\sec} \cdot t}$$

$$C \cdot (-60 \cdot V) \cdot \left(-\frac{2000}{\sec} \right) = 1.2 \cdot A \qquad C \cdot 48 \cdot V \cdot \left(-\frac{2500}{\sec} \right) = -1.2 \cdot A \qquad \text{and} \quad i(t) := 1.2 \cdot e^{-\frac{2000}{\sec} \cdot t} - 1.2 \cdot e^{-\frac{2500}{\sec} \cdot t}$$
same
Transients p. 1.13

Underdamped Example



The final and initial conditions are the same as before, since they did not depend on R and R is the only component that is different.

Let's find the current again this time.

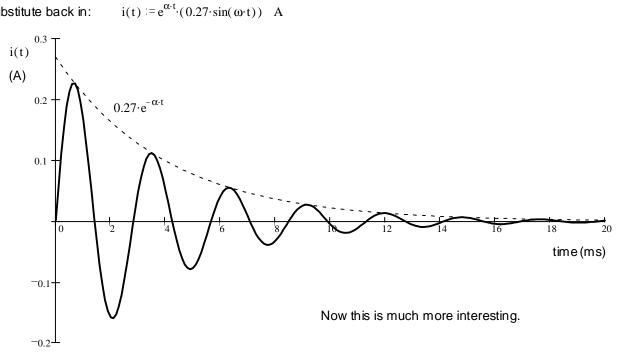
 $i(t) = i(\infty) + e^{\alpha \cdot t} (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$ (underdamped this time) $i(0) = i(\infty) + B$, 0 = 0 + B $\mathbf{B} := \mathbf{0} \cdot \mathbf{A}$

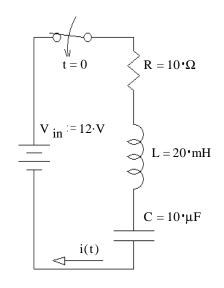
 $\label{eq:differentiate the solution: } \text{Differentiate the solution: } i(t) = i(\infty) + e^{\alpha \cdot t} \cdot (B \cdot cos(\omega \cdot t) + D \cdot sin(\omega \cdot t))$

to get:
$$\frac{d}{dt}i(t) = \alpha \cdot e^{\alpha \cdot t} \cdot (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t)) + e^{\alpha \cdot t} \cdot (-B \cdot \sin(\omega \cdot t) \cdot \omega + D \cdot \cos(\omega \cdot t) \cdot \omega)$$

At time $t = 0$: $\frac{d}{dt}i(0) = B \cdot \alpha + D \cdot \omega$ Solve for D: $D = \frac{\frac{d}{dt}i(0) - B \cdot \alpha}{\omega}$
 $\frac{d}{dt}i(0) = \frac{12 \cdot V}{L}$ $D = \frac{\frac{12 \cdot V}{L} - B \cdot \alpha}{\omega} = 0.27 \cdot A$

Substitute back in:

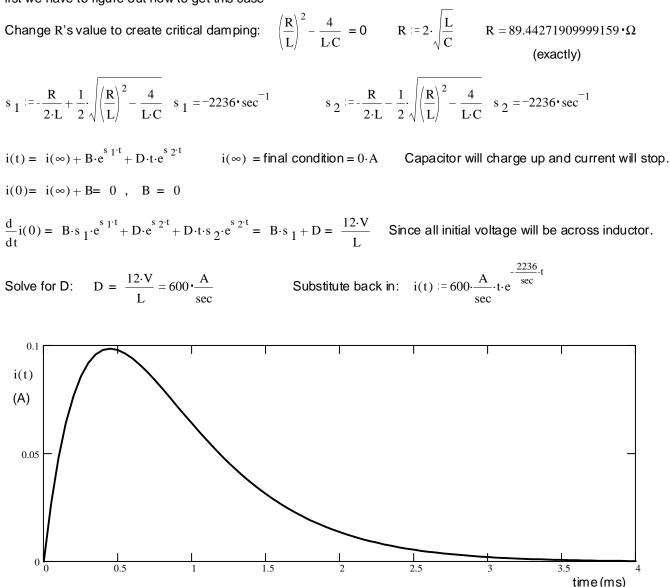




Transients p. 1.14

Critically Damped Example

First we have to figure out how to get this case



if you notice a remarkable similarity with the overdamped case, that's common for critical damping.

1.7 The Easy Way to get the Characteristic Equation

Recall from your Ordinary Differential Equations class, the Laplace transform method of solving differential equations. The Laplace transform allowed you to change time-domain functions to frequency-domain functions. We've already done this for steady-state AC circuits. We changed functions of t into functions of job. That was the frequency domain. Laplace let's us do the same sort of thing for transients. The general procedure is as follows.

1) Transform your forcing functions into the frequency domain with the Laplace transform.

2) Solve your differential equations with plain old algebra, where:

<u>d</u>	operation can be replaced with s,	and	∎ dt	can be replaced by $\frac{1}{-}$
dt			,	S

٩

3) Transform your result back to the time domain with the inverse Laplace transform.

Step 1 isn't too bad, but step 3 can be a total pain without a good computer program to do the job. However, step 2 sounds great. It turns out that we can use step 2 alone and still learn a great deal about our circuits and other systems without ever bothering with steps 1 and 3.

First remember from your study of Laplace that differentiation in the time domain was the same as multiplication by s in the frequency domain. That's really all we need and we're off and running.

$$v_{L}(t) = L \frac{d}{dt} i_{L}(t) \xrightarrow{\dots > V} V_{L}(s) = L \cdot s \cdot I_{L}(s) \quad \text{and} \quad i_{C}(t) = C \cdot \frac{d}{dt} v_{C}(t) \xrightarrow{\dots > I} V_{C}(s) = C \cdot s \cdot V_{C}(s)$$

Leading to the Laplace impedances: Ls for an inductor and $\frac{1}{C_s}$ for a capacitor.

That's it, now we can use these impedances just like the jo impedances, and we can use all the tools developed for DC. And with Laplace we don't even have to mess with complex numbers.

Look what happens to the RLC circuit now.

/ 1

Pick any dependent variable (I(s), $V_R(s)$, $V_L(s)$, or $V_C(s)$) and write a transfer function, t = 0 which is a ratio of the dependent variable to the input ($V_{1:n}(s)$). like this: t=U _Vin (s) |+ _ Ls3V

$$V_{in}(s) = I(s) \cdot \left(\frac{1}{C \cdot s} + R + L \cdot s\right)$$

Transfer function = H(s) = $\frac{I(s)}{V_{in}(s)} = \frac{1}{\left(\frac{1}{C \cdot s} + R + L \cdot s\right)}$

Manipulate this transfer function into this form: $\frac{a_1 \cdot s^2 + b_1 \cdot s + k_1}{s^2 + b_1 \cdot s + k}$ One polynomial divided by another.

$$\frac{I(s)}{V_{in}(s)} = \frac{1 \cdot (C \cdot s)}{(1 + R + L \cdot s \cdot (C \cdot s))} = \frac{\frac{1}{L} \cdot s}{\left(s^2 + \frac{R}{L} \cdot s + \frac{1}{L \cdot C}\right)}$$

in the correct form.

Set the denominator to 0 and you get the characteristic equation:

 $s^{2} + \frac{R}{L} \cdot s + \frac{1}{LC} = 0$

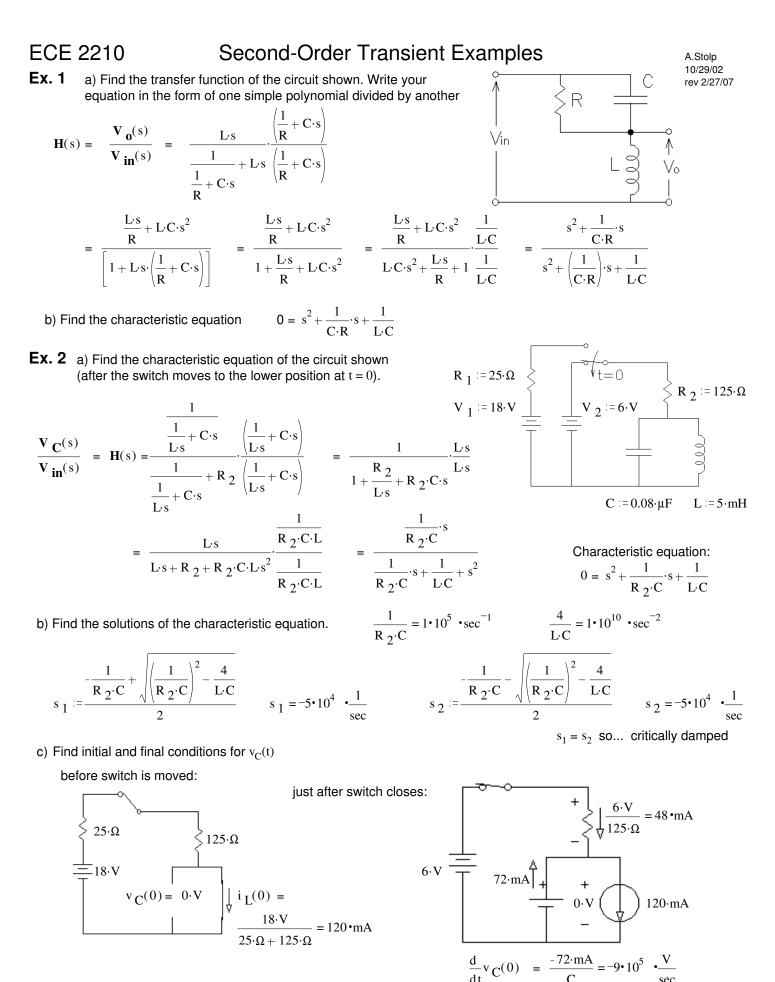
At this point you just proceed with the solution like you did before; Solve the characteristic equation to find s₁ and s₂. Decide which case you have (over-, under-, or critically damped). Use the two initial conditions, $i_{\rm L}(0)$ and $v_{\rm C}(0)$ to find the initial condition and the initial slope of your variable of interest, then use those to find the constants B and D.

Differential equation from the transfer function

You can also use the transfer function to go back and find the differential equation, just replace each s with a

 $\frac{d}{dt} \quad \text{and go back to functions of t.} \quad \frac{1}{L} \frac{d}{dt} V_{in}(t) = \left(\frac{d^2}{dt^2} i(t) + \frac{R}{L} \frac{d}{dt} i(t) + \frac{1}{L \cdot C} \cdot i(t) \right) \quad \text{Actually this is a pretty useless thing to do.}$

Transients p. 1.16



Sec

Second-Order Transient Examples, p.2
Final conditions:
6.V

$$v_{C}(\infty) = 0.V$$

 $v_{C}(\infty) = 0.V$
 $v_{C}(0) = v_{C}(\infty) + B = 0$
 $v_{C}(1) = -9.10^{5} \frac{V}{sec} + e^{8.11}$
 $v_{C}(2) = -9.10^{5}$

$$\alpha := \operatorname{Re}(s_1) \qquad \alpha = -6.25 \cdot 10^3 \cdot \operatorname{sec}^{-1} \qquad \omega := \operatorname{Im}(s_1) \qquad \omega = 4.961 \cdot 10^4 \cdot \operatorname{sec}^{-1}$$

Second-Order Transient Examples, p.2

c) Find initial and final conditions for $\boldsymbol{v}_{C}(t)$ See drawings above

$$v_{C}(\infty) = 0 \cdot V$$

d) Find the full expression of $v_{\rm C}(t)$. Underdamped

$$v_{C}(t) = v_{C}(\infty) + e^{\alpha t} \cdot (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$$

Ex. 3 a) Find the characteristic equation of the circuit shown. (after the switch opens at t = 0). Write your equation in the form of a simple polynomials.

$$H(s) = \frac{I}{V_{in}(s)} = \frac{1}{Z(s)} = \frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{1}{\frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}} = \frac{1}{\frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}} = \frac{\frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{\frac{1}{\frac{1}{R_{2}} + C \cdot s}}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{1}{\frac{1}{R_{2}} + C \cdot s} = \frac{1}{\frac{1}{\frac{1}{R_{2}} + C \cdot s}} = \frac{1}{\frac$$

Characteristic eq.:
$$0 = s^2 + \left(\frac{1}{C \cdot R_2} + \frac{R_1}{L}\right) \cdot s + \left(1 + \frac{R_1}{R_2}\right) \cdot \frac{1}{L \cdot C}$$

Second-Order Transient Examples, p.3

b) Find the solutions (numbers) of the characteristic equation:

$$b := \frac{1}{C \cdot R_2} + \frac{R_1}{L} \qquad b = 3.5 \cdot 10^4 \cdot \sec^{-1} \qquad k := \left(1 + \frac{R_1}{R_2}\right) \cdot \frac{1}{L \cdot C} \qquad k = 1.5 \cdot 10^9 \cdot \sec^{-2}$$

$$s_1 := \frac{-b + \sqrt{b^2 - 4 \cdot k}}{2} \qquad s_1 = -1.75 \cdot 10^4 + 3.455 \cdot 10^4 j \qquad \cdot \frac{1}{\sec} \qquad \alpha := -\frac{b}{2} \qquad \alpha = -1.75 \cdot 10^4 \cdot \sec^{-1}$$

$$s_2 := \frac{-b - \sqrt{b^2 - 4 \cdot k}}{2} \qquad s_2 = -1.75 \cdot 10^4 - 3.455 \cdot 10^4 j \qquad \cdot \frac{1}{\sec} \qquad \omega := \frac{1}{2} \cdot \sqrt{4 \cdot k - b^2} \qquad \omega = 3.455 \cdot 10^4 \cdot \sec^{-1}$$
Underdamped

1

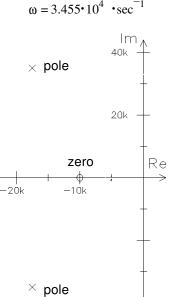
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c) Plot the poles and zeroes of the transfer function.

The poles are the s's where the denominator is zero, that is, the $s_1 \& s_2$ solutions to the characteristic equation.

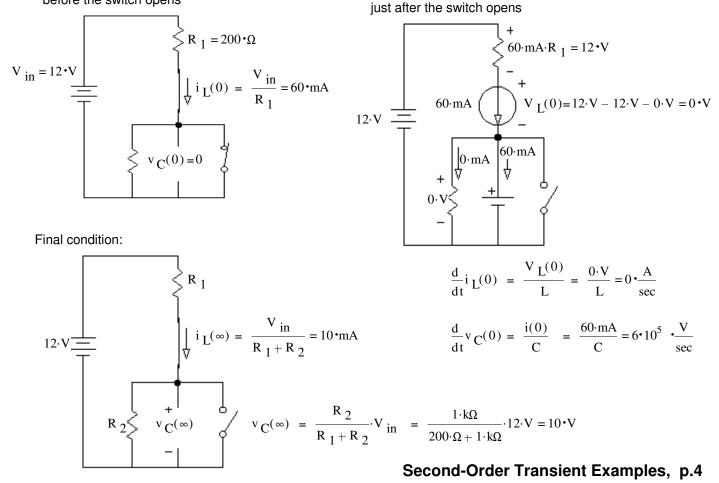
The zero is the s where the numerator is zero: $0 = \frac{1}{L \cdot C \cdot R_2} + \frac{C}{L \cdot C} \cdot s$

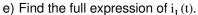
$$s = -\frac{1}{C \cdot R_2} = -1 \cdot 10^4 \cdot \sec^{-1}$$



d) Initial and final conditions for $\boldsymbol{i}_L(t)$ and $\boldsymbol{v}_C(t).$

before the switch opens





 $X(t) = X(\infty) + e^{\alpha t} (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$ Underdamped $i_{I}(t) = i_{I}(\infty) + e^{\alpha \cdot t} \cdot (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$ $i_{L}(0) = i_{L}(\infty) + B$ so.. $B = i_{L}(0) - i_{L}(\infty)$ $B := 60 \cdot mA - 10 \cdot mA$ $B = 50 \cdot mA$ $\frac{d}{dt}i_{L}(0) = B\cdot\alpha + D\cdot\omega \quad \text{ so.. } \quad D = \frac{\frac{d}{dt}i_{L}(0) - B\cdot\alpha}{\omega} \qquad D := \frac{0\cdot\frac{A}{sec} - B\cdot\alpha}{\omega}$ $D = 25.325 \cdot mA$ $i_{L}(t) := 10 \cdot mA + e^{-\frac{17500}{\sec} \cdot t} \left(50 \cdot mA \cdot \cos\left(\frac{34550}{\sec} \cdot t\right) + 25.325 \cdot mA \cdot \sin\left(\frac{34550}{\sec} \cdot t\right) \right)$ $i_{L}(0)_{60}$ 50 40 $i_{I}(t)$ (mA) 30 20 $i_{I}(\infty) = 10 \cdot mA$ 10 0 time (µs) 250 50 100 150 200 300 350 f) Find the full expression of $v_{C}(t)$. $D := \frac{6 \cdot 10^5 \cdot \frac{V}{\text{sec}} - B \cdot \alpha}{2}$ $\mathbf{B} := \mathbf{0} \cdot \mathbf{V} - \mathbf{10} \cdot \mathbf{V} \qquad \mathbf{B} = -\mathbf{10} \cdot \mathbf{V}$ $D = 12.301 \cdot V$ $v_{C}(t) = v_{C}(\infty) + e^{\alpha t} \cdot (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$ $\mathbf{v}_{\mathbf{C}}(\mathbf{t}) \coloneqq 10 \cdot \mathbf{V} + \mathbf{e}^{-\frac{17500}{\text{sec}} \mathbf{t}} \cdot \left(-10 \cdot \mathbf{V} \cdot \cos\left(\frac{34550}{\text{sec}} \cdot \mathbf{t}\right) + 12.301 \cdot \mathbf{V} \cdot \sin\left(\frac{34550}{\text{sec}} \cdot \mathbf{t}\right)\right)$ 20 $v c^{(t)}$ (volts) $v_{C}(\infty) = 10 \cdot V$ 10 5

time (µs)

200

250

300

350

Second-Order Transient Examples, p.5

50

100

150

h) What value of R1 would make this system critically damped?

$$\left(\frac{1}{C \cdot R_{2}} + \frac{R_{1}}{L}\right)^{2} = 4 \cdot \left(1 + \frac{R_{1}}{R_{2}}\right) \cdot \frac{1}{L \cdot C} \qquad \qquad \frac{1}{C^{2} \cdot R_{2}^{2}} + \frac{2}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} = \frac{4}{L \cdot C} + \frac{4}{C \cdot R_{2}} \cdot \frac{R_{1}}{L} + \frac{R_{1}^{2}}{L^{2}} + \frac{R_{1}^{2}}{L$$

Solve for \boldsymbol{R}_1 with quadradic equation:

Quadradic equation can be reduced to:

$$R_{1} = \frac{\frac{2 \cdot L}{C \cdot R_{2}} + \sqrt{\left(\frac{2 \cdot L}{C \cdot R_{2}}\right)^{2} - 4 \cdot \left(\frac{L^{2}}{C^{2} \cdot R_{2}^{2}} - \frac{4 \cdot L}{C}\right)}}{2} = \frac{L}{C \cdot R_{2}} - \frac{4}{2} \cdot \sqrt{\frac{L}{C}} = -485.7 \cdot \Omega \quad \text{this solution can't be}$$
$$= \frac{L}{C \cdot R_{2}} + \frac{4}{2} \cdot \sqrt{\frac{L}{C}} = 645.7 \cdot \Omega \quad \text{this must be the solution}$$

Ex. 2 with bigger R₁
$$R_1 := 1 \cdot k\Omega$$
 This should make the system overdamped
 $b := \frac{1}{C \cdot R_2} + \frac{R_1}{L}$ $b = 1.35 \cdot 10^5 \cdot \sec^{-1}$ $k := \left(1 + \frac{R_1}{R_2}\right) \cdot \frac{1}{L \cdot C}$ $k = 2.5 \cdot 10^9 \cdot \sec^{-2}$
 $s_1 := \frac{-b + \sqrt{b^2 - 4 \cdot k}}{2}$ $s_1 = -2.215 \cdot 10^4 \cdot \frac{1}{\sec}$ $s_2 := \frac{-b - \sqrt{b^2 - 4 \cdot k}}{2}$ $s_2 = -1.128 \cdot 10^5 \cdot \frac{1}{\sec}$

Overdamped

$$v_{C}(0) = 0 \qquad i_{L}(0) = \frac{V_{in}}{R_{1}} = 12 \cdot mA = i_{C}(0) \qquad \frac{d}{dt} v_{C}(0) = \frac{i(0)}{C} = \frac{12 \cdot mA}{C} = 1.2 \cdot 10^{5} \cdot \frac{V}{sec}$$

$$v_{C}(\infty) = \frac{R_{2}}{R_{1} + R_{2}} \cdot V_{in} = 6 \cdot V \qquad i_{L}(\infty) = \frac{V_{in}}{R_{1} + R_{2}} = 6 \cdot mA \qquad 1.2 \cdot 10^{5} \cdot \frac{1}{sec} = 1.2 \cdot 10^{5} \cdot \frac{1}{sec} = 1.2 \cdot 10^{5} \cdot \frac{V}{sec}$$

$$v_{C}(0) = v_{C}(\infty) + B + D \qquad 1.2 \cdot 10^{5} \cdot \frac{1}{sec} = 1.2 \cdot 10^{5$$

$$0 \cdot V = 6 \cdot V + B + D \qquad B = -(6 \cdot V + D)$$

$$\frac{d}{dt} v_{C}(0) = B \cdot s_{1} + D \cdot s_{2} = -6 \cdot V \cdot s_{1} - D \cdot s_{1} + D \cdot s_{2} \qquad D := \frac{1.2 \cdot 10^{5} \cdot \frac{V}{\sec} + 6 \cdot V \cdot s_{1}}{s_{2} - s_{1}} \qquad D = 0.143 \cdot V$$

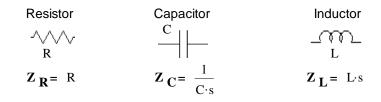
$$B = -(6 \cdot V + D) = -6.143 \cdot V$$

$$v_{C}(t) = v_{C}(\infty) + B \cdot e^{s_{1} \cdot t} + D \cdot e^{s_{2} \cdot t} \qquad v_{C}(t) := 6 \cdot V - 6.143 \cdot V \cdot e^{\frac{2.215 \cdot 16^{4}}{\sec} \cdot t} + 0.143 \cdot V \cdot e^{\frac{1.128 \cdot 10^{5}}{\sec} \cdot t}$$

$$v_{C}(\infty) = 6 \cdot V$$

(volts) time (µs) Second-Order Transient Examples, p.6 50 100 150 200 250 300 350

Second Order Transients Notes.



Transfer function

Laplace impedances

Use Laplace impedances, manipulate your circuit equation(s) to find a transfer function:

 $\frac{\text{output}}{\text{input}} = \frac{\mathbf{V}_{\mathbf{X}}(s)}{\mathbf{V}_{\mathbf{in}}(s)} = \frac{a_{1} \cdot s^{2} + b_{1} \cdot s + k_{1}}{s^{2} + b \cdot s + k} = \text{transfer function}$ a_1, b_1, k_1 coefficients may be zero Rearrange circuit equation to: H(s) =

Characteristic equation

To find the poles of the transfer function

Complete solution

Complete solution Solutions to the characteristic equation: $s_1 = -\frac{b}{2} + \frac{\sqrt{b^2 - 4 \cdot k}}{2}$ $s_2 = -\frac{b}{2} - \frac{\sqrt{b^2 - 4 \cdot k}}{2}$

Find initial Conditions $(v_{C} \text{ and/or } i_{L})$

Find conditions of just before time t = 0, $v_C(0)$ and $i_L(0)$. These will be the same just after time t = 0, $v_C(0)$ and $i_L(0)$ and will be your initial conditions.

 $s^2 + b \cdot s + k = 0$

characteristic equation

Use normal circuit analysis to find your desired variable: $v_X(0)$ or $i_X(0)$ Also find: $\frac{d}{dt}v_X(0)$ or $\frac{d}{dt}i_X(0)$ The trick to finding these is to see that: $\frac{d}{dt}v_C(0) = \frac{i_C(0)}{C}$ and $\frac{d}{dt}i_L(0) = \frac{v_L(0)}{T}$

Find final conditions ("steady-state" or "forced" solution)

DC inputs: Inductors are shorts Capacitors are opens Solve by DC analysis $v_X(\infty)$ or $i_X(\infty)$ AC inputs: Solve by AC steady-state analysis using $j\omega$

X(t) may be replaced by $v_{\rm X}(t),\,i_{\rm X}(t)$ or any desired variable in the equations below

Overdamped
$$b^2 - 4 \cdot k > 0$$
 s_1 and s_2 are real and negative

$$X(t) = X(\infty) + B \cdot e^{s} + D \cdot e^{s}$$

 $X(t) = X(\infty) +$

$$X(0) = X(\infty) + B + D$$
 $\frac{d}{dt}X(0) = B \cdot s_1 + D \cdot s_2$ Solve simultaneously for B and D

<u>Critically damped</u> $b^2 - 4 \cdot k = 0$ $s_1 = s_2 = -\frac{b}{2} = s$ s_1 and s_2 are real, equal a real, equal and $X(t) = X(\infty) + B \cdot e^{s \cdot t} + D \cdot t \cdot e^{s \cdot t}$ negative $\begin{array}{lll} X(0) = & X(\infty) + B \\ \text{so.. } B = & X(0) - X(\infty) \end{array} & \begin{array}{lll} \frac{d}{dt}X(0) &= & B \cdot s + D \\ \frac{d}{dt}X(0) - & B \cdot s + D \end{array} & \begin{array}{lll} \text{so.. } D = & \frac{d}{dt}X(0) - B \cdot s \\ \end{array}$ $X(0) = X(\infty) + B$

<u>Underdamped</u> $b^2 - 4 \cdot k < 0$ $s_1 = \alpha + j \cdot \omega$ $s_2 = \alpha - j \cdot \omega$ α is negative complex s, and s_a

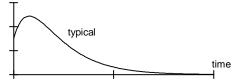
$$e^{\omega \cdot t} \cdot (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$$

 $\frac{d}{dt}$

$$X(0) = X(\infty) + B \qquad \qquad \frac{d}{dt}X(0) = B \cdot \alpha + D \cdot \omega \quad \text{so.. } D = \frac{\frac{d}{dt}X(0) - B \cdot \alpha}{\omega}$$

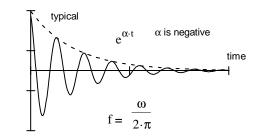
so.. B = X(0) - X(\infty)

ECE 2210 Notes, Second Order Transients



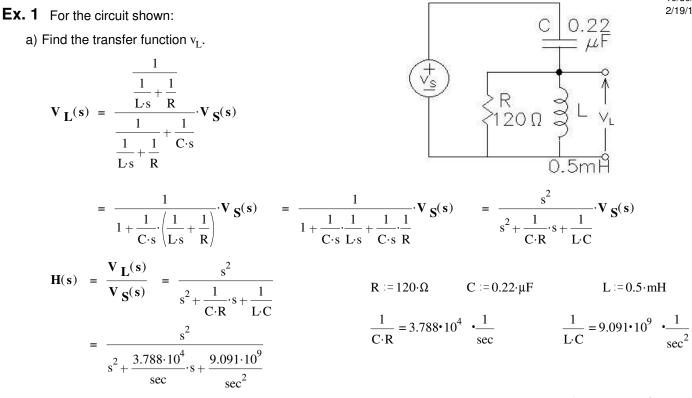
typical

time



ECE 2210 Lecture 18 notes Second order Transient examples

A. Stolp 10/30/06 2/19/10



b) Find the characteristic equation for this circuit.

 $0 = s^{2} + \frac{1}{C \cdot R} \cdot s + \frac{1}{L \cdot C} = s^{2} + \frac{3.788 \cdot 10^{4}}{sec} \cdot s + \frac{9.091 \cdot 10^{9}}{sec^{2}}$ Just the denominator set to zero. The solutions of the

characteristic equation are the "poles" of the transfer function.

c) Find the differential equation for v_{I} .

Cross-multiply the transfer function

$$s^{2} \cdot \mathbf{V}_{\mathbf{S}}(\mathbf{s}) = \left(s^{2} + \frac{1}{\mathbf{C} \cdot \mathbf{R}} \cdot \mathbf{s} + \frac{1}{\mathbf{L} \cdot \mathbf{C}}\right) \cdot \mathbf{V}_{\mathbf{L}}(\mathbf{s})$$

$$s^{2} \cdot \mathbf{V}_{\mathbf{S}}(\mathbf{s}) = s^{2} \cdot \mathbf{V}_{\mathbf{L}}(\mathbf{s}) + \frac{1}{\mathbf{C} \cdot \mathbf{R}} \cdot \mathbf{s} \cdot \mathbf{V}_{\mathbf{L}}(\mathbf{s}) + \frac{1}{\mathbf{L} \cdot \mathbf{C}} \cdot \mathbf{V}_{\mathbf{L}}(\mathbf{s})$$

$$\frac{d^{2}}{dt^{2}} \mathbf{v}_{\mathbf{S}}(t) = \frac{d^{2}}{dt^{2}} \mathbf{v}_{\mathbf{L}}(t) + \frac{1}{\mathbf{C} \cdot \mathbf{R}} \cdot \frac{d}{dt} \mathbf{v}_{\mathbf{L}}(t) + \frac{1}{\mathbf{L} \cdot \mathbf{C}} \cdot \mathbf{v}_{\mathbf{L}}(t)$$

$$\frac{d^{2}}{dt^{2}} \mathbf{v}_{\mathbf{S}}(t) = \frac{d^{2}}{dt^{2}} \mathbf{v}_{\mathbf{L}}(t) + \frac{3.788 \cdot 10^{4}}{\sec} \cdot \frac{d}{dt} \mathbf{v}_{\mathbf{L}}(t) + \frac{9.091 \cdot 10^{9}}{\sec^{2}} \cdot \mathbf{v}_{\mathbf{L}}(t)$$

d) What are the solutions to the characteristic equation?

$$s_{1} = \frac{-3.788 \cdot 10^{4}}{2} + \frac{1}{2} \cdot \sqrt{(3.788 \cdot 10^{4})^{2} - 4 \cdot (9.091 \cdot 10^{9})} = -1.894 \cdot 10^{4} + 9.345 \cdot 10^{4} j$$

$$s_{2} = \frac{-3.788 \cdot 10^{4}}{2} - \frac{1}{2} \cdot \sqrt{(3.788 \cdot 10^{4})^{2} - 4 \cdot (9.091 \cdot 10^{9})} = -1.894 \cdot 10^{4} - 9.345 \cdot 10^{4} j$$

e) What type of response do you expect from this circuit?

The solutions to the characteristic equation are complex so the response will be **underdamped**.

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ECE 2210 Lecture 18 notes p2

Ex. 2 Analysis of the circuit shown yields the characteristic equation below. The switch has been in the open position for a long time and is closed (as shown) at time t = 0. Find the initial and final conditions and write the full expression for i_{t} (t), including all the constants that you find.

$$s^{2} + \left(\frac{1}{C \cdot R_{1}}\right) \cdot s + \left(\frac{1}{L \cdot C}\right) = 0$$

$$\left(\frac{1}{C \cdot R_{1}}\right) = 1 \cdot 10^{4} \cdot \frac{1}{sec} \qquad \left(\frac{1}{L \cdot C}\right) = 2 \cdot 10^{7} \cdot \frac{1}{sec^{2}}$$

$$s^{2} + 10000 \cdot \frac{1}{sec} \cdot s + 2 \cdot 10^{7} \cdot \frac{1}{sec^{2}} = 0$$

$$s_{1} := \left[\frac{-10000}{2} + \frac{1}{2} \cdot \sqrt{(10000)^{2} - 4 \cdot (2 \cdot 10^{7})}\right] \cdot sec^{-1} \qquad s_{2} := \left[\frac{-10000}{2} - \frac{1}{2} \cdot \sqrt{(10000)^{2} - 4 \cdot (2 \cdot 10^{7})}\right] \cdot sec^{-1}$$

$$s_{1} = -2764 \cdot sec^{-1} \qquad s_{2} = -7236 \cdot sec^{-1} \qquad s_{1} \text{ and } s_{2} \text{ are both real and distinct, overdamped}$$

Find the initial conditions:

Before the switch closed, the inductor current was: $\frac{15 \cdot V}{R_1 + R_2} = 30 \cdot mA = i_L(0)$

Before the switch closed, the capacitor voltage was:

When the switch is closed, the inductor is suddenly in parallel with the capacitor, and:

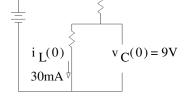
$$v_{L}(0) = v_{C}(0)$$

$$\frac{d}{dt}i_{L}(0) = \frac{1}{L} \cdot v_{L}(0) =$$

$$\frac{1}{L} \cdot 9 \cdot V = 90 \cdot \frac{A}{sec}$$

ECE 2210

 $\frac{R_2}{R_1 + R_2} \cdot (15 \cdot V) = 9 \cdot V = v_C(0)$



 $R_1 = 200 \cdot \Omega$

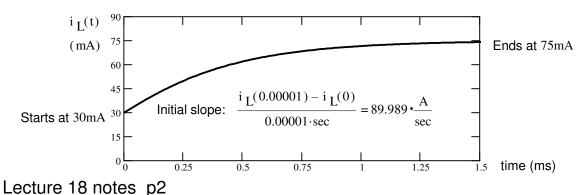
Find the final condition: $i_{L}(\infty) =$ $\frac{15 \cdot V}{R_{1}} = 75 \cdot mA$ $i_{L}(\infty) = 0V$

 $V_{in} = 15 \cdot V$

General solution for the overdamped condition: $i_{L}(t) = i_{L}(\infty) + B \cdot e^{s_{1} \cdot t} + D \cdot e^{s_{2} t}$ Initial conditions: $i_{L}(0) = \frac{15 \cdot V}{R_{1} + R_{2}} = i_{L}(\infty) + B + D$, so $B = i_{L}(0) - i_{L}(\infty) - D = 30 \cdot mA - 75 \cdot mA - D$ $= -45 \cdot mA - D$ $\frac{d}{dt}i_{L}(0) = 90 \cdot \frac{A}{sec} = s_{1} \cdot B + s_{2} \cdot D = s_{1} \cdot (-45 \cdot mA - D) + s_{2} \cdot D = s_{1} \cdot (-45 \cdot mA) - s_{1} \cdot D + s_{2} \cdot D$

solve for D & B:
$$D := \frac{90 \cdot \frac{1}{8 \text{ sec}} - \text{s}_1 \cdot (-45 \cdot \text{mA})}{\frac{-\text{s}_1 + \text{s}_2}{-\text{s}_1 + \text{s}_2}}$$
 $D = 7.69 \cdot \text{mA}$ $B := -45 \cdot \text{mA} - D$ $B = -52.7 \cdot \text{mA}$

Plug numbers back in: $i_{L}(t) = 75 \cdot mA - 52.7 \cdot mA \cdot e^{-2764t} + 7.69 \cdot mA \cdot e^{-7236t}$



Ex. 3

Analysis of the circuit shown yields the characteristic equation and s values below. The switch has been in the closed position for a long time and is opened (as shown) at time t = 0. Find the initial and final conditions and write the full expression for $v_{\rm C}(t)$, including all the constants.

$$0 = s^{2} + \frac{R_{1}}{L} \cdot s + \frac{1}{L \cdot C}$$

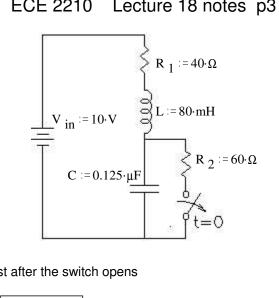
s₁ := $(-250 + 10^{4} \cdot j) \cdot \frac{1}{sec}$, s₂ := $(-250 - 10^{4} \cdot j) \cdot \frac{1}{sec}$

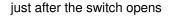
before switch opens

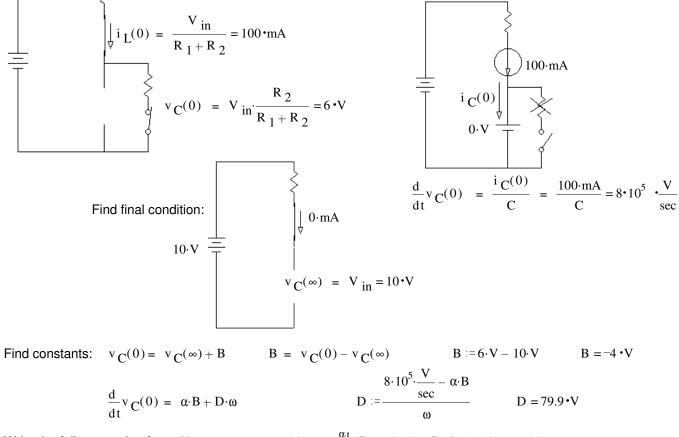
 $\alpha := -250 \cdot \frac{1}{\sec}$

Solution:

Initial conditions:







 $\omega := 10000 \cdot \frac{\text{rad}}{10000}$

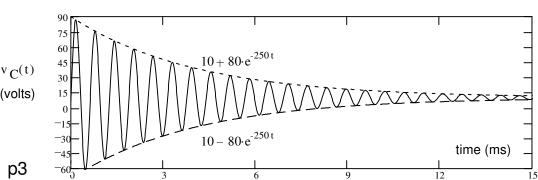
Write the full expression for $v_{\rm C}(t)$, including all the constants that you find.

$$\mathbf{v}_{\mathbf{C}}(t) = \mathbf{e}^{\boldsymbol{\omega} t} \cdot (\mathbf{B} \cdot \cos(\boldsymbol{\omega} \cdot t) + \mathbf{D} \cdot \sin(\boldsymbol{\omega} \cdot t)) + \mathbf{v}_{\mathbf{C}}(\infty)$$

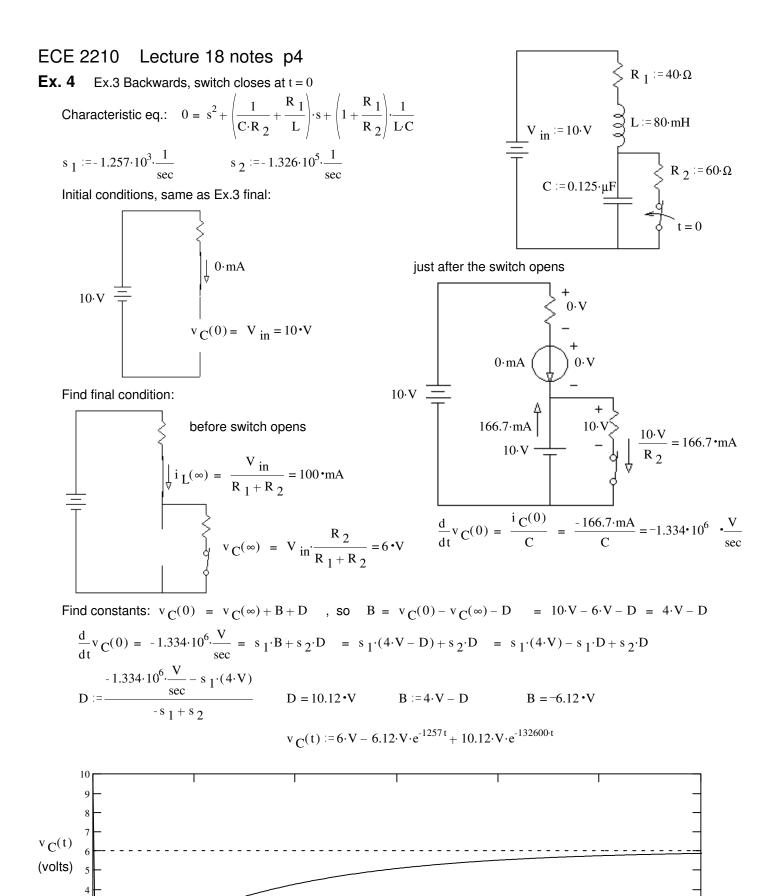
$$\mathbf{v}_{\mathbf{C}}(t) := \mathbf{e}^{-250t} \cdot \left(-4 \cdot \mathbf{V} \cdot \cos\left(10^{4} \cdot t\right) + 79.9 \cdot \mathbf{V} \cdot \sin\left(10^{4} \cdot t\right)\right) + 10 \cdot \mathbf{V}$$

$$\sqrt{D^2 + B^2} = 80 \cdot V$$
 (volt

ECE 2210 Lecture 18 notes p3



ECE 2210 Lecture 18 notes p3





15

2

2.5

0.5

Ex. 5 Analysis of a circuit (not pictured) yields the characteristic equation below.

$$0 = s^{2} + 400 \cdot s + 400000$$
 $R := 80 \cdot \Omega$ $L := 20 \cdot mH$ $C := 2 \cdot \mu F$

Further analysis yields the followiing initial and final conditions:

$$i_{L}(0) = 120 \cdot \text{mA} \qquad v_{L}(0) = -3 \cdot \text{V} \qquad v_{C}(0) = 7 \cdot \text{V} \qquad i_{C}(0) = -80 \cdot \text{mA}$$
$$i_{L}(\infty) = 800 \cdot \text{mA} \qquad v_{L}(\infty) = 0 \cdot \text{V} \qquad v_{C}(\infty) = 12 \cdot \text{V} \qquad i_{C}(\infty) = 0 \cdot \text{mA}$$

Write the full expression for $i_L(t)$, including all the constants that you find. $i_L(t) = ?$ Solution:

$$\frac{400}{2} = 200 \qquad \frac{\sqrt{400^2 - 4.400000}}{2} = 600j$$

s₁:=(-200+600·j)· $\frac{1}{\sec}$ and s₂:=(-200-600·j)· $\frac{1}{\sec}$
 $\alpha := \operatorname{Re}(s_1) \qquad \alpha = -200 \cdot \sec^{-1} \qquad \omega := \operatorname{Im}(s_1) \qquad \omega = 600 \cdot \sec^{-1}$

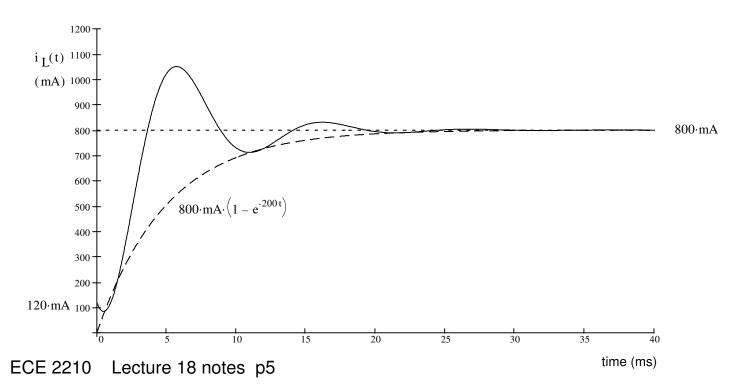
Initial slope: $\frac{d}{dt}i_{L}(0) = \frac{v_{L}(0)}{L} = \frac{-3 \cdot V}{L} = -150 \cdot \frac{A}{sec}$

General solution for the underdamped condition: $i_{L}(t) = i_{L}(\infty) + e^{\alpha t} \cdot (B \cdot \cos(\omega \cdot t) + D \cdot \sin(\omega \cdot t))$ Find constants: $i_{L}(0) = i_{L}(\infty) + B$ $B = i_{L}(0) - i_{L}(\infty)$ $B := 120 \cdot mA - 800 \cdot mA$ $B = -680 \cdot mA$

$$\frac{d}{dt}i_{L}(0) = \alpha \cdot B + D \cdot \omega \qquad D := \frac{-150 \cdot \frac{A}{sec} - \alpha \cdot B}{\omega} \qquad D = -476.667 \cdot mA$$

Write the full expression for $i_1(t)$, including all the constants that you find.

$$i_{L}(t) = 800 \cdot mA + e^{-200 t} \cdot (-680 \cdot mA \cdot \cos(600 \cdot t) - 477 \cdot mA \cdot \sin(600 \cdot t))$$



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Ex. 6

Analysis of a circuit (not pictured) yields the characteristic equation below.

 $0 = s^2 + 800 \cdot s + 160000$ $L := 350 \cdot mH$ $C := 20 \cdot \mu F$ $V_{in} := 12 \cdot V$ $\mathbf{R} := 60 \cdot \Omega$

Further analysis yields the following initial and final conditions:

 $i_{I}(0) = 30 \cdot mA$ $v_{L}(0) = -7 \cdot V$ $v_{C}(0) = 5 \cdot V$ $i_{C}(0) = 70 \cdot mA$ $i_{I}(\infty) = 90 \cdot mA$ $v_{I}(\infty) = 0 \cdot V$ $v_{C}(\infty) = 12 \cdot V$ $i_{C}(\infty) = 0 \cdot mA$

Write the full expression for $i_{I}(t)$, including all the constants that you find. $i_{I}(t) = ?$

Include units in your answer

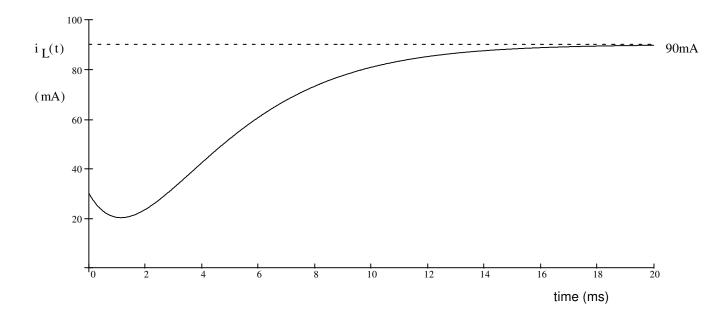
Solution:

 $\frac{-800 + \sqrt{800^2 - 4 \cdot 160000}}{2} = -400 \qquad \text{s}_1 := -400 \cdot \frac{1}{\text{sec}} \qquad \text{s}_2 := -400 \cdot \frac{1}{\text{sec}} \qquad \text{s}_1 \text{ and } \text{s}_2 \text{ are the same,}$ critically damped $\frac{\mathrm{d}}{\mathrm{dt}} i_{\mathrm{L}}(0) = \frac{v_{\mathrm{L}}(0)}{\mathrm{L}} = \frac{-7 \cdot \mathrm{V}}{\mathrm{L}} = -20 \cdot \frac{\mathrm{A}}{\mathrm{sec}}$ Initial slope:

General solution for the critically damped condition: $i_{I}(t) = i_{I}(\infty) + B \cdot e^{s_{1}t} + D \cdot t \cdot e^{s_{2}t}$

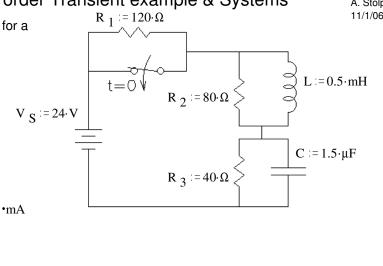
 $\mathbf{B} = \mathbf{i}_{\mathbf{L}}(0) - \mathbf{i}_{\mathbf{L}}(\infty)$ Find constants: $i_{I}(0) = i_{I}(\infty) + B$ $B := 30 \cdot mA - 90 \cdot mA$ $B = -60 \cdot mA$ $\frac{d}{dt}i_{L}(0) = B\cdot s + D \qquad D := -20 \cdot \frac{A}{sec} - B\cdot s_{1} \qquad D = -44 \cdot \frac{A}{sec}$

including all the constants that you find. $i_{L}(t) := 90 \cdot mA - 60 \cdot mA \cdot e^{-\frac{400}{\sec}t} - 44 \cdot \underline{A} \cdot t \cdot e^{-\frac{400}{\sec}t}$

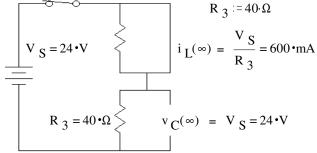


ECE 2210 Lecture 19 notes Second order Transient example & Systems

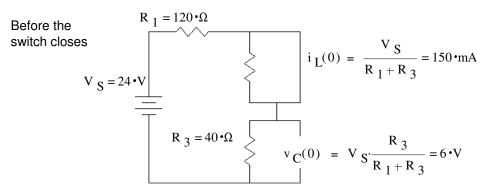
Ex 1. The switch at right has been in the open position for a long time and is closed (as shown) at time t = 0.



a) What are the final conditions of i_L and the v_C ?



b) Find the initial condition and initial slope of i_L so that you could find all the constants in $i_L(t)$. Don't find $i_{I}(t)$ or it's constants, just the initial conditions.



Just after the switch closes:

$$V_{S} = 24 \cdot V$$

$$R_{2} = 80 \cdot \Omega$$

$$R_{2} = 80 \cdot \Omega$$

$$R_{2} = 80 \cdot \Omega$$

$$V_{L}(0) = 24 \cdot V - 6 \cdot V = 18 \cdot V$$

$$\frac{d}{dt} i_{L}(0) = \frac{18 \cdot V}{L} = 36000 \cdot \frac{A}{sec}$$

$$\frac{6 \cdot V}{40 \cdot \Omega} = 150 \cdot mA$$

$$V_{L}(0) = 150 \cdot mA + \frac{18 \cdot V}{R_{2}} - \frac{6 \cdot V}{R_{3}} = 225 \cdot mA$$

$$R_{3} = 40 \cdot \Omega$$

$$R_{3} = 40 \cdot \Omega$$

c) Find the initial condition and initial slope of $v_{\rm C}$ so that you could find all the constants in $v_{\rm C}(t)$. Don't find $v_{C}(t)$ or it's constants, just the initial conditions.

$$v_{C}(0) = V_{S} \cdot \frac{R_{3}}{R_{1} + R_{3}} = 6 \cdot V$$
 $\frac{d}{di} v_{C}(0) = \frac{225 \cdot mA}{C} = 150000 \cdot \frac{V}{sec}$

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Systems

Now that we' ve developed the concept of the transfer function, we can now develop system block diagrams using blocks which contain transfer functions.

Consider a circuit:

This could be represented in as a block operator:

0-

$$\mathbf{V}_{\mathbf{in}}(s)$$
 \longrightarrow $\frac{\mathbf{L}_2 \cdot \mathbf{s} + \mathbf{R}}{(\mathbf{L}_1 + \mathbf{L}_2) \cdot \mathbf{s} + \mathbf{R}}$ \longrightarrow $\mathbf{V}_{\mathbf{0}}(s) = \mathbf{V}_{\mathbf{in}}(s) \cdot \mathbf{H}(s)$

Transfer functions can be written for all kinds of devices and systems, not just electric circuits and the input and output do not have to be similar. For instance, the potentiometers used to measure angular position in the lab servo can be represented like this:

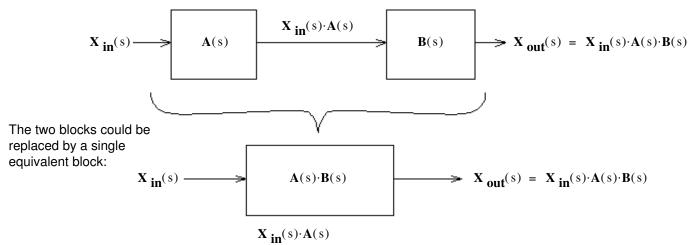
$$\boldsymbol{\theta}_{in}(s) \longrightarrow Kp = 0.7 \cdot \frac{V}{rad} = 0.012 \cdot \frac{V}{deg} \longrightarrow V_{out}(s) = K_p \cdot \boldsymbol{\theta}_{in}(s)$$

In general:

$$\mathbf{H}(s) = \frac{\mathbf{X}_{out}(s)}{\mathbf{X}_{in}(s)} \qquad \qquad \mathbf{X}_{in}(s) \longrightarrow \qquad \mathbf{H}(s) \qquad \implies \mathbf{X}_{out}(s) = \mathbf{X}_{in}(s) \cdot \mathbf{H}(s)$$

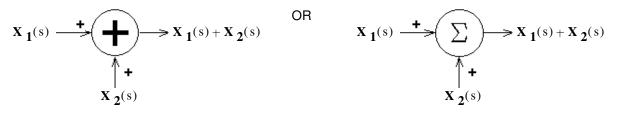
 X_{in} and X_{out} could be anything from small electrical signals to powerful mechanical motions or forces.

Two blocks with transfer functions A(s) and B(s) in a row would look like this:



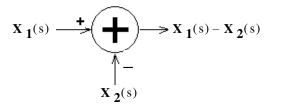
ECE 2210 Lecture 19 notes p3

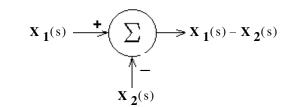
Summer blocks can be used to add signals:



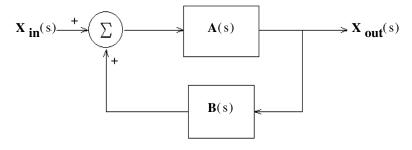
OR

or subtract signals:



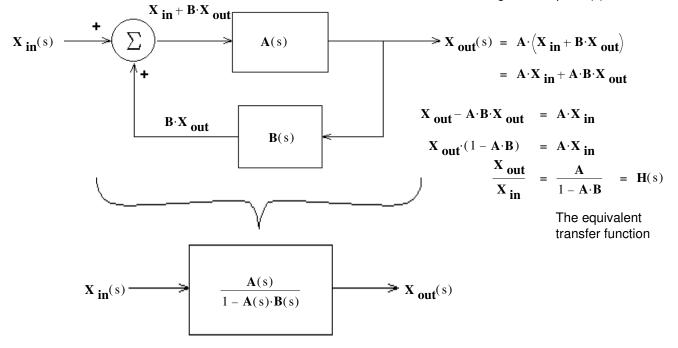


A feedback loop system is particularly interesting and useful:



The entire loop can be replaced by a single equivalent block:

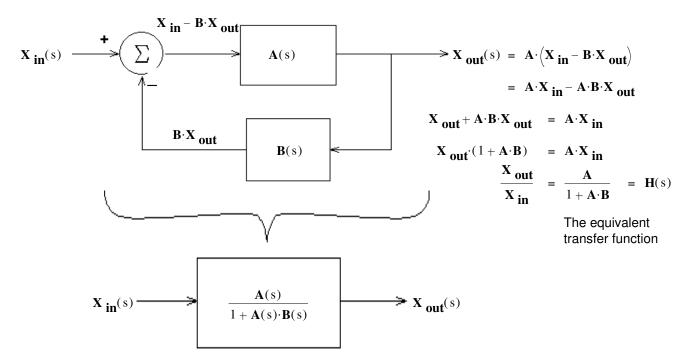
Note that I' ve begun to drop the (s)



 $A(s) \cdot B(s)$ is called the "loop gain" or "open loop gain"

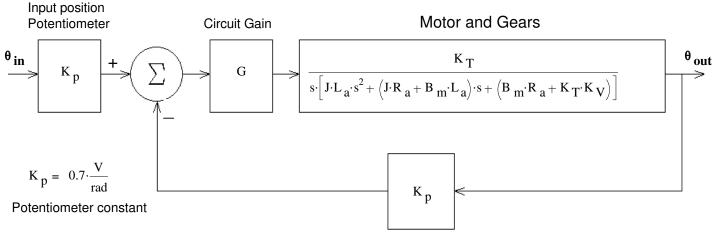
ECE 2210 Lecture 19 notes p4

Negative feedback is more common and is used as a control system:



This is called a "closed loop" system, whereas a a system without feedback is called "open loop". The term "open loop" is often used to describe a system that is out of control.

The servo used in our lab can be represented by:



Motor Position Potentiometer

$$\mathbf{H}(s) = \frac{\boldsymbol{\theta}_{out}(s)}{\boldsymbol{\theta}_{in}(s)} = \frac{\mathbf{G} \cdot \mathbf{K}_{T} \cdot \mathbf{K}_{p}}{\mathbf{s} \cdot \left[\mathbf{J} \cdot \mathbf{L}_{a} \cdot \mathbf{s}^{2} + \left(\mathbf{J} \cdot \mathbf{R}_{a} + \mathbf{B}_{m} \cdot \mathbf{L}_{a} \right) \cdot \mathbf{s} + \left(\mathbf{B}_{m} \cdot \mathbf{R}_{a} + \mathbf{K}_{T} \cdot \mathbf{K}_{V} \right) \right] + \mathbf{K}_{p} \cdot \mathbf{G} \cdot \mathbf{K}_{T}}$$

See the appendix to lab 9 for the complete analysis

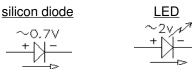
ECE 2210 Lecture 19 notes p4

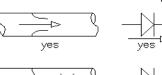
Diodes Notes

ECE 2210

A. Stolp 4/8/03, 2/27/07

Diodes are basically electrical check valves. They allow current to flow freely in one direction, but not the other. Check valves require a small forward pressure to open the valve. Similarly, a diode requires a small forward voltage (bias) to "turn on". This is called the forward voltage drop. There are many different types of diodes, but the two that you are most likely to see are silicon diodes and light-emitting diodes (LEDs). These two have forward voltage drops of about 0.7V and 2V respectively.







Mechanical check valve

Diode

cathode

The electrical symbol for a diode looks like an arrow which shows the forward current direction and a small perpendicular line. The two sides of a diode are called the "anode" and the "cathode" (these names come from vacuum tubes). Most small diodes come in cylindrical packages with a band on one end that corresponds to the

small perpendicular line, and shows the polarity, see the picture. Normal diodes are rated by the average forward current and the peak reverse voltage that they can handle. Diodes with significant current ratings are known as "rectifier" or "power" diodes. (Rectification is the process of making AC into DC.) Big power diodes come in a variety of packages designed to be attached to heat sinks. Small diodes are known as "signal" diodes because they're designed to handle small signals rather than power.

Diodes are nonlinear parts

So far in this class we've only worked with linear parts. The diode is definitely NOT linear, but it can be modeled as linear in its two regions of operation. If it's forward biased, it can be replaced by battery of 0.7V (2V for LEDs) which opposes the current flow. Otherwise it can be replaced by an open circuit. These are "models" of the actual diode. If you're not sure of the diode's state in a circuit, guess. Then replace it with the appropriate model and analyze the circuit. If you guessed the open, then the voltage across the diode model should come out less than +0.7V (2V for LEDs). If you guessed the battery, then the current through the diode model should come out in the direction of the diode's arrow. If your guess doesn't work out right, then you'll have to try the other option. In a circuit with multiple diodes (say "n" diodes), there will be 2^n possible states, all of which may have to be tried until you find the right one. Try to guess right the first time.

1 Assume the diode is operating in one of the linear regions (make an educated guess).

- 2 Analyze circuit with a linear model od the diode.
- 3 Check to see if the diode was really in the assumed region.
- 4 Repeat if necessary.

ECE 2210 Diode Notes p1

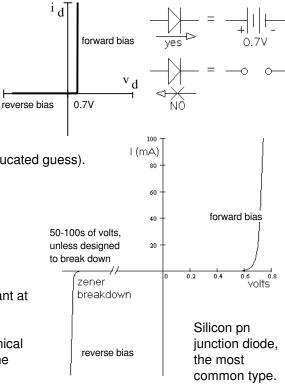
Actual diode curve

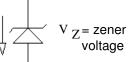
The characteristics of real diodes are actually more complicated than the constant-voltage-drop model. The forward voltage drop is not quite constant at any current and the diode "leaks" a little current when the voltage is in the reverse direction. If the reverse voltage is large enough, the diode will "breakdown" and let lots of current flow in the reverse direction. A mechanical check valve will show similar characteristics. Breakdown does not harm the diode as long as it isn't overheated.

Zener diodes are special diodes designed to operate in the reverse breakdown region. Since the reverse breakdown voltage across a diode is very constant for a large range of current, it can be used as a voltage reference or regulator. Zener diodes are also used for over-voltage protection. In the forward direction zeners work the same as regular diodes.

Constant-voltage-drop model

This is the most common diode model and is the only one we'll use in this class. It gives quite accurate results in most cases.

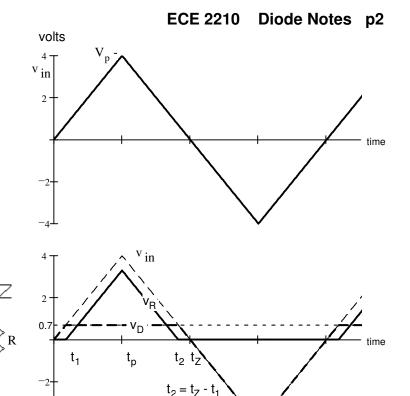




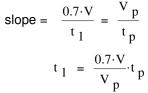
I recommend that you try some of the DC analysis in the Diode Circuit Examples handout before you proceed here.

Diodes in AC Circuits

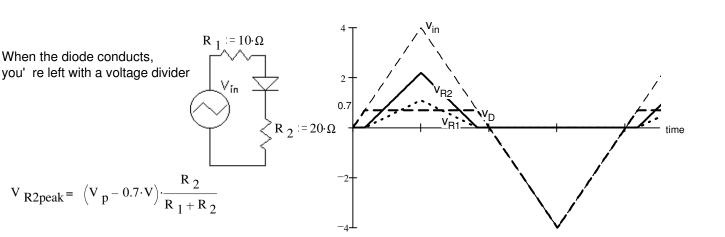
Diodes are often used to manipulate AC waveforms. We' II start with some triangular waveforms to get the general idea.



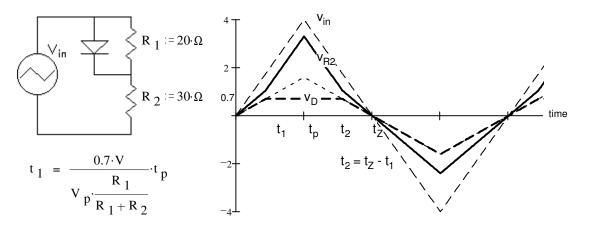
Diode doesn't conduct until i_n reaches 0.7V, so 0.7V is a dividing line between the two models of the diode.



Vin



Sometimes it's helpful to figure out what the voltage across the diode would be if it never conducted (light dotted line).

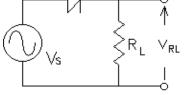


Rectifier Circuits & Power Supplies

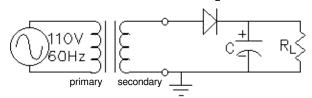
Half-wave rectification

What if the input is a sine wave?

 V_{RL} is now DC, although a bit bumpy. Some things are better if they' re bumpy, but not roads and not DC voltages.



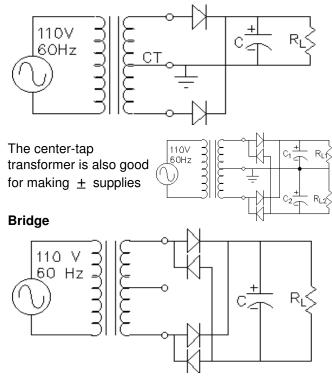
Rectification is the process of making DC from AC. Usually the AC is derived from the AC wall outlet (often through a transformer) and the DC is needed for electronic circuitry modeled by $R_{\rm I}$ here.



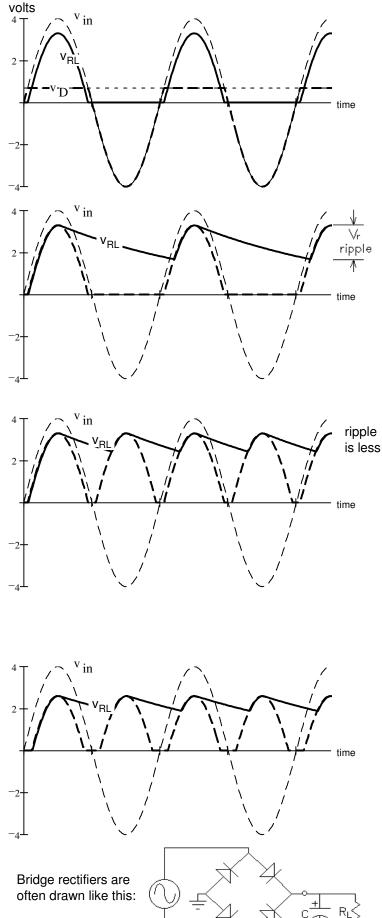
A "filter" capacitor (usually a big electrolytic) helps smooth out the bumps, although it sure looks like we could a bit bigger one here. The remaining bumpiness is called "ripple", V_r is peak-to-peak ripple

Full-wave rectification

The "center tap" in the secondary of this transformer makes it easy to get full-wave rectification.



A "bridge" circuit or "bridge rectifier" can give you full-wave rectification without a center-tap transformer, but now you loose another "diode drop"



ECE 2210 Diode Notes p3

Other Useful Diode Circuits

Simple limiter circuits can be made with diodes.

A common input protection to protect circuit from excessive input voltages such as static electricity.

The input to the box marked "sensitive circuit" can' t get higher than the positive supply + 0.7V or lower than the negative supply - 0.7V.

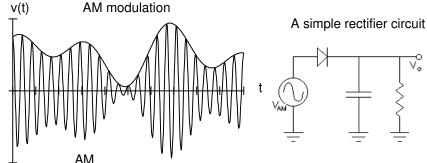
Put a fuse in the V_{in} line and the diodes can make it blow, providing what's known as "crowbar" protection.

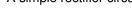
Another example of crowbar protection:

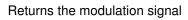
If the input voltage goes above 16 V. the fuse will blow, protecting the circuitry.

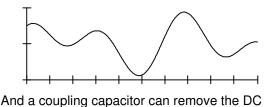
Or, If the input voltage is hooked up backwards the fuse will blow, protecting the circuitry.

AM detector









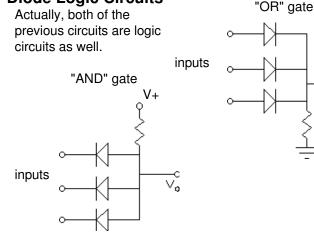
Battery Isolator

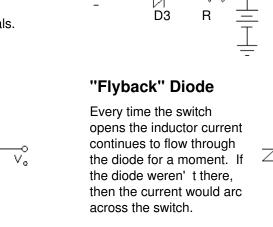
Like you might find in an RV. One alternator is used to charge two batteries. When the alternator is not charging, the batteries, the circuits they are hooked to should be isolated from one another. If not, then one battery might discharge through the second, especially if second is bad. Also, you wouldn' t want the accessories in the RV to drain the starting battery, or your uncle George from South Dakota might never leave your driveway.

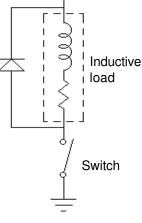
Battery Backup Power

Normally the power supply powers the load through D1. However, if it fails, the load will remain powered by the battery through D2. Finally, D3 and R may be added to keep the battery charged when the power supply is working. These sorts of circuits are popular in hospitals.

Diode Logic Circuits



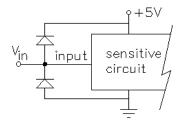


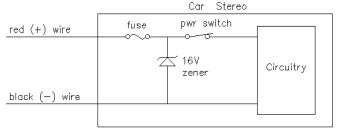


+ V

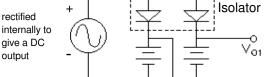
Vo2

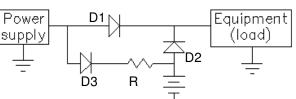
ECE 2210 Diode Notes p4





Alternator rectified internally to





ECE 2210 Diode Notes p4

Basic diode circuit analysis

1 Make an educated guess about each diode's state.

2 Replace each diode with the appropriate model:

3 Redraw and analyze circuit.

4 Make sure that each diode is actually in the state you assumed:

Note: 0.7V is for silicon junction diodes & will be different for other types. (2V for LED)

conducting

-1>

current

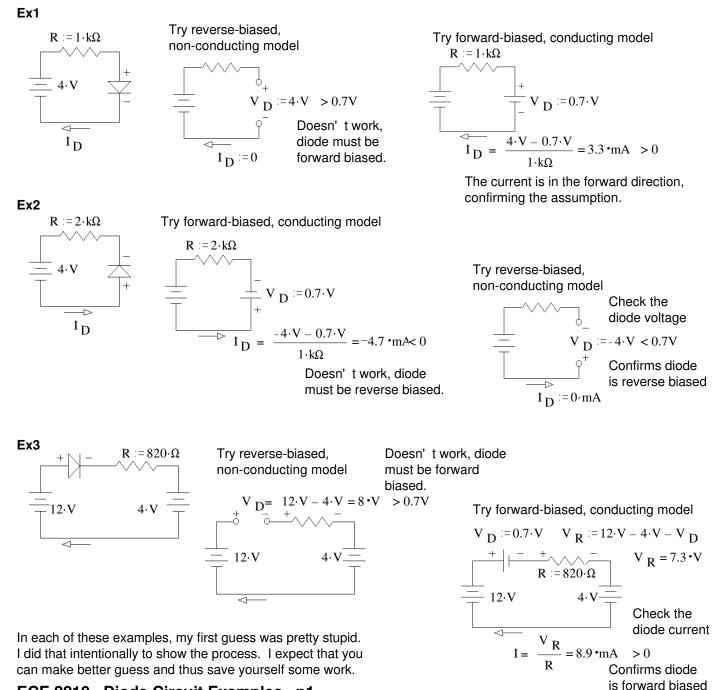
not conducting

 $V_{d} < 0.7V$

Check

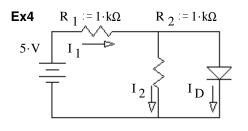
or

If any of your guesses don't work out right, then you' II have to start over with new guesses. In a circuit with n diodes there will be 2ⁿ possible states, all of which may have to be tried until you find the right one. Try to guess right the first time.



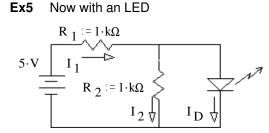
ECE 2210 Diode Circuit Examples p1





Assume diode conducts: Analyze $V_{R2} = V_D$ $I_2 = \frac{V_{R2}}{R_2}$ $I_2 = 0.7 \cdot MA$ Assume diode conducts: Analyze $V_{R1} = 5 \cdot V - V_D$ $V_{R1} = 4.3 \cdot V$ $I_1 := \frac{V_{R1}}{R_1}$ $I_1 = 4.3 \cdot mA$ We assumed conducting (assuming a voltage), so check the current.

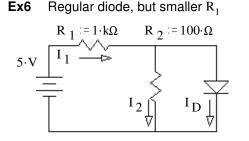
 $I_D = I_1 - I_2$ $I_D = 3.6 \cdot mA > 0$, so assumption was correct



Assume diode conducts $\overrightarrow{||}$ V_D := 2·V = V_{R2} Analyze $V_{R2} = V_D$ $I_2 = \frac{V_{R2}}{R_2}$ $I_2 = 2 \cdot mA$ $V_{R1} := 5 \cdot V - V_D$ $V_{R1} = 3 \cdot V$ $I_1 := \frac{V_{R1}}{R_1}$ $I_1 = 3 \cdot mA$

We assumed conducting (assuming a voltage), so check the current.

 $I_D := I_1 - I_2$ $I_D = 1 \cdot mA > 0$, so assumption was correct, but the current is probably too small to create noticeable light

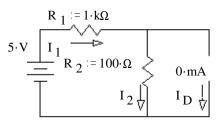


 $\begin{cases} \downarrow & \downarrow \\ \downarrow & \downarrow \\ \downarrow & \downarrow \\ \end{cases} V_{D} := 0.7 \cdot V = V_{R2}$ Analyze $V_{R2} := V_D$ $I_2 := \frac{V_{R2}}{R_2}$ $I_2 = 7 \cdot mA$ $V_{R1} = 5 \cdot V - V_D$ $V_{R1} = 4.3 \cdot V$ $I_1 := \frac{V_{R1}}{R_1}$ $I_1 = 4.3 \cdot mA$

We assumed conducting (assuming a voltage), so check the current.

$$I_D := I_1 - I_2$$
 $I_D = -2.7 \cdot mA < 0$, so assumption was **WRONG** !

Assume diode does not conduct



 $I_{D} = 0 \cdot mA$

Assume diode conducts

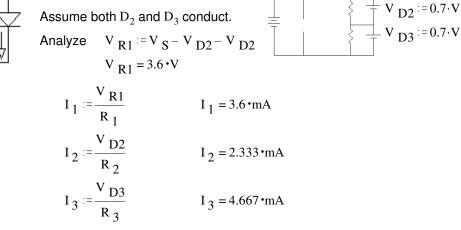
Analyze
$$I_1 := \frac{5 \cdot V}{R_1 + R_2}$$
 $I_2 := I_1$

We assumed not conducting (assuming a current), so check the voltage.

 $V_{R2} = I_2 \cdot R_2$ $V_{R2} = 0.455 \cdot V < 0.7V$, so assumption was correct Actually, this final check isn' t necessary, since first assumption didn' t work, so this one had to.

$$\begin{array}{c|c} R_1 := 1 \cdot k\Omega \\ \hline I_1 & \\ \hline V_S := 5 \cdot V \\ \hline I_{D1} & \\ \hline I_{D1} & \\ \hline I_{D1} & \\ \hline I_{3} & \\ \hline I_{D3} \\ \hline \end{array}$$

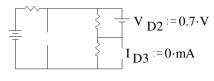
You can safety say that diode D_1 doesn't conduct without rechecking later because no supply is even trying to make current flow through that diode the right way.



We assumed $D_1 \& D_2$ conduct (assumed a voltage), so check currents.

$I_{D2} = I_1 - I_2$	$I_{D2} = 1.267 \cdot mA > 0$, so assumption OK
$I_{D3} := I_1 - I_3$	I $_{D3}$ = -1.067 ·mA < 0, so assumption wrong

Assume D₂ conducts and D₃ doesn' t.



Analyze
$$I_2 := \frac{V_{D2}}{R_2}$$
 $I_2 = 2.333 \cdot mA$
 $I_1 := \frac{V_S - V_{D2}}{R_1 + R_3}$ $I_1 = 3.739 \cdot mA$

 $V_{R3} = I_1 \cdot R_3$

Assumed D_2 conducts, so check D_2 current. $I_{D2} = I_1 - I_2$ $I_{D2} = 1.406 \cdot mA > 0$, so assumption OK

Assumed D_3 doesn't conduct, so checl D_3 voltage.

Once you find one case that works, you don't have to try any others.

Zener Diodes

Zener diodes are special diodes designed to operate in the reverse breakdown region. Since the reverse breakdown voltage across the diode is very constant for a large range of current, it can be used as a voltage reference or regulator. Diodes are not harmed by operating in this region as long as their power rating isn't exceeded. In the forward direction zeners work the same as regular diodes.

Now there are three possible regions of operation:

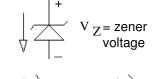
Same basic diode circuit analysis

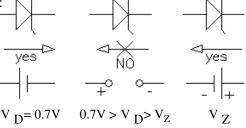
1 Make an educated guess about each diode's state.

2 Replace each diode with the appropriate model:

3 Redraw and analyze circuit.

4 Make sure that each diode is actually in the state you assumed:



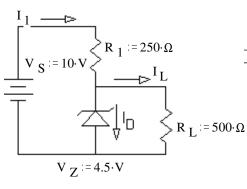


 $V_{R3} = 0.561 \cdot V < 0.7V$, so OK

ECE 2210 Diode Circuit Examples p3

Zener Diode Circuit Examples

Ex1 Typical shunt regulator circuit:



ECE 2210 Diode Circuit Examples p4

$$V_{Z} = 4.5 \cdot V$$
 $R_{L} = 500 \cdot \Omega$

Assume conducting in breakdown region

$$| I_{D} \rangle = V_{D} = V_{Z}$$

$$I_{L} = \frac{V_{Z}}{R_{L}}$$

$$I_{L} = 9 \cdot mA$$

$$I_{1} = \frac{V_{S} - V_{Z}}{R_{1}}$$

$$I_{1} = 22 \cdot mA$$

Assumed a conducting region, so check the current to see if the current flows in the direction shown.

$$I_D := I_1 - I_L$$
 $I_D = 13 \cdot mA > 0$, so assumption OK

Ex2 What if R_L is smaller? $R_L = 150 \cdot \Omega$

Assume conducting in breakdown region

$$I_1 := \frac{V_S - V_Z}{R_1}$$
 $I_1 = 22 \cdot mA$

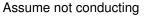
 $V_D := V_Z$ $I_L := \frac{V_Z}{R_L}$

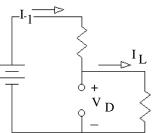
 $^{\mathrm{I}}\mathrm{L}$

 I_1

$$I_L = 30 \cdot mA$$

 $I_D := I_1 - I_L \qquad \qquad I_D = -8 \cdot mA \quad <0, \text{ so assumption is WRONG !}$ Circuit "falls out of regulation"





$$I_{L} = I_{1} := \frac{V_{S}}{R_{1} + R_{L}}$$
 $I_{1} = 25 \cdot mA$

Assumed a non-conducting region, so check the voltage to see if it's in the right range.

$$V_D := \frac{R_L}{R_1 + R_L} \cdot V_S$$
 $V_D = 3.75 \cdot V < V_Z = 4.5 \cdot V$
so this assumption is OK

Ex3 What if V_S is smaller instead of R_L ? $V_S = 6 \cdot V$ $R_L = 500 \cdot \Omega$

Assume conducting in breakdown region $V_D :=$

$$I_1 := \frac{V_S - V_Z}{R_1}$$
 $I_1 = 6$

$$V_{D} := V_{Z}$$

$$I_L := \frac{V_Z}{R_L}$$
 $I_L = 9 \cdot mA$

•mA $I_D := I_1 - I_L$ $I_D = -3 \cdot mA < 0$, so assumption is **WRONG !** Circuit "falls out of regulation"

Assume not conducting $I_L = I_1 := \frac{V_S}{R_1 + R_L}$ $I_1 = 8 \cdot mA$

Assumed a non-conducting region, so check
the voltage to see if it's in the right range.
$$V_{D} := \frac{R_{L}}{R_{1} + R_{L}} \cdot V_{S}$$
$$V_{D} = 4 \cdot V < V_{Z} = 4.5 \cdot V$$

so this assumption is OK

ECE 2210 Diode Circuit Examples p4

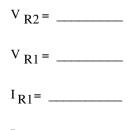
Exam-type Diode Circuit Examples

On an exam, I usually tell you what assumptions to make about the diodes, then you can show that you know how to analyze the circuit and test those assumptions. Since everyone starts with the same assumptions, everyone should do the same work.

In the circuit shown, use the constant-voltage-drop model for the silicon diode.

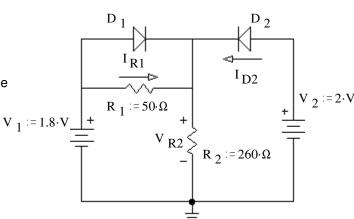
a) Assume that diode D_1 does NOT conduct. Assume that diode D_2 does conduct.

Find V_{R2} , V_{R1} , I_{R1} , & I_{D2} , based on these assumptions. Stick with these assumptions even if your answers come out absurd. Hint: think in nodal voltages.

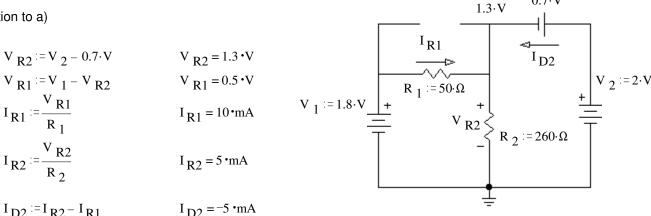




Solution to a)



0.7·V



yes

- b) Based on your numbers above, does it look like the assumption about D₁ was correct? yes no (circle one) How do you know? (Specifically show a value which is or is not within a correct range.)
- c) Based on your numbers above, does it look like the assumption about D₂ was correct? yes no (circle one) How do you know?

no
$$I_{D2} = -5 \cdot mA < 0$$

d) Based on your answers to b) and c), which (if any) of the following was not correctly calculated in part a.

$$V_{R2}$$
 V_{R1} I_{R1} I_{D2}

(circle any number of answers)

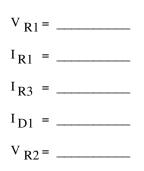
Circle all in this case

 $V_{D1} = V_{R1} = 0.5 \cdot V < 0.7 V$

Assume that diode D_1 is conducting and that diode D_2 is not conducting.

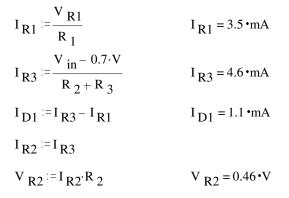
a) Find V_{R1} , I_{R1} , I_{R3} , I_{D1} , V_{R2} based on these assumptions.

Do not recalculate if you find the assumptions are wrong.

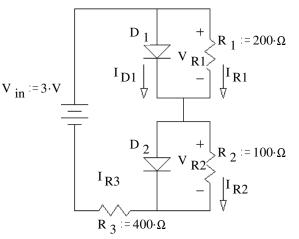


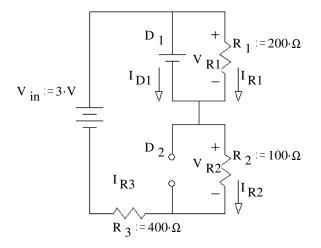
Solution:





b) Was the assumption about D1 correct?





(circle one) yes

How do you know? (Specifically show a value which is or is not within a correct range.)

yes
$$I_{R2} = 4.6 \cdot mA > 0$$

no

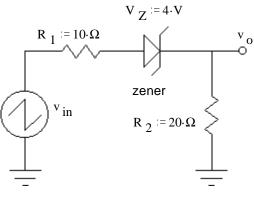
c) Was the assumption about D₂ correct? yes no How do you know? ves $V_{D2} = V_{R2} = 0.46 \cdot V < 0.7V$

d) Based on your answers to b) and c), which (if any) of the following was not correctly calculated in part a.

V R1 I R1 I R3 I D2 V R2 (circle any number of answers) Circle none in this case

A voltage waveform (dotted line) is applied to the circuit shown. <u>Accurately</u> draw the output waveform (v_0) you expect to see. Label important times <u>and</u> voltage levels.

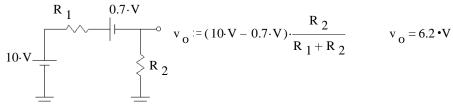
If diode doesn't conduct: v_0 :=



Positive half

Diode conducts at: $0.7 \cdot V$ input at time: $\frac{0.7}{10}$ Maximum:

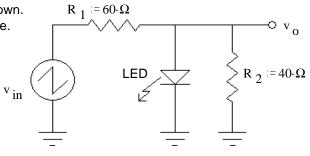
at time: $\frac{0.7 \cdot V}{10 \cdot V} \cdot 10 \cdot ms = 0.7 \cdot ms$



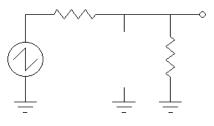
Negative half

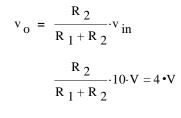
at time: $20 \cdot \text{ms} - \frac{4 \cdot \text{V}}{10 \cdot \text{V}} \cdot 10 \cdot \text{ms} = 16 \cdot \text{ms}$ Diode conducts at: -4.V input Maximum: $\begin{array}{c} & & \\ & &$ R ₁ 10·V_ 12 v_o 10 8 in/ 6.2V 6 4 16ms 12 18 24 ms 0.7ms -12

A voltage waveform (dotted line) is applied to the circuit shown. <u>Accurately</u> draw the output waveform (v_o) you expect to see. Label important times and voltage levels.



If diode doesn't conduct:

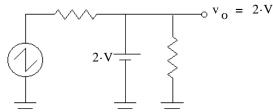


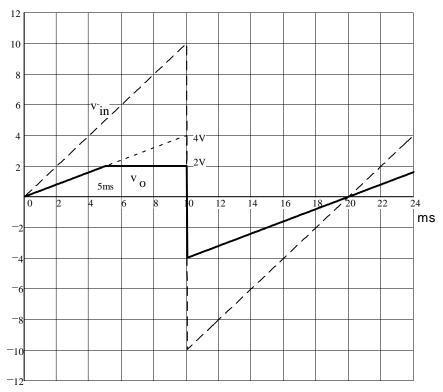


When:

 $v_{in} := \frac{R_1 + R_2}{R_2} \cdot 2 \cdot V$ $v_{in} = 5 \cdot V$ at: $5 \cdot ms$ Diode begins to conduct

When diode conducts:





Diode Physics (The simple version) FYI Only, You don't need to know this

EE 2210

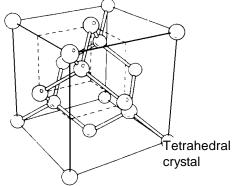
A.Stolp 3/22/01, rev, 2/25/16

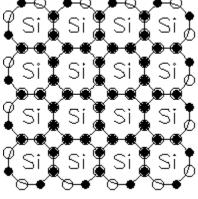
Silicon atoms

Silicon atoms each have 4 valence electrons (electrons in their outermost shell). That leaves 4 spaces in the outer shell of 8. This makes silicon a very reactive chemical, like carbon, which has the same valence configuration.



Each atom covalently bonds with four neighboring atoms to form a tetrahedral crystal, which we'll represent in 2D.

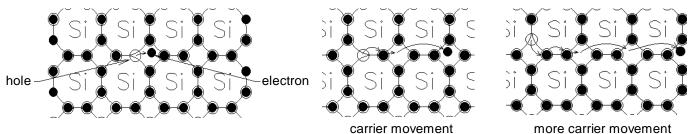




2-dimensional representation

In the pure, "intrinsic" crystal, practically all the electrons are used in bonds and all the spaces are filled, which leaves almost no electrons free to move and thus no way to make current flow.

By the effects of heat, light and/or large electric fields, a few electrons do break free of the bonds and become "free" carriers. That is, they're free to move about crystal and "carry" an electrical current.



Interestingly, the space that was vacated by the electron also acts like a carrier. This pseudo-carrier is called a "hole" and it acts like a positively charged carrier.

Unless there's a lot of heat or light, the intrinsic silicon is still a very bad conductor. Silicon is considered a semiconductor.

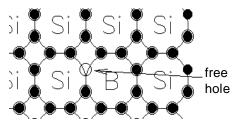
Doping

p-type

Some atoms, like boron and aluminum naturally have 3 valence electrons in their outer shells.



If you replace some of the silicon atoms in a crystal with boron there won't be quite enough electrons to fill the crystalline bond structure and unfilled spaces will act just like free holes. This "doped" silicon crystal is now called an p-type semiconductor. The p refers to the "extra" "positive" carriers.



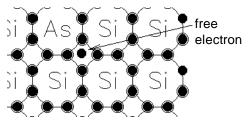
Diode Physics (The simple version)

n-type

Some atoms, like arsenic and phosphorus naturally have 5 valence electrons in their outer shells.



If you replace some of the silicon atoms in a crystal with arsenic the 5th electron doesn't fit into the crystalline bond structure and is therefore free to roam about and be a carrier. This "doped" silicon crystal is now called an n-type semiconductor. The n refers to the "extra" negative carriers.





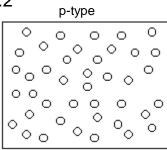
Diode Physics (The simple version) p.2

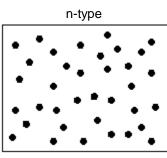
It turns out that the free carriers are the most important things in the semiconductor crystals, so we can simplify the drawings to show only these free carriers.

PN Junction

When a p-type semiconductor is created next to an n-type, some of the free electrons from the n side will cross over and fill some of the free holes on the p side. This makes the p side negatively charged and leaves the n side positively charged. When the voltage across the junction reaches about 0.7 V the electrons find it too difficult to move against the charge and the process stops.

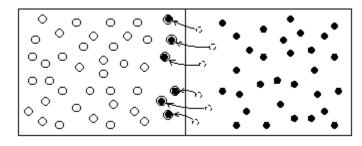
A region near the junction is now depleted of carriers and (surprise) is called the depletion region.

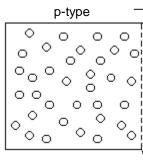


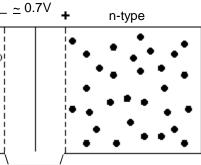


circles represent free holes

dots represent free electrons

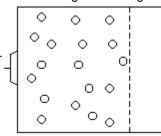




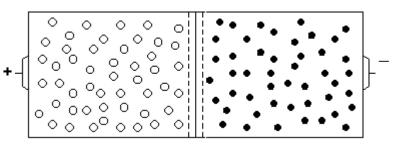


depletion region

"positive" holes move toward the negative voltage negative electrons move toward the positive voltage



With reverse bias the depletion region gets bigger



With forward bias the depletion region gets smaller and eventually (at about 0.7V) conducts freely.



Reverse bias

This pn junction is now a diode. If you place an external voltage across the diode in the reverse bias direction, the depletion region gets bigger and no current flows.

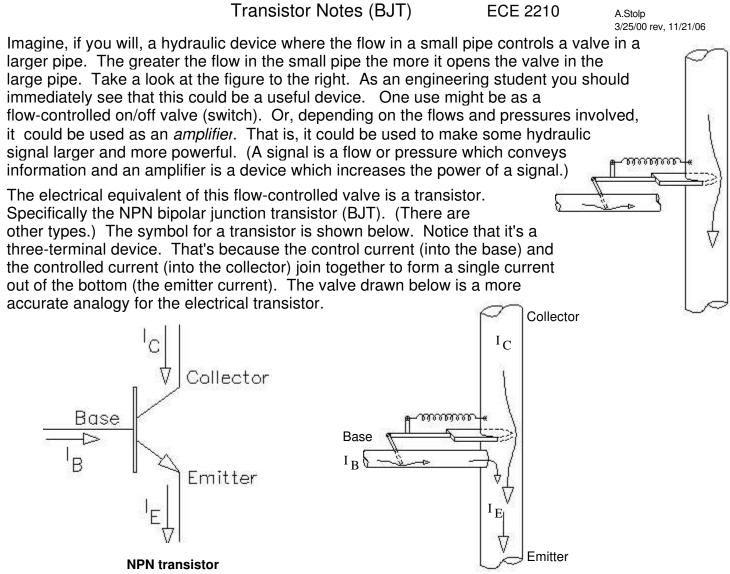
This reverse bias region can be used as a heat or light sensor since the only current flow should be due to a few carriers produced by these effects.

The reverse biased diode can also be used as a voltage variable capacitor since it is essentially an insulator (the depletion region) sandwiched between two conducting regions.

Forward bias

If you place an external voltage across the diode in the forward bias direction, the depletion region shrinks until your external voltage reaches about 0.7V. After that the diode conducts freely..

Diode Physics (The simple version) p.2



A transistor has three terminals-- the base, the collector, and the emitter. The current flow from the collector to the emitter (through the transistor) is controlled by the current flow from the base to the emitter. A small base current can control a much larger collector current. Often they are related by a simple factor, called beta (β). For a given base current, the transistor will allow β times as much collector current. The key word here is *allow*. The transistor doesn't make the current flow-- some outside power source does that. It simply regulates the current like the valve above. Big power transistors usually have a β s between 20 and 100. For little signal transistors, β is usually between 100 and 400. Darlington transistors (really two transistors in one package) can have β s in the 1000s.

A transistor can be used as a current controlled switch. When there's no base current, it's off, like an open switch. When there is a base current, it's on. If something outside of the transistor is limiting the collector current to less than β times the base current then the transistor will turn on as much as it can, like a closed switch. A transistor that is off is operating in its "cutoff" region. A transistor that is fully on is operating in its "saturation" region. A transistor that is partially on is in active control of its collector current (β times the base current) and is operating in its "active" region. (Note the valve analogy has a problem with the "open" and "closed" terms.)

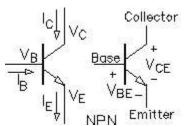
There are many types of transistors. PNP transistors work like the NPN transistors, except that all the currents and voltages are backwards. Field-effect transistors (FETs) are are controlled by voltage instead of current and come in many varieties. In this class we'll only work with NPN transistors.

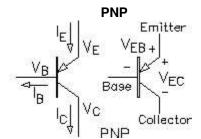
Silicon diodes are made of two layers of doped silicon, a P layer is the anode and an N layer is the cathode. A P-N junction is a diode. Anode

Bipolar junction transistors (BJTs) consist of three layers of doped silicon. The NPN transistor has a thin layer of P-doped silicon sandwiched between two layers of N-doped silicon. Each P-N junction can act like a diode. In fact, this is a fairly good way to check a transistor with an ohmmeter (set to the diode setting).

The base-emitter junction always acts like a diode, but because the base is very thin, it makes the other junction act like a controlled valve (you probably don' t want to know the details, so call it magic).

Transistor Symbols





Replace v_{BF} with v_{FB} and

 v_{CF} with v_{FC} in equations below

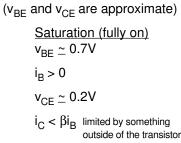
Notice the subscripts

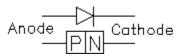
 $v_{BE} = v_B - v_E$

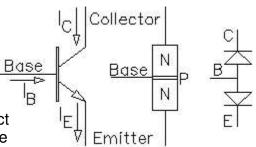
 $v_{CE} = v_{C} - v_{E}$

Modes or regions of operation

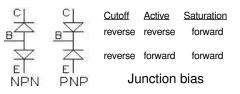
<u>Cutoff (off)</u> v _{BE} < 0.7V	$\frac{Active (partially on)}{v_{BE} \simeq 0.7V}$
i _B = 0	i _B > 0
	$v_{CE} \ge 0.2V$
i _C = 0	$i_{C} = \beta i_{B} = \alpha i_{E}$ $\alpha \simeq 1$ controlled by the transistor

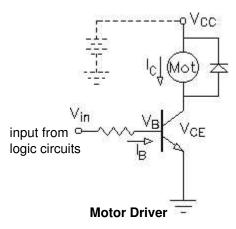






A bipolar junction transistor contains two diode junctions





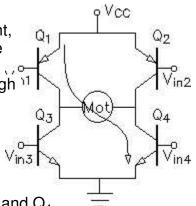
The Transistor as a switch

One of the most common uses of a transistor is as a current-controlled switch. Transistor switches are the basis for all digital circuits, but that's probably not where you' II use the transistor. More likely, you' II want to control a high-current device, like a motor, with arintegrated-circuit output from a computer or logic circuit. The small integrated circuit won' t be able to supply enough current to run the motor, so you' II use a transistor to switch the larger current that flows through the motor. The input is hooked to the base of the transistor. (Often through a current limiting resistor, since V_B will only be 0.7V when the transistor is on.) A small I_B can switch on the much larger I_C and V_{CE} can be as low as 0.2V.

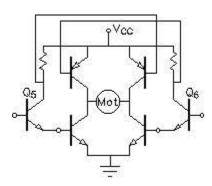
 V_{CC} : The terminal marked V_{CC} above is just a circuit terminal hooked to a power supply, drawn in dotted lines here, but usually not shown at all. Power supply wires, like ground wires are often not shown explicitly on schematics. It makes the schematics a little less cluttered and easier to read.

Diode: If you' re switching an inductive load, like a motor, you should add a diode so that you' re not trying to switch off the motor current instantly. The diode (called a *flyback* diode when used like this) provides a path for the current still flowing through the motor when the transistor is switched off.

H-bridge: Of course, if you want to make the motor turn in both directions you' II need a more complex circuit. Look at the circuit at right, it's has the shape of an H, hence the name. If transistors G and Q_4 are on, then the current flows as shown, left-to-right through the motor. If Q_4 are transistors Q_2 and Q_3 are on, then the current flows the other way through 11 the motor and the motor will turn in the opposite direction. (The motor here is a permanent-magnet DC motor.) In my circuit, the top two transistors are PNPs, which makes the circuit more efficient. The H-bridge could also be made with all NPNs or with power MOSFET $V_{in.3}^{O-1}$ transistors.



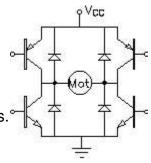
An H-bridge requires four inputs, all operated in concert. To turn on Q_1 and Q_4 , as shown, V_{in1} would have to be low and V_{in4} would have to be high. At the same time, the other two transistors would have to be off, so V_{in2} would have to be high and V_{in3} would have to be low.



If the control circuit makes a mistake and turns on Q_1 and Q_3 (or Q_2 and Q_4) at the same time you' II have a toaster instead of a motor driver, at least for a short while.

The circuit at left requires only two inputs. Transistors Q_5 and Q_6 work as *inverters*, when their inputs are high, their outputs are low and vice-versa. The resistors are known as *pull-up* resistors.

The H-bridge should also include flyback diodes.



Linear Amplifiers

The objective of a linear amplifier is to output a faithful reproduction of an input signal, only bigger. A voltage amplifier makes the signal voltage bigger. A current amplifier makes the signal current bigger. Many amplifiers do both. All amplifiers should make the signal power bigger (depends somewhat on the load). Of course that means that they need a source of power, generally DC power from a battery or power supply. The signals are usually AC.

Unlike transistor switches, which operate in cutoff and saturation, linear amplifiers must operate in the active region. **Important relations:** (active region)

$$v_{BE} = v_B - v_E = 0.7 \cdot V$$
 $v_{CE} = v_C - v_E > 0.7 \cdot V$ ($\simeq 0.2V$ if saturated)
 $i_C = \beta \cdot i_B$ $i_C = \alpha \cdot i_E \simeq i_E$

Bias:

Outside of the active region the input (base current) doesn't linearly control the output (collector current). To work as an linear amplifier, a transistor must operate in the active region. That means that the transistor must be turned on part way even when there's no signal at al Look back at the valve analogy, if small fluctuations in the horizontal pipe flow (i_B) should produce larger but similar fluctuations in the vertical pipe flow (i_C), then there must always be *some* flow. If either flow ever stops, the horizontal pipe flow (i_B) is no longer in control.

To work in the active region i_B and i_C must be positive for all values of the AC signals. i_B and i_C must be *biased* to some positive DC value. We use capital letters (I_B and I_C) for these DC bias values and lower case letters (i_b and i_c) for the AC signals that will appear as fluctuations of these DC values

Transistor Notes (BJT) p3

All voltages and currents can be shown in three different ways

e _{CAP}			<u>meaning</u> DC, Bias
sm _{sm}	^v b	ⁱ c	AC, signal
sm _{CAP}	^v B	ⁱ C	DC and AC Together

The objective of bias then, is to partially turn on the transistor, to turn it, sort-of, half-way on. Now if I twiddle i_B, i_C will show a similar, but bigger, twiddle-- that' s the whole idea. The transistor should never go into cutoff for any expected input signal, otherwise you' II ge*clipping* at the output. Clipping is a form of distortion, where the output no longer looks like the input.

Furthermore, the transistor must not saturate. That will also cause clipping at the output.

Because β can vary widely from transistor to transistor of the same part number and V_{BF} changes with temperature, achieving a stable bias can be a bit of a problem. Usually an emitter resistor (R_{F}) is needed to stabilize the bias.

DC Analysis in the active region

DC analysis applies to both switching and bias, although the circuits we' II look at here will include an R_F and we' II be working in the active region, meaning they are bias circuits. The key to DC analysis with an R_F is usually finding V_B .

The circuit at right shows a typical bias arrangement. The equations below are for that circuit, adapt them as necessary to fit your actual circuit.

If you can neglect I_B:

Often in quick-and-dirty analysis you can neglect the base current, ${\rm I}_{\rm B}$. In that case: $\frac{P}{R_{B2}} \qquad V_E = V_B - 0.7 \cdot V \qquad I_E = \frac{V_E}{R_E} \simeq I_C \qquad V_C = V_{CC} - I_C \cdot R_C$

$$V_B = V_{CC} \cdot \frac{R_{B2}}{R_{B1} + R_B}$$

This assumption is OK if: $R_{B1} \parallel R_{B2} \ll \beta R_E$

Quick check: R $_{B1}$ < 10·R $_E$ and/or R $_{B2}$ < 10·R $_E$ Should result in <10% error if β =100

If you can't neglect I_B:

Then you need to make a Thevenin equivalent of the base bias resistors.

$$V_{BB} = V_{CC} \frac{R_{B2}}{R_{B1} + R_{B2}}$$
 $R_{BB} = \frac{1}{\frac{1}{\frac{1}{R_{B1}} + \frac{1}{R_{B2}}}}$

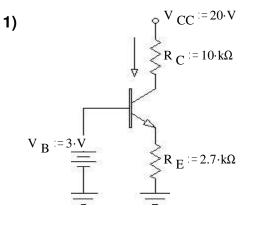
From the base' s point-of-view, the emitter resistor will look β + 1) times bigger than it really is. This is because $(\beta + 1)$ times as much current flows through R_F than into the base. We can ignore the fact that the current is bigger if we pretend that the resistor is bigger. That leads to the simplified circuit. (Usually we use β as the factor rather than (β + 1), after all β just isn' t that well known anyway.)

$$I_{B} = \frac{V_{BB} - 0.7 \cdot V}{R_{BB} + \beta \cdot R_{E}} \qquad I_{C} = \beta \cdot I_{B} \simeq I_{E} \qquad V_{E} = I_{E} \cdot R_{E} \simeq I_{C} \cdot R_{E} \qquad V_{B} = V_{E} + 0.7 \cdot V$$
$$V_{C} = V_{CC} - I_{C} \cdot R_{C}$$
$$OR: \quad V_{B} = I_{B} \cdot \beta \cdot R_{E} + 0.7 \cdot V \qquad V_{E} = V_{B} - 0.7 \cdot V \qquad I_{E} = \frac{V_{E}}{R_{E}} \simeq I_{C} \qquad V_{C} = V_{CC} - I_{C} \cdot R_{C}$$

(Thevenin Eq.)

RB2 E

Examples, DC (Bias) Analysis



 $\begin{array}{c|c} V_{CC} \coloneqq 20 \cdot V & \text{Given:} \\ V_B \coloneqq 3 \cdot V, \text{ regardless of current into base} \\ V_CC \coloneqq 20 \cdot V_R_C \coloneqq 10 \cdot k\Omega & R_E \coloneqq 2.7 \cdot k\Omega \\ \hline V_{CC} \coloneqq 20 \cdot V_R_C \coloneqq 10 \cdot k\Omega & R_E \coloneqq 2.7 \cdot k\Omega \\ \hline Find I_C, V_C, V_{CE}, \text{ and } P_Q \colon \\ \hline Solution: \\ V_E \coloneqq V_B - 0.7 \cdot V & V_E = 2.3 \cdot V \\ I_E \coloneqq \frac{V_E}{R_E} & I_E = 0.852 \cdot \text{mA} \simeq I_C \coloneqq I_E \\ V_C \coloneqq V_{CC} - I_C \cdot R_C & V_C = 11.48 \cdot V \\ V_{CE} \coloneqq V_C - V_E & V_{CE} = 9.18 \cdot V > 0.2V, \text{ OK, is in active region} \\ P_Q \coloneqq V_{CE} \cdot I_C & P_Q = 7.82 \cdot \text{mW} \\ \hline I_C \mid V_R C & \text{Given: may neglect } I_B \\ V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 8 \cdot k\Omega & R_{B2} \coloneqq 2 \cdot k\Omega & R_E \coloneqq 220 \cdot \Omega \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 8 \cdot k\Omega & R_{B2} \coloneqq 2 \cdot k\Omega & R_E \coloneqq 220 \cdot \Omega \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V & R_{B1} \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \coloneqq 10 \cdot V & V_C \coloneqq 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \coloneqq 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash 10 \cdot V \\ \hline V_CC \vdash 10 \cdot V & V_C \vdash$

2)

$$V_{CC} = 10 \cdot V$$

$$R_{B1} = 8 \cdot k\Omega$$

$$V_{C} = 7.0 \cdot V$$

$$R_{B2} = 2 \cdot k\Omega$$

$$R_{B2} = 2 \cdot k\Omega$$

$$R_{C} := \frac{V_{CC} - V_{C}}{I_{C}} \qquad R_{C} = 508 \cdot \Omega$$
$$I_{RB2} := \frac{V_{B}}{R_{B2}} \qquad I_{RB2} = 1 \cdot mA$$

Given: may neglect I_B

$$V_{CC} := 10 \cdot V \quad V_C := 7.0 \cdot V \quad R_{B1} := 8 \cdot k\Omega \quad R_{B2} := 2 \cdot k\Omega \quad R_E := 220$$

Find V_B, V_E, I_C, R_C, V_{CE}, I_{RB2}, and P_G:
Solution:
 $V_B := V_{CC} \cdot \frac{R_{B2}}{R_{B1} + R_{B2}} \quad V_B = 2 \cdot V$
 $V_E := V_B - 0.7 \cdot V \quad V_E = 1.3 \cdot V$
 $I_E := \frac{V_E}{R_E} \quad I_E = 5.91 \cdot mA \quad \simeq \ I_C := I_E$
 $V_{CE} := V_C - V_E \quad V_{CE} = 5.7 \cdot V > 0.2V$, OK, is in active region

3) R_{B1} $I_C := 4 \cdot mA$ $V_C := 6 \cdot V$ $V_E := 2.0 \cdot V$ R_E $I_{RB2} := 0.1 \cdot mA$

 $V_{CC} := 12 \cdot V \qquad \text{Given: may NOT neglect } I_B \qquad \beta := 150$ $V_{CC} := 12 \cdot V \qquad V_E := 2.0 \cdot V \qquad V_C := 6 \cdot V \qquad I_{RB2} := 0.1 \cdot \text{mA} \qquad I_C := 4 \cdot \text{mA}$ Find R_E, R_C, V_B, I_B, R_{B2}, and R_{B1}:
Solution: $V_{CE} := 0 \cdot V \qquad V_{CE} := V_{C} - V_{E} \qquad V_{CE} = 4 \cdot V \qquad > 0.2V, \text{ is in active region}$ $I_E \simeq I_C \qquad I_E := I_C \qquad R_E := \frac{V_E}{I_E} \qquad R_E = 500 \cdot \Omega$ $R_C := \frac{V_{CC} - V_C}{I_C} \qquad R_C = 1.5 \cdot k\Omega$

 $P_Q := V_{CE} \cdot I_C$ $P_Q = 33.68 \cdot mW$

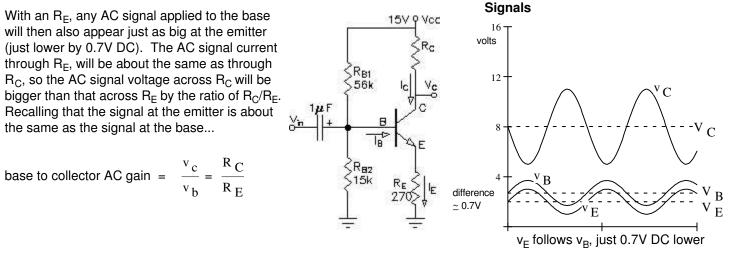
$$R_{C} := \frac{V_{C}}{I_{C}} \qquad R_{C} = 1.5 \cdot k\Omega$$

$$V_{B} := V_{E} + 0.7 \cdot V \qquad V_{B} = 2.7 \cdot V \qquad I_{B} := \frac{I_{C}}{\beta} \qquad I_{B} = 0.027 \cdot mA$$

$$R_{B2} := \frac{V_{B}}{I_{RB2}} \qquad R_{B2} = 27 \cdot k\Omega \qquad R_{B1} := \frac{V_{CC} - V_{B}}{I_{RB2} + I_{B}} \qquad R_{B1} = 73.4 \cdot k\Omega$$

Transistor Notes (BJT) p5

AC Analysis of Common emitter (CE) amplifier



If a capacitor is placed in parallel with R_E then the effective AC resistance in the emitter goes way down and the gain goes way up. In that case we need a way to estimate the AC resistance within the base-emitter junction itself.

This is called the small-signal emitter resistance:
$$r_e = \frac{25 \cdot mV}{I_C}$$

To find the gains when the input has a source resistance and the output is connected to a load resistor, the calculations become a little more complex. YOU DON' T NEED TO KNOW THE FOLLOWING MATERIAL.

R $_{\rm E}$ is the DC resistance from emitter to ground

R $_{e}$ is the AC signal resistance from emitter to ground, may be zero

Input impedance: $R_i = R_{B1} || R_{B2} || \beta (r_e + R_e)$

Output impedance: $R_0 = R_C ||r_0| < --r_0$ Often neglected

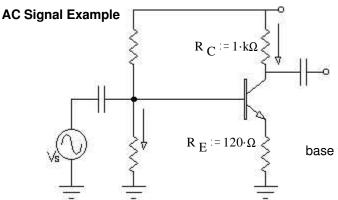
AC collector resistance: $r_c = R_C ||R_L||r_o$

r_o is a characteristic of the transistor, and is often neglected

Voltage gain:
$$A_v = \frac{v_o}{v_b} = \frac{r_c}{r_e + R_e}$$

OR: $\frac{v_o}{v_s} = \frac{R_i}{R_s + R_i} \cdot \frac{r_c}{r_e + R_e}$
Current gain: $A_i = \frac{i_o}{i_i} = \frac{r_c}{r_e + R_e} \cdot \frac{R_i}{R_L} = A_v \cdot \frac{R_i}{R_L}$

There are several other types of transistor amplifiers, but we won't look at them here.

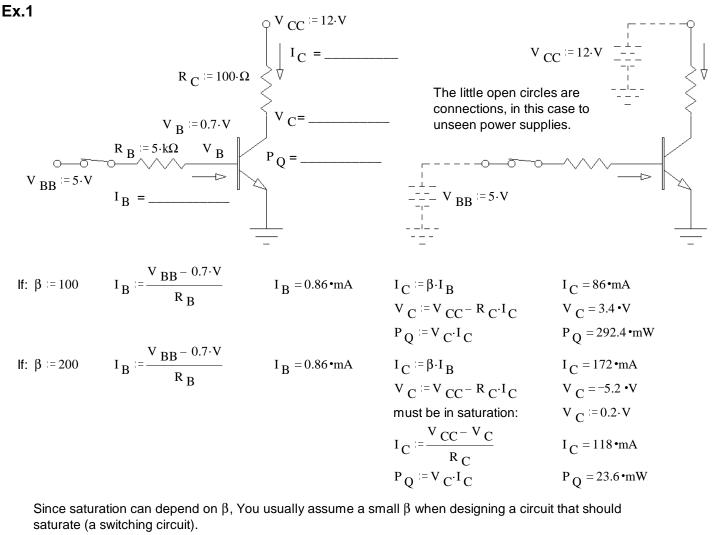


 R_{B1} R_{C1} R_{B1} R_{C1} R_{B1} R_{C1} R_{C1} R

If the v_s signal were applied at the base, an AC signal would also appear at the collector. How much larger would it be? (Voltage gain).

base to collector AC gain = $\frac{v_c}{v_b} = \frac{R_C}{R_E} = 8.33$ times bigger

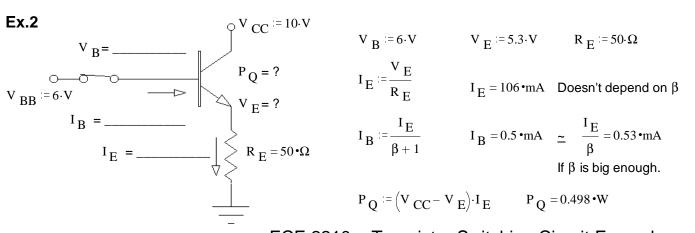
Transistor Notes (BJT) p6



Saturation also depends on R_C and V_{CC} .

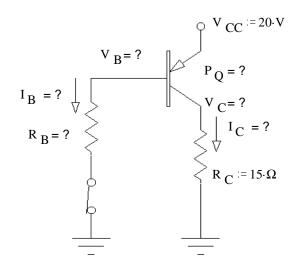
What is the largest value that R_B could be and still keep the transistor in saturation?

 $I_{Csat} := \frac{V_{CC} - 0.2 \cdot V}{R_{C}}$ $I_{Csat} = 236 \cdot mA$ $I_{B} := \frac{I_{Csat}}{\beta}$ $I_{B} = 1.18 \cdot mA$ $R_{Bmax} = \frac{5 \cdot V - 0.7 \cdot V}{I_{B}} = 3.644 \cdot k\Omega$

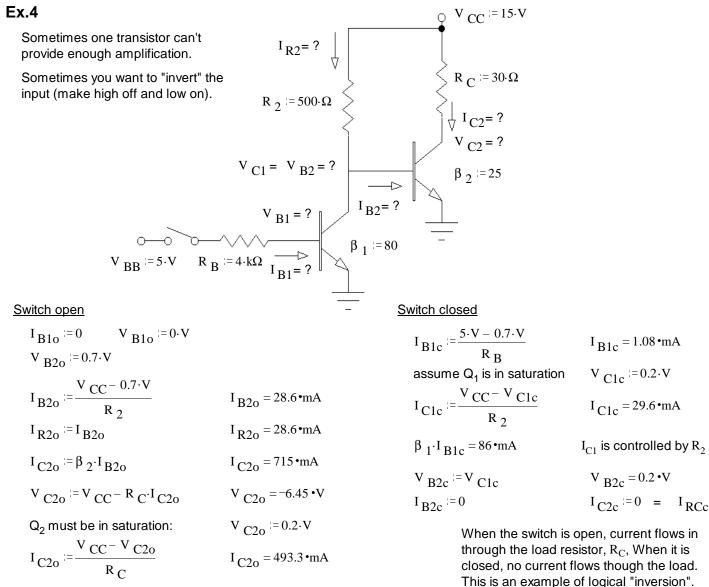


Ex.3 If the load must be connected to ground, a PNP transistor is often a better choice.

Let's assume a a small β and saturation and find the R_B necessary.



a small
$$\beta$$
: $\beta = 20$
 $V_{C} = V_{CC} - 0.2 \cdot V$ $V_{C} = 19.8 \cdot V$
 $R_{C} = 15 \cdot \Omega$
 $I_{Csat} = \frac{V_{C}}{R_{C}}$ $I_{Csat} = 1.32 \cdot A$
 $I_{B} = \frac{I_{Csat}}{\beta}$ $I_{B} = 66 \cdot mA$
 $V_{B} = V_{CC} - 0.7 \cdot V$ $V_{B} = 19.3 \cdot V$
 $R_{B} = \frac{V_{B}}{I_{B}}$ $R_{B} = 292 \cdot \Omega$
 $P_{O} = 0.2 \cdot V \cdot I_{C}$ $P_{O} = 34 \cdot mW$



Ex.5 Modified from F07 Final

A transistor is used to control the current flow through an inductive load (in the dotted box, it could be a relay coil or a DC motor).

a) Assume the transistor is in saturation (fully on) and that switch has been closed for a long time. What is the load current?

$$I_C = ?$$

 $I_{Csat} := \frac{V_{CC} - 0.2 \cdot V}{R_I}$ $I_{Csat} = 600 \cdot mA$

b) $\beta = 80$ find the minimum value of V_s, so that the transistor will be in saturation.

$$I_{Bmin} := \frac{I_{Csat}}{\beta}$$
 $I_{Bmin} = 7.5 \cdot mA$

$$\mathbf{V}_{\mathbf{Smin}} = \mathbf{I}_{\mathbf{Bmin}} \cdot \left(\mathbf{R}_{\mathbf{S}} + \mathbf{R}_{1} \right) + 0.7 \cdot \mathbf{V}$$
 $\mathbf{V}_{\mathbf{Smin}} = 2.8 \cdot \mathbf{V}_{\mathbf{Smin}}$

Use this V_S for the rest of the problem.

c) Does the diode in this circuit ever conduct a significant current? If yes, when and how much?

 $I_{Dmax} = I_{Csat} = 600 \cdot mA$ When the switch opens. from part a)

d) You got a bad transistor. $\beta = 60$ Find the new I_C, and V_{CE} and P_O.

 $I_C = \beta \cdot I_{Bmin}$ $I_{C} = 450 \cdot mA$ ^IC = ? Now operating in active region $V_{CE} = ?$ $V_{CE} = V_{CC} - R_L I_C$ $V_{CE} = 1.4 \cdot V$ $P_Q := V CE \cdot I C$ P_O = ? $P_{O} = 0.63 \cdot W$

 $\beta = 60$ Use this for the rest of the problem.

c) Find the minimum value of R_L so that the transistor will be in saturation.

$$I_{B} := \frac{V_{Smin} - 0.7 \cdot V}{R_{S} + R_{1}}$$
$$I_{B} = 7.5 \cdot mA$$
$$I_{Cmax} := \beta \cdot I_{B}$$
$$I_{Cmax} = 450 \cdot mA$$

$$I_{Cmax} = \beta \cdot I_B$$
 I_{Cmax}

 $I_{Cent} = 600 \cdot mA$

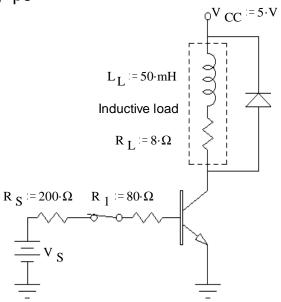
$$R_{Lmin} = \frac{V_{CC} - 0.2 \cdot V}{L_{T}} \qquad R_{Lmin} = 10.7 \cdot \Omega$$

d)
$$R_1$$
, can't be changed, so find the maximum value of R_1 so that the transistor will be in saturation.

$$I_{Bmin} := \frac{I_{Csat}}{\beta} \qquad I_{Bmin} = 10 \cdot mA$$

$$R_{1max} = \frac{V_{Smin} - 0.7 \cdot V}{I_{Bmin}} - R_{S} = 10 \cdot \Omega$$

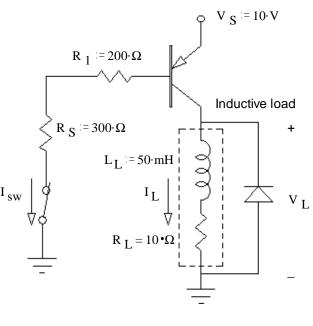
from part a)



Ex.6 From F05 Final with modifications from F06 Final

A transistor is used to control the current flow through an inductive load (in the dotted box, it could be a relay coil or a DC motor).

- a) $\beta = 25$ Assume the transistor is in the active region, find I_{sw} , I_L , V_L , V_{EC} and P_Q .
 - $I_{B} := \frac{V_{S} 0.7 \cdot V}{R_{S} + R_{1}}$ $I_{B} = 18.6 \cdot mA = I_{sw}$ $I_{L} := \beta \cdot I_{B}$ $I_{L} = 465 \cdot mA$ $R_{L} := 10 \cdot \Omega$ $V_{L} := I_{L} \cdot R_{L}$ $V_{L} = 4.65 \cdot V$ $V_{EC} := V_{S} V_{L}$ $V_{EC} = 5.35 \cdot V$ $P_{O} := V_{EC} \cdot I_{L}$ $P_{O} = 2.488 \cdot W$



b) Was the transistor actually operating in the active region? yes no (circle one) yesHow do you know? (Specifically show a value which is or is not within a correct range.)

$$V_{EC} = 5.35 \cdot V > 0.2 \cdot V$$

c) Find the maximum value of R_1 , so that the transistor will be in saturation.

If saturated:
$$V_{EC} := 0.2 \cdot V$$

 $I_{Csat} := \frac{V_S - 0.2 \cdot V}{R_L}$
 $I_{Csat} = 0.98 \cdot A$
 $I_{Bmin} := \frac{I_{Csat}}{\beta}$
 $I_{Bmin} = 39.2 \cdot mA$
 $R_{1max} = \frac{V_S - 0.7 \cdot V}{I_{Bmin}} - R_S = -63 \cdot \Omega$ NOT POSSIBLE

d) R $_1 = 200 \cdot \Omega$ and can't be changed, find the minimum value of β so that the transistor will be in saturation.

$$I_{Csat} = 0.98 \cdot A$$
 $\beta_{min} = \frac{I_{Csat}}{I_{B}}$ $\beta_{min} = 52.7$

e) How much power is dissipated by the transistor if it has the β you found in part d)

$$P_Q = 0.2 \cdot V \cdot I_{Csat}$$
 $P_Q = 0.196 \cdot W$

- f) Does the diode in this circuit ever conduct a significant current? If yes, when and how much? When the switch opens. $I_{Dmax} = I_{Csat} = 0.98 \cdot A$ from part a)
- g) The switch is open for a while. What is the load current (I_f) now? 0

Ex.7 From F13 Final

A transistor is used to control the current flow through an inductive load (in the dotted box, it could be a relay coil or a DC motor).

- a) In order for current to flow in through the load, the switch should be:
 i) closed or ii) open (Circle one)
- b) Assume the switch has been in the position you circled above for a long time. I_L is 1.3A. Find the power dissipated by transistor Q_2 (neglect base current and V_{BE}).

 $I_{L} := 1.3 \cdot A \qquad P_{Q2} = ? \qquad R_{L} := 3 \cdot \Omega$ $V_{CE2} := V_{CC2} - I_{L} \cdot R_{L} \qquad V_{CE2} = 1.1 \cdot V$ $P_{Q2} := V_{CE2} \cdot I_{L} \qquad P_{Q2} = 1.43 \cdot W$

c) This is an unacceptable power loss, so you would like to determine the minimum β_2 needed so that Q_2 will be in saturation. Assume Q_1 is also in saturation. You may assume $I_E = I_C$ for both traistors. $\beta_{2\min} = ?$

$$I_{L} := \frac{V_{CC2} - 0.2 \cdot V}{R_{L}} \qquad I_{L} = 1.6 \cdot A = I_{C2}$$

$$V_{E2} := V_{CC2} - 0.2 \cdot V \qquad V_{E2} = 4.8 \cdot V$$

$$V_{B2} := V_{E2} + 0.7 \cdot V \qquad V_{B2} = 5.5 \cdot V$$

$$V_{C1} := V_{B2} + 0.2 \cdot V \qquad V_{C1} = 5.7 \cdot V$$

$$I_{C1} := \frac{V_{CC1} - V_{C1}}{R_{2}} \qquad I_{B2} := I_{C1} \qquad I_{B2} = 57.5 \cdot M \qquad \beta_{2min} = \frac{I_{L}}{I_{B2}} = 27.826$$
Better answer
$$I_{B2} := I_{C1} \cdot \left(\frac{\beta_{1} + 1}{\beta_{1}}\right) \qquad I_{B2} = 58.075 \cdot M \qquad \beta_{2min} = \frac{I_{L}}{I_{B2}} - 1 = 26.551$$

You replace Q_2 with a new transistor that has a β greater than what you just calculated.

d) How much power is dissipated by the new transistor Q_2 (neglect base current and V_{BE})? P_{O2} = ?

$$P_{O2} = 0.2 \cdot V \cdot I_L$$
 $P_{O2} = 320 \cdot mW$

e) What is the maximum value of R_1 needed to saturate Q_1 ? $\beta_1 = 100$

$$I_{B1min} := \frac{{}^{1}C1}{\beta_{1}}$$

$$I_{B1min} = 0.575 \cdot mA$$

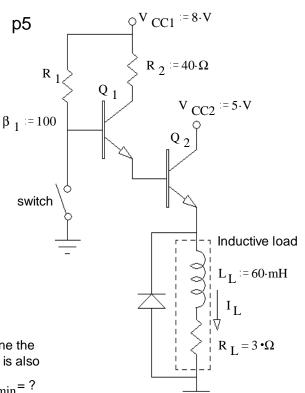
$$V_{B1} := V_{B2} + 0.7 \cdot V$$

$$V_{B1} = 6.2 \cdot V$$

$$R_{1max} := \frac{V_{CC1} - V_{B1}}{I_{B1min}}$$

$$R_{1max} = 3.13 \cdot k\Omega$$

f) Does the diode in this circuit ever conduct a significant current? If yes, when and how much? When the switch closes. $I_{Dmax} = I_L = 1.6 \cdot A$ from part c)



Ex.8 From F12 Final

A couple of transistors are used to control the current flow through an inductive load. The switch has been closed, as shown, for a long time.

a) You measure the voltage at each collector (referenced to ground) as shown on the drawing. Find the power dissipated by transistor Q_2 .

$$V_{C1} := 5 \cdot V \qquad V_{C2} := 2 \cdot V$$

$$I_{L} := \frac{V_{CC} - 2 \cdot V}{R_{L}} \qquad I_{L} = 1.5 \cdot A$$

$$P_{Q2} := V_{C2} \cdot I_L \qquad P_{Q2} = 3 \cdot W$$

b) Find the β of transistor Q_2 .

$$V_{R2} := 5 \cdot V - 0.7 \cdot V$$

 $V_{R2} := 4.3 \cdot V$
 $I_{R2} := \frac{V_{R2}}{R_2}$
 $\beta_2 := \frac{I_L}{I_{R2}}$
 $\beta_2 = 34.884$

c) Find the β of transistor Q_1 .

$$I_{R1} := \frac{V_{CC} - 0.7 \cdot V}{R_1}$$
 $\beta_1 := \frac{I_{R2}}{I_{R1}}$ $\beta_1 = 58.9$

d) Find the minimum β for transistor Q₁ to be in saturation. $\beta_{1\min} = ?$

If Q_1 is saturated: $V_{R2} = V_{CC} - 0.2 \cdot V - 0.7 \cdot V$ $V_{R2} = 7.1 \cdot V_{R2}$

If Q₁ is saturated:
$$I_{R2} = \frac{V_{R2}}{R_2}$$
 $I_{R2} = 71 \cdot mA$ $\beta_{1\min} = \frac{I_{R2}}{I_{R1}}$ $\beta_{1\min} = 97.3$

You replace Q_1 with a different transistor so that now: $\beta_1 = 200$ Use this from now on. e) Find the new load current (I_L) assuming transistor Q_2 is in the active region.

 Q_1 is saturated: $I_{R2} = 71 \cdot mA$ $I_L = I_{R2} \cdot \beta_2$ $I_L = 2.477 \cdot A$

f) Check the assumption that ${\rm Q}_2$ is in the active region and recaculate ${\rm I}_L$ if necessary.

$$I_{R2} \cdot \beta_{2} \cdot R_{L} = 9.907 \cdot V \qquad V_{CE2} := V_{CC} - I_{R2} \cdot \beta_{2} \cdot R_{L} \qquad V_{CE2} = -1.907 \cdot V \text{ Not possible}$$

$$Q_{2} \text{ is saturated:} \quad I_{L} := \frac{V_{CC} - 0.2 \cdot V}{R_{L}} \qquad I_{L} = 1.95 \cdot A$$

g) Does the diode in this circuit ever conduct a significant current? If yes, when and how much?

When the switch opens. I $_{Dmax}$ = 1.95 A from part f)

 $R_{1} := 10 \cdot k\Omega$ $R_{2} := 100 \cdot \Omega$ $R_{2} := 100 \cdot \Omega$

Operational Amplifiers

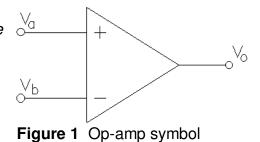
A. Stolp, 4/22/01 rev, 12/5/05

An operational amplifier is basically a complete high-gain voltage amplifier in a small package. Op-amps were originally developed to perform mathematical operations in analog computers, hence the odd name. They are now made using integrated circuit technology, so they come in the typical multi-pin IC packages. With the proper external components, the operational amplifier can perform a wide variety of "operations" on the input voltage. It can multiply the input voltage by nearly any constant factor, positive or negative, it can add the input voltage to other input voltages, and it can integrate or differentiate the input voltage. The respective circuits are called *amplifiers*, *summers*, *integrators*, and *differentiators*. Op-amps are also used to make active frequency filters, current-to-voltage converters, voltage-to-current converters, current amplifiers, voltage comparators, etc. etc.. These little parts are so versatile, useful, handy, and cheap that they're kind of like electronic Lego blocks — although somewhat drably colored.

Op-amp characteristics

Operational amplifiers have several very important characteristics that make them so useful:

An op-amp has *two* inputs and it amplifies the *voltage* difference between those two inputs. These two inputs are known as the noninverting input, labeled (+), and the inverting input, labeled (-), as shown in Fig. 1. The output voltage is a function of the noninverting input voltage minus the inverting input voltage.



That is: $v_a = G(v_a - v_b)$ Where G = voltage gain of the op-amp.

2. The op-amp must be connected to external sources of power (not shown on the drawing above). The output voltage (v_0) cannot be more positive than the positive power source or more negative than the negative power source. The gain (G) is very high, typically more than 100,000. Together that means that if the output (v_0) is in the *active* range (somewhere between its physical limits, often called "rails"), then $v_a - v_b \approx 0$, and $v_a \approx v_b$. This is a very important point. If you don't see this, look back at the equation above, v_0 is limited, G is very big, so $(v_a - v_b)$ must be very small.

If the output is:	The inputs must be:
In active range	$V_a \approx V_b$
- rail < v _o < +rail	
If the inputs are:	The output must be:
<u>If the inputs are</u> : $V_a > V_b$	<u>The output must be</u> : + rail

3. In fact, $v_a - v_b$ must be so small that it's very difficult to make $v_a \& v_b$ close enough

without using some *negative feedback*. Negative feedback makes the op-amp maintain $v_a \approx v_b$ for itself. With the proper negative feedback the op-amp keeps $v_a \approx v_b$ so close that you can assume that $v_a = v_b$. Without this negative feedback the op-amp output will almost certainly be at one of its limits, either high or low, i.e. NOT in its active, or *linear*, range. Incidentally, circuits without negative feedback are also useful, but then the output is either high or low (digital) and not linearly related to the input. These types of circuits are called *nonlinear* circuits.

- 4. Op-amps amplify DC as well as AC.
- The input currents are almost zero. In more technical terms, the op-amp has very high input impedance. As long as you use reasonable resistor values in your circuits (say ≤ 1 MΩ), you can neglect the input currents.

Simple, isn't it? OK, so it doesn't sound so simple yet, but the application of these characteristics really isn't hard. Let's look at some circuits.

Linear Circuits

Linear circuits employ negative feedback to keep $v_a \approx v_b$. If a circuit has a connection from the output to the inverting (-) input, then it has negative feedback.

Voltage follower

The voltage follower shown in Fig. 2 is probably the simplest linear op-amp circuit. Notice the feedback from the output to the inverting (-) input. If we were to hook the circuit input (v_a) up to some voltage source,

say 2 volts DC, what would happen? If the output was lower than 2 V, then the input voltage difference $(v_a - v_b)$ would be positive and the huge gain of the op-amp would drive the output higher. If the output was higher than 2 V, then $v_a - v_b$ would be negative the output would go down. Very quickly the output voltage v_o would change until $v_a - v_b$ becomes very small. Or basically, until $v_o = 2 V$. This is the concept of negative feedback! A fraction (in this case all) of the output voltage is "fed back" to the input in order to control the gain of the op-amp. The op-amp works very hard to maintain a very small difference between the voltages on its inputs. This circuit is known as a voltage follower because the output "follows" the input.

Negative feedback is an important concept. It is used in almost all systems, including all natural systems. A very simple example is the heating system in your house. If the air temperature is too low the thermostat detects a difference between its setting and the air temperature and turns on the heater. When the air temperature reaches the set temperature the thermostat turns off the heater—negative feedback. The servo system that you've seen in lab is another example of negative feedback. When the motor position sensor senses a different position than the input position sensor the circuit makes the motor turn in such a way that the difference is minimized and the positions line up.

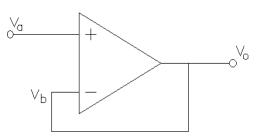


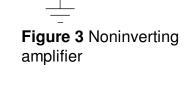
Figure 2 Voltage follower

Noninverting amplifier

Now suppose we feed back only a fraction of the output voltage rather than all of it. The method used for this is shown in Fig. 3. R_1 and R_f constitute a voltage divider. Remember, the current flowing into an op-amp input is virtually nil, so we can neglect its effect on the voltage divider. This is one of the very nice features of an op-amp. In this circuit, as in the voltage follower, the op-amp works very hard to keep $v_a - v_b$ very small. Only now v_b is a fraction of v_o and the op-amp has to make v_o that much larger.

$$v_{in} = v_a \approx v_b = \frac{R_1}{R_1 + R_f} v_o$$

For all practical purposes:



 $V_{o} = \frac{R_{1} + R_{f}}{R_{1}} V_{in} = (1 + \frac{R_{f}}{R_{1}}) V_{in}$

Rf

℅

<u>%</u>

Notice that by adjusting the ratio of R_f and R_1 , we can make the gain of the op-amp circuit almost anything we want. Isn' t that neat? The circuit in Fig. 3 is called a noninverting amplifier because the output voltage is in phase with the input voltage; that is, it is *not* inverted. When the input voltage increases, the output voltage will also increase and vice-versa. Yes, noninverting is a double negative and kind-of a dumb name.

Inverting amplifier

Before going on, observe that I've swapped the positions of the two inputs(- & +) on my op-amp symbol. Either way of drawing the op-amp is OK, whatever makes the whole schematic look better. The noninverting input is on the bottom in this case because it's hooked to ground. Draw the op-amp so that the surrounding circuitry is clear.

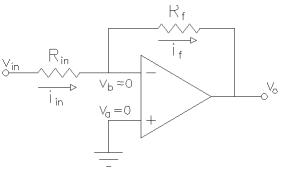


Figure 4 Inverting amplifier

The op-amp output is still connected to the inverting **Figure 4** inverting amplifier (-) input, so again we have negative feedback. If $v_b > v_a$ then the output will go down, taking v_b with it, until $v_b \approx v_a$. If $v_b < v_a$ then the output will go up until $v_b \approx v_a$. Negative feedback makes the op-amp do its best to equalize its inputs. In this circuit $v_a = 0$, which means that the op-amp will try to keep $v_b \approx 0$ as well. The current into the op-amp is zero, so i_{in} and i_f must be the same ($i_f = i_{in}$). Using these two ideas together:

$$i_{in} = \frac{V_{in} - 0}{R_{in}} = i_f = \frac{0 - V_o}{R_f}$$
 $V_o = -\frac{R_f}{R_{in}}V_{in}$

The minus sign means that v_o will be inverted with respect to v_{in} , hence the name of this amplifier. When v_{in} is positive, v_o is negative, and when v_{in} is negative, v_o is positive. The gain of the inverting amplifier, like that of the noninverting amplifier, is completely dependent on our choices of R_f and R_{in} .

Summer

The inverting amplifier can also be used as a summing amplifier; that is, it can be made to add the effects of several input voltages together. Look at the circuit in Fig. 5.

$$i_f = i_1 + i_2 + i_3$$

$$\dot{I}_{f} = -\frac{V_{o}}{R_{f}} = \frac{V_{1}}{R_{1}} + \frac{V_{2}}{R_{2}} + \frac{V_{3}}{R_{3}}$$

$$V_o = -\frac{R_f}{R_1}V_1 - \frac{R_f}{R_2}V_2 - \frac{R_f}{R_3}V_3$$

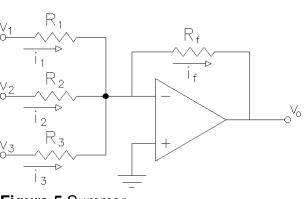


Figure 5 Summer

The summer can be expanded to any number of inputs. See? This is getting easier.

Differentiator

The differentiator looks an awful lot like the inverting amplifier, and is analyzed in a very similar way.

$$i_{C} = C \frac{dv_{in}}{dt} = i_{f} = -\frac{v_{o}}{R_{f}}$$
$$v_{o} = -CR_{f} \frac{dv_{in}}{dt}$$

Integr

ator

Another useful op-amp circuit is the integrator, shown in Fig. 7. For this circuit:

$$i_{in} = \frac{v_{in}}{R_{in}} = i_C = -C\frac{dv_o}{dt}$$
$$v_o = -\frac{1}{CR_{in}}\int v_{in}dt$$

Unfortunately, The simple integrator does have one little practical problem. Notice that if the input voltage has any dc component, the output voltage will soon try to run off to infinity. (Actually it will stop when the op-amp reaches one of its output limits, either negative or positive.) A resistor is usually placed in parallel with the capacitor to eliminate this rather annoying effect. The circuit in Fig. 8 has such a resistor. This is a *runningaverage* or *Miller* integrator.

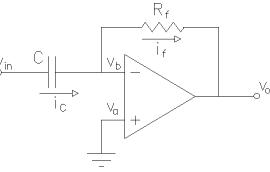


Figure 6 Differentiator

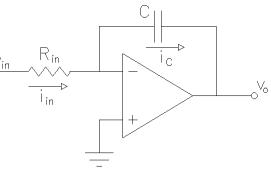


Figure 7 Integrator

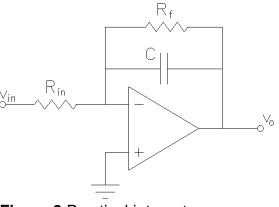


Figure 8 Practical integrator

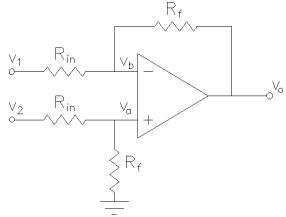
Active Filters

If you replace the resistors in the inverting and noninverting amplifiers with frequency dependant impedances (capacitors and/or inductors), you can make all sorts of frequency dependant circuits, including filters. In fact, the differentiator and integrator circuits can be thought of as filters.

One of the main advantages of active filters is that you don't need to use inductors. Real inductors are far from ideal, as you've no doubt observed in lab. Real capacitors are much closer to ideal capacitors and they're cheaper than inductors. Entire books are devoted to these *active* filters and we won't cover them any further here.

Differential amplifier

This circuit amplifies only the difference between the two inputs. In this circuit there are two resistors labeled R_{in} , which means that their values are equal. Same goes for the two R_{f} 's.



$$v_{a} = \frac{R_{f}}{R_{in} + R_{f}} v_{2} = v_{b} = \frac{R_{f}}{R_{in} + R_{f}} (v_{1} - v_{o}) + v_{o}$$

$$\frac{R_{f}}{R_{in} + R_{f}} v_{2} = \frac{R_{f}}{R_{in} + R_{f}} (v_{1} - v_{o}) + \frac{R_{in} + R_{f}}{R_{in} + R_{f}} v_{o}$$

$$R_{f} v_{2} = R_{f} v_{1} - R_{f} v_{o} + R_{in} v_{o} + R_{f} v_{o}$$

$$R_{f} v_{2} = R_{f} v_{1} + R_{in} v_{o}$$

$$v_{o} = \frac{R_{f}}{R_{in}} (v_{2} - v_{1})$$

Figure 10 Differential amplifier

Don't confuse the differential amplifier with the differentiator. The differential amplifier amplifies the <u>difference</u> of two inputs while the differentiator amplifies the <u>slope</u> of an input.

Instrumentation Amplifier

The differential amplifier isn't really very practical. The current that flows into the top input depends on the voltage applied to the bottom input. This may not seem that bad, but it is. It means that the input characteristics of this circuit are not constant. One way to get around this would be to place a voltage follower on each input, as shown here.

$$v_o = \frac{R_f}{R_{in}}(v_2 - v_1)$$

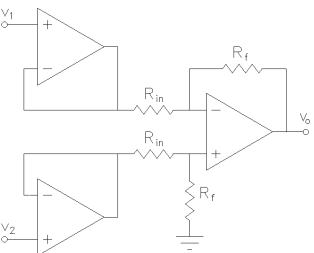


Figure 9 Buffered differential amplifier

Now this is a perfectly good circuit. If the two R_is are closely matched and the two R_{in}s are also closely matched, then this circuit will amplify differential voltages very well and reject *common* voltages (a voltage that is common to both inputs should subtract out of the equation. In EE terms, it has a good Common-Mode-Rejection-Ration (CMRR).

But what if you want to change the gain? You'd have to change two resistors at the same time. By adding two more matched resistors and variable resistor we'll get the instrumentation amplifier shown at right. The equation for this circuit is:

$$v_o = (1 + \frac{2R_2}{R_1}) \frac{R_4}{R_3} (v_2 - v_1)$$

This is an important circuit and you will probably see it again many times. For instance, if you had to amplify the output of a wheatstone bridge of stain gages, this would be the amp for the job.

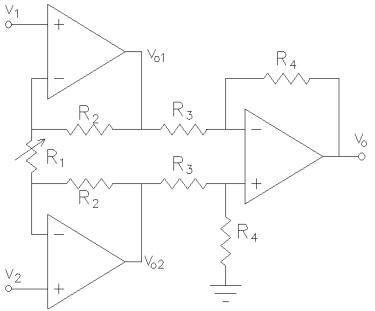


Figure 11 Instrumentation amplifier

Op-amp with extra current amplification

Most op-amps cannot supply much current to the load. They are often limited to 10 or 20 mA, about enough to light an LED, but not much more. That can br very limiting. The circuit at right shows a quick and dirty way to use two transistors to greatly increase the load current (at a small cost in output voltage swing). Notice that the feedback is taken from the output of the transistors, so they sort-of become part of the op-amp and the op-amp will do a pretty good job of eliminating the "crossover" dead-zone that occurs as one transistor turns off and the other turn on.

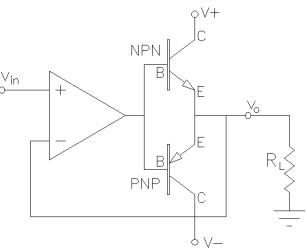


Figure 12 How to get more current

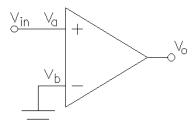
This particular circuit is a simple voltage follower. You can adapt this same current amplification to most of the other op-amp circuits that we have discussed. A few words of warning, however. The extra delay in the feedback can result in instabilities. Try it with the parts you intend to use before you depend on this design. Also, if you use a low quality op-amp (with a slow slew rate) you can get significant crossover distortion.

Nonlinear Circuits

In all cases so far, the feedback signal (voltage) has been applied to the inverting (-) input of the op-amp. This means that the feedback is negative. Negative feedback tends to reduce the difference between the v_a and v_b voltages and make linear circuits. Without negative feedback the op-amp cannot minimize the difference between v_a and v_b and the very high sensitivity of the op-amp results in *switching*, or *nonlinear* circuits.

Comparator

Now look at Fig. 14. This circuit will **not** work as a linear circuit. If $v_a > 0$ the output will be as high as the op-amp can make it, usually a volt or two below the positive power supply. If $v_a < 0$ the output will be as low as the op-amp can make it, usually a volt or two above the negative power supply. The output is no longer linearly related to the input– it's more like a digital signal, high or low depending on how v_{in} compares to ground (0 V). The comparator is a *nonlinear* circuit.





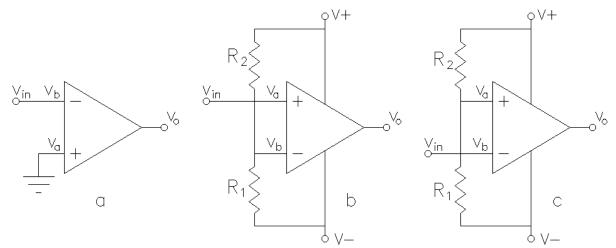


Figure 14 Other comparators

All the circuits above are also comparators. In the first circuit, the input is again compared to ground, but this time the output goes low when the input goes high and vise-versa. In the remaining circuits the input is compared not to 0 V, but to some voltage set by the voltage divider of R_1 and R_2 .

Schmitt trigger

The Schmitt trigger is a variation of the simple comparator which has hysteresis, that is, it has a toggle action. When the output is high, positive feedback makes the switching level higher than it is when the output is low. A little positive feedback makes a comparator with better noise immunity. Increase the positive feedback and the Schmitt trigger can be used in other switching applications.

Look at the Schmitt ttrigger circuit shown at right. Notice that $v_a = [R_1/(R_1 + R_i)]v_o$, it depends on the output. Lets say

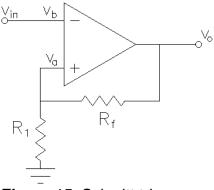


Figure 15 Schmitt trigger

the output is low and the input is decreasing. When $v_{in} < v_a$ the output goes high and suddenly v_a goes a little bit higher with it. That makes the difference between v_b and v_a even bigger. To make the circuit switch again v_{in} has to go back up beyond the original switching level. It has to reach the new v_a before the output will switch low. In this circuit the two switching levels are above and below ground by the same amount (unless you have nonsymmetric power supplies).

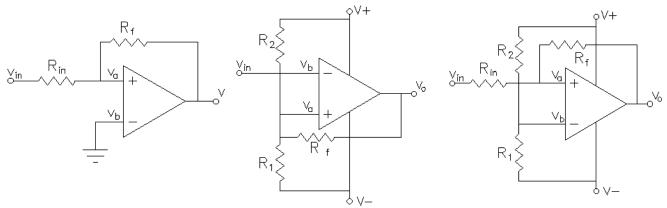
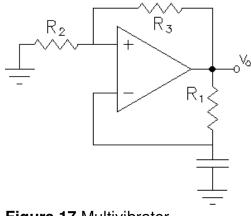


Figure 16 Other Schmitt triggers

The circuits above are variations of the Schmitt trigger. In the first circuit, the input is again compared to levels above and below ground, but this time the output goes high when the input goes high and vise-versa. In the remaining circuits the switching levels are not symmetric about 0 V, but about some voltage set by the voltage divider of R_1 and R_2 .

Multivibrator (square wave generator)

The heart of the multivibrator is a Schmitt trigger with lots of positive feedback. Usually $R_2 = R_3$, which set the switching levels at about $\frac{1}{2}$ V+ and $\frac{1}{2}$ V-. When the output is high the capacitor charges through R_1 until it reaches the $\frac{1}{2}$ V+ switching level, the output switches low and the capacitor discharges to zero and then charges up (down) until it reaches the $\frac{1}{2}$ V- switching level. That makes the output switch high and the process repeats.

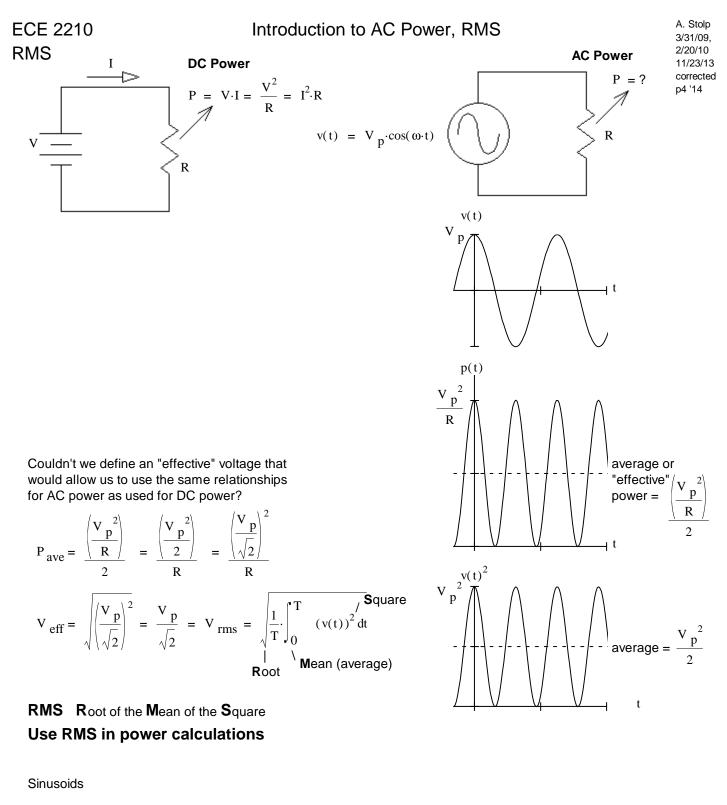


Conclusion



In all of these circuits, with either negative or positive feedback, the output voltage v_o cannot increase without bounds. It is bounded in the positive direction by V+, the op-amp positive power supply voltage, and is bounded in the negative direction by V-, the op-amp negative supply voltage. If the output voltage is within these bounds, $v_a - v_b$ must be very small. If $v_a - v_b$ were not very small, v_o would soon be forced to one of its limits. Linear circuits use negative feedback to keep this difference small. Without negative feedback you can reasonably assume that the circuit is some kind of switching circuit and that the output is always at one or the other of its limits.

This only scratches the surface of what you can do with op-amps. Get a copy of *The Op-amp Cookbook* for lots more ideas presented in a no-nonsense way.

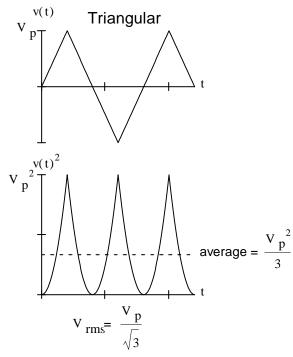


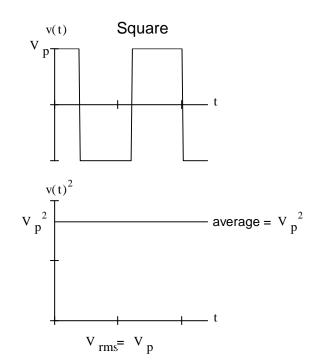
$$V_{\text{rms}} = \sqrt{\frac{1}{T}} \int_{0}^{T} (v(t))^{2} dt = \sqrt{\frac{1}{T}} \int_{0}^{T} (V_{p} \cdot \cos(\omega \cdot t))^{2} dt = \sqrt{\frac{1}{T}} \int_{0}^{T} V_{p}^{2} \cdot \left(\frac{1}{2} + \frac{1}{2} \cdot \cos(2 \cdot \omega \cdot t)\right) dt$$
$$= \frac{V_{p}}{\sqrt{2}} \cdot \sqrt{\frac{1}{T}} \int_{0}^{T} (1) dt + \frac{1}{T} \cdot \int_{0}^{T} \cos(2 \cdot \omega \cdot t) dt = \frac{V_{p}}{\sqrt{2}} \cdot \sqrt{1+0}$$

Common household power

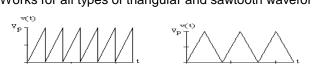


What about other wave shapes??





Works for all types of triangular and sawtooth waveforms



 $= \sqrt{\frac{1}{T}} \int_{0}^{T} \left(V_{p} \cdot \cos(\omega \cdot t) + V_{DC} \right)^{2} dt$

How about AC + DC ?

 $V_{\text{rms}} = \sqrt{\frac{1}{T}} \int_{0}^{T} (v(t))^2 dt$

$$= \sqrt{\frac{1}{T}} \cdot \int_{0}^{T} \left[\left(V_{p} \cdot \cos(\omega \cdot t) \right)^{2} + 2 \cdot \left(V_{p} \cdot \cos(\omega \cdot t) \right) \cdot V_{DC} + V_{DC}^{2} \right] dt$$
$$= \sqrt{\frac{1}{T}} \cdot \int_{0}^{T} \left(V_{p} \cdot \cos(\omega \cdot t) \right)^{2} dt + \frac{1}{T} \cdot \int_{0}^{T} 2 \cdot \left(V_{p} \cdot \cos(\omega \cdot t) \right) \cdot V_{DC} dt + \frac{1}{T} \cdot \int_{0}^{T} V_{DC}^{2} dt$$
$$- - - \text{ zero over one period} - - -$$

= $\sqrt{V_{\text{rmsAC}}^2 + 0 + V_{\text{DC}}^2}$ = $\sqrt{V_{\text{rmsAC}}^2 + V_{\text{DC}}^2}$

Same for DC

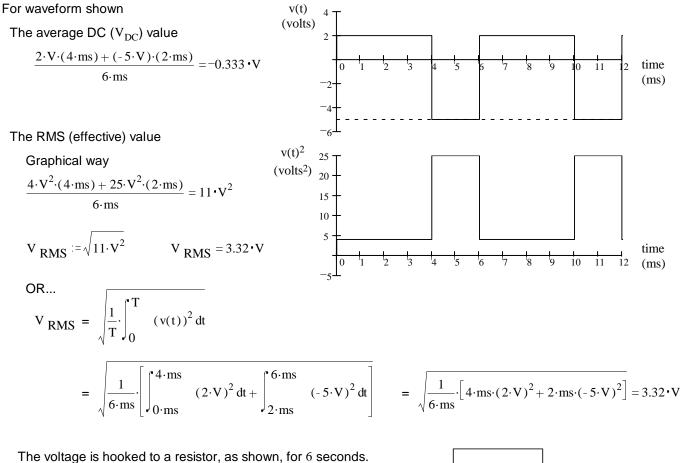
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 $V_{rms} = \frac{V_p}{\sqrt{2}}$ $I_{rms} = \frac{I_p}{\sqrt{2}}$ sinusoid: triangular: $V_{rms} = \frac{V_p}{\sqrt{3}}$ $I_{rms} = \frac{I_p}{\sqrt{3}}$ $V_{ra} = \frac{1}{2} V_p$ $I_{ra} = \frac{1}{2} I_p$ square: $V_{rms} = V_p$ $I_{rms} = I_p$ $v_{rms} = \sqrt{v_{rmsAC}^2 + v_{DC}^2}$

<u>rectified average</u> $V_{ra} = \frac{1}{T} \left| v(t) \right| dt$ \bigvee $V_{ra} = \frac{2}{\pi} V_p$ $I_{ra} = \frac{2}{\pi} I_p$ $V_{ra} = V_{rms} = V_p$ $I_{ra} = I_{rms} = I_p$ Most AC meters don't measure true RMS. Instead, they measure V_{ra} , display $1.11 V_{ra}$, and call it RMS. That works for sine waves

but not for any other waveform.

Some waveforms don't fall into these forms, then you have to perform the math from scratch



The energy is transferred to the resistor during that 6 seconds:

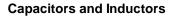
 $P_L = \frac{V_{RMS}^2}{R_T}$ $P_L = 0.22 \cdot W$ $W_{I} := P_{I} \cdot 6 \cdot sec$ $W_{L} = 1.32$ ·joule All converted to heat

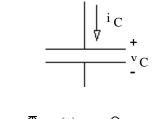
 $\langle R_{\rm L} := 50 \cdot \Omega$

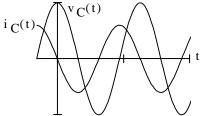
Use RMS in power calculations

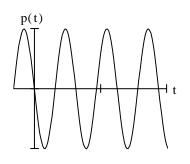
$$P = I_{Rrms}^{2} \cdot R = \frac{V_{Rrms}^{2}}{R}$$

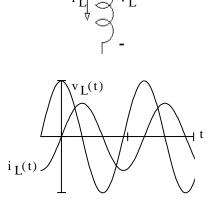
for Resistors ONLY !!

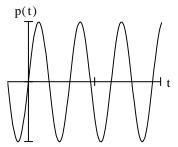




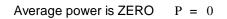








Average power is ZERO P = 0



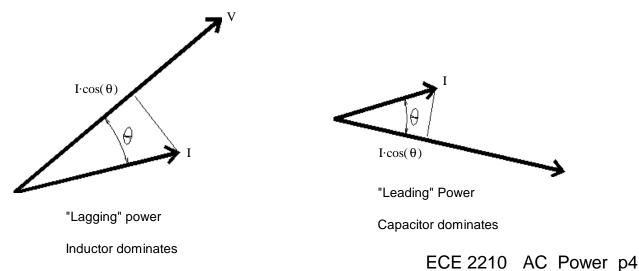
2

Capacitors and Inductors DO NOT dissipate (real) average power.

Reactive power is negative

Reactive power is negativeReactive power is positive
$$Q_C = -I_{Crms} \cdot V_{Crms}$$
 $Q_L = I_{Lrms} \cdot V_{Lrms}$ $= -I_{Crms}^2 \cdot \frac{1}{\omega \cdot C} = -V_{Crms}^2 \cdot \omega \cdot C$ $= I_{Lrms}^2 \cdot \omega \cdot L = \frac{V_{Lrms}^2}{\omega \cdot L}$

If current and voltage are not in phase, only the in-phase part of the current matters for the power-- DOT PRODUCT



Real Power

$$P = I_{Rrms}^{2} \cdot R = \frac{V_{Rrms}^{2}}{R}$$
 for resistors -

other wise

$$P = V_{rms} \cdot I_{rms} \cdot \cos(\theta) = I_{rms}^{2} \cdot |\mathbf{Z}| \cdot \cos(\theta) = \frac{V_{rms}^{2}}{|\mathbf{Z}|} \cdot \cos(\theta)$$
 units: watts, kW, MW, etc.

P = "Real" Power (average) = V_{rms}·I_{rms}·pf = I_{rms}²· |Z|·pf =
$$\frac{V_{rms}}{|Z|}$$
·pf

Reactive Power

$$--\left|--- \text{ capacitors -> - Q} - Q_{C} = I_{Crms}^{2} \cdot X_{C} = \frac{V_{Crms}^{2}}{X_{C}} - X_{C}^{2} - \frac{1}{\omega \cdot C} \text{ and is a negative number} \right|$$

2

_____ inductors -> + Q
$$Q_L = I_{Lrms}^2 \cdot X_L = \frac{V_{Lrms}}{X_L}$$

other wise

$$Q = Reactive "power" = V_{rms} \cdot I_{rms} \cdot \sin(\theta)$$
 units: VAR, kVAR, etc. "volt-amp-reactive

Complex and Apparent Power

$$S = Complex "power" = V_{rms} \cdot \overline{I_{rms}} = P + jQ = V_{rms} I_{rms} \cdot \underline{/\theta}$$
 units: VA, kVA, etc. "volt-amp"

NOT
$$I_{\text{rms}}^2 \cdot \mathbf{Z}$$
 NOR $\frac{V_{\text{rms}}^2}{\mathbf{Z}}$

S = Apparent "power" =
$$|S| = V_{rms} \cdot I_{rms} = \sqrt{P^2 + Q^2}$$

Power factor

 $pf = cos(\theta) = power factor (sometimes expressed in %) 0 \le pf \le 1$

 θ is the **phase angle** between the voltage and the current or the phase angle of the impedance. $\theta = \theta_{T}$

 $\theta < 0$ Load is "Capacitive", power factor is "leading". This condition is very rare

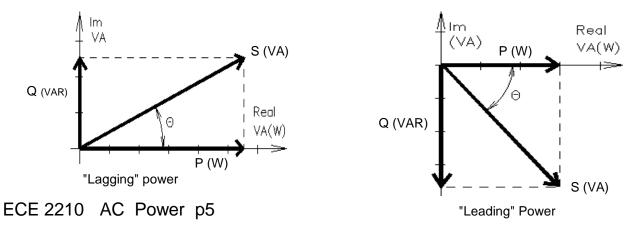
complex congugate

 $\theta > 0$ Load is "Inductive", power factor is "lagging". This condition is so common you can assume any power factor given is lagging unless specified otherwise. Transformers and motors make most loads inductive.

Industrial users are charged for the reactive power that they use, so power factor < 1 is a bad thing.

Power factor < 1 is also bad for the power company. To deliver the same power to the load, they have more line current (and thus more line losses).

Power factors are "corrected" by adding capacitors (or capacitve loads) in parallel with the inductive loads which cause the problems. (In the rare case that the load is capacitive, the pf would be corrected by an inductor.)



 $X_{L} = \omega \cdot L$ and is a positive number

units: VA, kVA, etc. "volt-amp"

Transformer basics and ratings

A Transformer is two coils of wire that are magnetically coupled.

Transformers are only useful for AC, which is one of the big reasons electrical power is generated and distributed as AC.

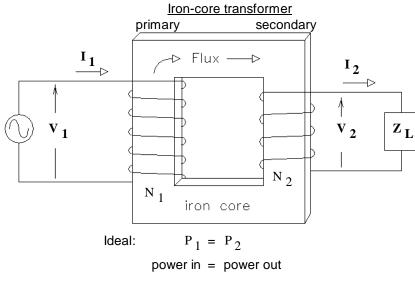
Transformer turns and turns ratios are rarely given, V_p/V_s is much more common where V_p/V_s is the rated primary over rated secondary voltages. You may take this to be the same as N_1/N_2 although in reality N_2 is usually a little bit bigger to make up for losses. Also common: $V_p : V_s$.

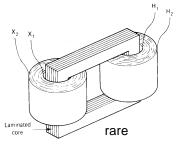
Both RMS

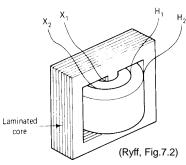
Transformers are rated in VA Transformer Rating (VA) = (rated V) x (rated I), on either side.

Don't allow voltages over the rated V , regardless of the actual current. Don't allow currents over the rated I , regardless of the actual voltage.

Ideal Transformers







common

Transformation of voltage and current

$$\frac{\mathbf{N}_1}{\mathbf{N}_2} = \frac{\mathbf{V}_1}{\mathbf{V}_2} = \frac{\mathbf{I}_2}{\mathbf{I}_1}$$

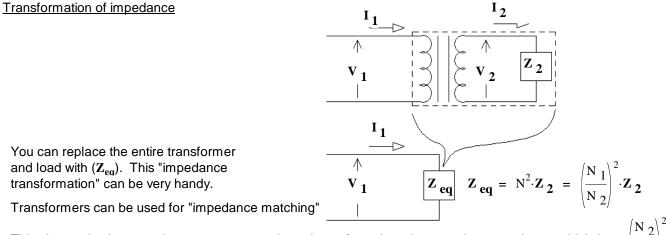
<u>Turns ratio</u>

Turns ratio as defined in Chapman text: $a = \frac{N_1}{N_2}$, same as $N = \frac{N_1}{N_2}$ Note: some other texts define the turns ratio as:

N 1

N 2

Be careful how you and others use this term



This also works the opposite way, to move an impedance from the primary to the secondary, multiply by:

 $\left(\frac{\frac{N}{2}}{N_{1}}\right)$

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Other Transformers

ECE 2210 AC Power p7

Multi-tap transformers: Many transformers have more than two connections to primary and/or the secondary. The extra connections are called "taps" and may allow you to select from several different voltages or get more than one voltage at the same time.

Isolation Transformers: Allmost all transformers isolate the primary from the secondary. An Isolation transformer has a 1:1 turns ratio and is just for isolation.

Auto Transformers: Auto transformers have only one winding with taps for various voltages. The primary and secondary are simply parts of the same winding. These parts may overlap. Any regular transformer can be wired as an auto transformer. Auto transformers DO NOT provide isolation.

Vari-AC: A special form of auto transformer with an adjustable tap for an adjustable output voltage.

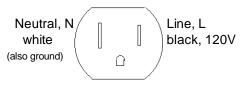
LVDT A Linear-Variable-Differential-Transformers has moveable core which couples the primary winding to the secondary winding(s) in such a way the the secondary voltage is proportional to the position of the core. LVDTs are used as position sensors.

Home power

Standard 120 V outlet connections are shown at right.

The 3 lines coming into your house are NOT 3-phase. They are +120 V, Gnd, -120 V

(The two 120s are 180° out-of-phase, allowing for 240 V connections)





3-Phase Power (FYI ONLY)

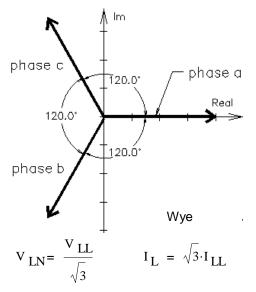
Single phase power pulses at 120 Hz. This is not good for motors or generators over 5 hp.

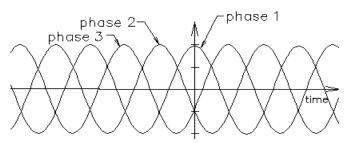
Three phase power is constant as long as the three loads are balanced.

Three lines are needed to transmit 3-phase power. If loads are balanced, ground return current will be zero.

Wye connection:

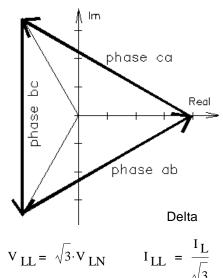
Connect each load or generator phase between a line and ground.





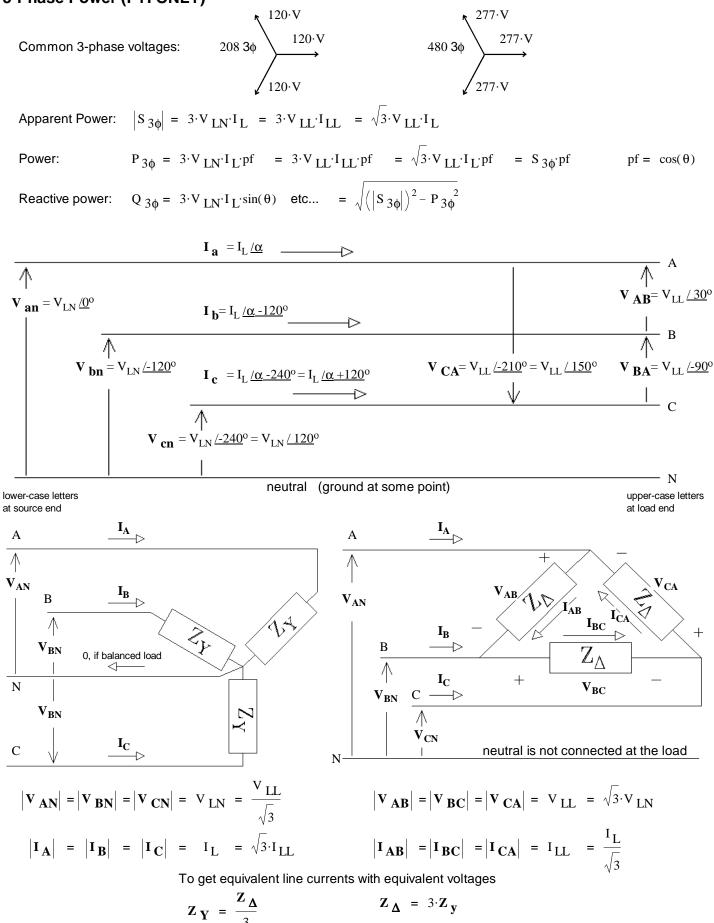
Delta connection:

Connect each load or generator phase between two lines.



3-Phase Power (FYI ONLY)

ECE 2210 AC Power p8

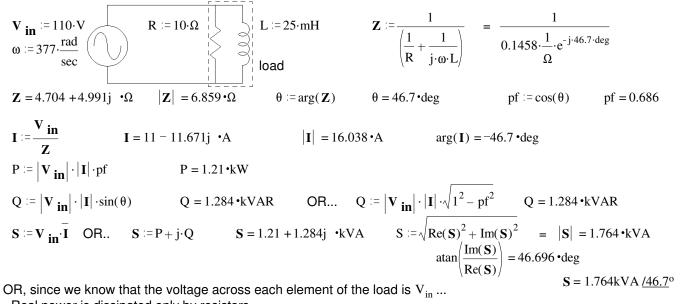


ECE 2210

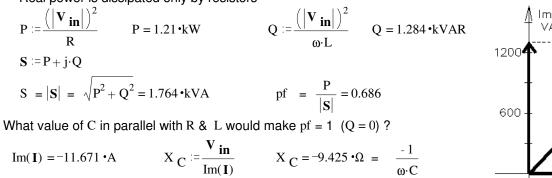
AC Power Examples

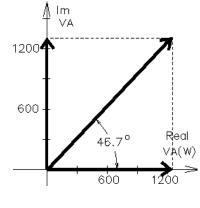
A.Stolp 11/06/02 rev 2/27/07

Ex. 1 R & L together are the load. Find the real power P, the reactive power Q, the complex power S, the apparent power |S|, & the power factor pf. Draw phasor diagram for the power.



OR, since we know that the voltage across each element of the load is $V_{\rm in} \ldots$ Real power is dissipated only by resistors

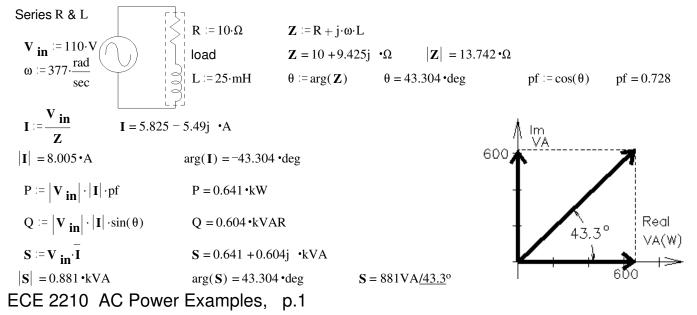




 $\frac{1}{|\mathbf{X}_{\mathbf{C}}| \cdot \boldsymbol{\omega}} = 281 \cdot \boldsymbol{\mu} \mathbf{F} \qquad \mathsf{OR..} \qquad \boldsymbol{\omega} = \frac{1}{\sqrt{\mathbf{L} \cdot \mathbf{C}}} \qquad \mathbf{C} := \frac{1}{\mathbf{L} \cdot \boldsymbol{\omega}^2} \qquad \mathbf{C} = 281 \cdot \boldsymbol{\mu} \mathbf{F}$

Ex. 2 R & L together are the load. Find the real power P, the reactive power Q, the complex power S,

the apparent power |S|, & the power factor pf. Draw phasor diagram for the power.



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OR, if we first find the magnitude of the current which flows through each element of the load...

$$|\mathbf{I}| = \frac{\mathbf{v} \cdot \mathbf{in}}{\sqrt{\mathbf{R}^2 + (\omega \cdot \mathbf{L})^2}} = 8.005 \cdot \mathbf{A}$$

$$\mathbf{P} := (|\mathbf{I}|)^2 \cdot \mathbf{R} \qquad \mathbf{P} = 0.641 \cdot \mathbf{kW} \qquad \mathbf{Q} := (|\mathbf{I}|)^2 \cdot (\omega \cdot \mathbf{L}) \qquad \mathbf{Q} = 0.604 \cdot \mathbf{kVAR}$$

$$\mathbf{S} := \mathbf{P} + \mathbf{j} \cdot \mathbf{Q} \qquad |\mathbf{S}| = \sqrt{\mathbf{P}^2 + \mathbf{Q}^2} = 0.881 \cdot \mathbf{kVA} \qquad \mathbf{pf} = \frac{\mathbf{P}}{|\mathbf{S}|} = 0.728$$
What value of C in parallel with R & L would make pf = 1 (Q = 0)?

$$\mathbf{Q} = 603.9 \cdot \mathbf{VAR} \qquad \text{so we need:} \qquad \mathbf{Q} \cdot \mathbf{C} := -\mathbf{Q} \qquad \mathbf{Q} \cdot \mathbf{C} = -603.9 \cdot \mathbf{VAR} = \frac{\mathbf{V} \cdot \mathbf{in}^2}{\mathbf{V}}$$

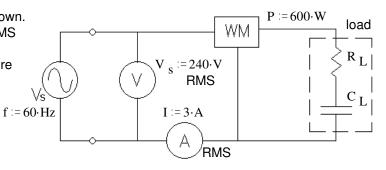
$$X_{C} := \frac{V_{in}^{2}}{Q_{C}} \qquad X_{C} = -20.035 \cdot \Omega = \frac{-1}{\omega \cdot C} \qquad C := \frac{1}{|X_{C}| \cdot \omega} \qquad C = 132 \cdot \mu F$$

Check:
$$\frac{1}{\frac{1}{R+j\cdot\omega\cdot L}+j\cdot\omega\cdot C} = 18.883\cdot\Omega$$
 No j term, so $\theta = 0^{\circ}$

- **Ex. 3** R, & C together are the load in the circuit shown. The RMS voltmeter measures 240 V, the RMS ammeter measures 3 A, and the wattmeter measures 600 W. Find the following: Be sure to show the correct units for each value.
 - a) The value of the load resistor. $R_{L} = ?$

$$P = I^2 \cdot R_L$$

$$R_{L} := \frac{P}{I^{2}} \qquad R_{L} = 66.7 \cdot \Omega$$



- b) The apparent power. $|\mathbf{S}| = ?$ $\mathbf{S} := \mathbf{V}_{\mathbf{S}} \cdot \mathbf{I}$ $\mathbf{S} = 720 \cdot \mathbf{VA}$ c) The reactive power. $\mathbf{Q} = ?$ $\mathbf{Q} := -\sqrt{\mathbf{S}^2 - \mathbf{P}^2}$ $\mathbf{Q} = -398 \cdot \mathbf{VAR}$ d) The complex power. $\mathbf{S} = ?$ $\mathbf{S} := \mathbf{P} + \mathbf{j} \cdot \mathbf{Q}$ $\mathbf{S} = 600 - 398\mathbf{i} \cdot \mathbf{VA}$ e) The power factor. $\mathbf{pf} = ?$ $\mathbf{pf} := \frac{\mathbf{P}}{\mathbf{V}_{\mathbf{S}} \cdot \mathbf{I}}$ $\mathbf{pf} = 0.833$
- f) The power factor is leading or lagging? leading (load is capacitive, Q is negative)
- g) The two components of the load are in a box which cannot be opened. Add (draw it) another component to the circuit above which can correct the power factor (make pf = 1). Show the correct component in the correct place and <u>find its value</u>. This component should not affect the real power consumption of the load.

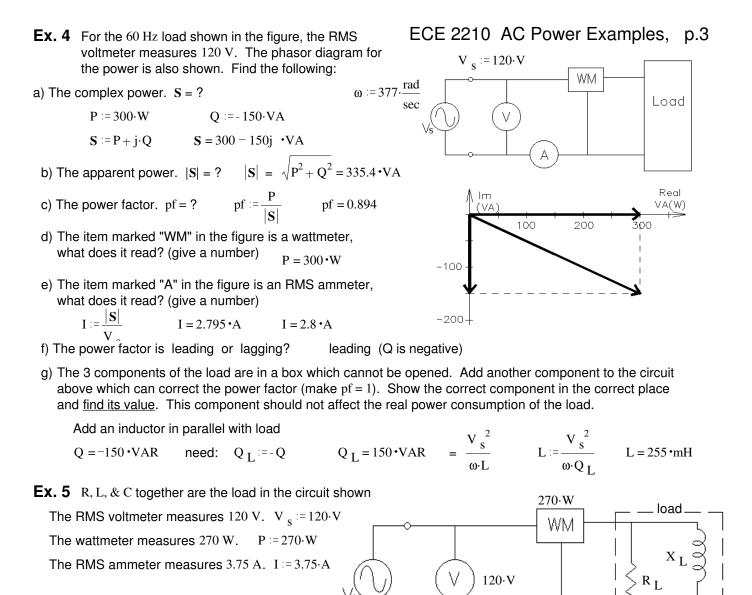
Add an inductor in parallel with load

$$f = 60 \cdot Hz \qquad \omega := 377 \cdot \frac{rad}{sec}$$

$$Q = -398 \cdot VAR \qquad so we need: Q_{L} := -Q \qquad Q_{L} = 398 \cdot VAR \qquad = \frac{V_{s}^{2}}{X_{L}}$$

$$X_{L} := \frac{V_{s}^{2}}{Q_{L}} \qquad X_{L} = 144.725 \cdot \Omega = \omega \cdot L \qquad L := \frac{|X_{L}|}{\omega} \qquad L = 384 \cdot mH$$

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Find the following: Be sure to show the correct units for each value.

a) The value of the load resistor. $R_L = ?$

$$P = \frac{V_{s}^{2}}{R_{L}}$$
 $R_{L} = \frac{V_{s}^{2}}{P}$ $R_{L} = 53.3 \cdot \Omega$

b) The magnitude of the impedance of the load inductor (reactance) . $|\mathbf{Z}_L| = X_L = ?$

$$I_{R} := \frac{V_{s}}{R_{L}} \qquad I_{R} = 2.25 \cdot A \qquad I_{L} := \sqrt{I^{2} - I_{R}^{2}} \qquad I_{L} = 3 \cdot A \qquad X := \frac{V_{s}}{I_{L}} \qquad X = 40 \cdot \Omega$$
$$X_{C} := -10 \cdot \Omega \qquad X_{L} := X - X_{C} \qquad X_{L} = 50 \cdot \Omega$$

 $f := 60 \cdot Hz$

c) The reactive power. Q = ? Q := $\sqrt{\left(V_{s} \cdot I\right)^{2} - P^{2}}$ Q = 360 ·VAR

2 = 360 •VAR positive, because the load is primarily inductive

3.75·A

Α

- 10j·Ω

lagging (load is inductive, Q is positive)

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e) The 3 components of the load are in a box which cannot be opened. Add another component to the circuit above which can correct the power factor (make pf = 1). Show the correct component in the correct place and <u>find its value</u>. This component should not affect the real power consumption of the load.

Add a capacitor in parallel with load

$$f = 60 \cdot Hz \qquad \omega := 377 \cdot \frac{rad}{sec}$$

$$Q = 360 \cdot VAR \qquad so we need: \qquad Q_C := -Q \qquad Q_C = -360 \cdot VAR \qquad = -\frac{V_s^2}{\frac{1}{\omega \cdot C}} = -\omega \cdot C \cdot V_s^2$$

$$C := \frac{Q_C}{-\omega \cdot V_s^2} \qquad C = 66.3 \cdot \mu F$$

Ex. 6 A step-down transformer has an output voltage of 220 V (rms) when the primary is connected across a 560 V (rms) source.

a) If there are 280 turns on the primary winding, how many turns are required on the secondary?

b) If the current in the primary is 2.4 A, what current flows in the load connected to the secondary?

c) If the transformer is rated at 700/275 V, 2.1 kVA, what are the rated primary and secondary currents?

$$280 \cdot \frac{220 \cdot \text{volt}}{560 \cdot \text{volt}} = 110 \text{ turns}$$
$$2.4 \cdot \text{amp} \cdot \frac{280}{110} = 6.11 \cdot \text{A}$$

$$\frac{2.1 \cdot kVA}{700 \cdot V} = 3 \cdot A$$
 sec: $\frac{2.1 \cdot kVA}{275 \cdot V} = 7.636 \cdot A$

EX. 7 The transformer shown in the circuit below
is ideal. Find the following:
a)
$$|\mathbf{I}_1| = ?$$

 $V_s := 120 \cdot V$
 $\omega := 377 \frac{\mathrm{rad}}{\mathrm{sec}}$
 $V_s := 120 \cdot V$
 $\omega := 377 \frac{\mathrm{rad}}{\mathrm{sec}}$
 $N_1 := 150 \cdot \mathrm{turns}$
 $R_1 := 20 \cdot \Omega$
Make an
equivalent circuit:
 $R_1 := 20 \cdot \Omega$
 $R_1 = 20$

pri:

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