

Stuff

Exam 3 Wednesday 4/9/03

Primarily Ch.4 (BJTs), but may include any earlier material.
Old exams are available on the HW web page.

MOSFET Lab today: Bring lab card with at least \$4.50.

Bring your textbook to lab**Depletion-type MOSFET Continued**

Equations are often rewritten in terms of I_{DSS} ,
because I_{DSS} is so easy to measure

$$\frac{2 \cdot I_{DSS}}{V_t^2} = k'_n \frac{W}{L}$$

Small v_{DS} $r_{DS} = \frac{1}{2 \cdot I_{DSS} \cdot (V_{GS} - V_t)}$

Triode: $i_D = I_{DSS} \cdot \left[2 \cdot \left(\frac{v_{GS}}{V_t} - 1 \right) \cdot \frac{v_{DS}}{V_t} - \left(\frac{v_{DS}}{V_t} \right)^2 \right]$

Saturation: $i_D = I_{DSS} \cdot \left(1 - \frac{v_{GS}}{V_t} \right)^2$

$$i_D = I_{DSS} \cdot \left(1 - \frac{v_{GS}}{V_t} \right)^2 \cdot (1 + \lambda \cdot v_{DS})$$

Transconductance: $g_m = \frac{2 \cdot I_{DSS}}{V_t^2} \cdot (V_{GS} - V_t) = \sqrt{\frac{4 \cdot I_{DSS} \cdot I_D}{V_t^2}}$

DC Circuits (bias)

You need to know the important parameters of the MOSFET: V_t and

In reality, these are not that well known, and you may have to repeat calculations for max and min values

Important circuit parameters are: V_G , R_S & I_D Need to know 2

Any single one of these could be unknown.

Next you need to assume a region of operation.

Cutoff Region If $V_G < V_t$, you're done. There's no way the MOSFET will turn on at all

$$I_D = 0 \quad R_S \text{ can be anything}$$

Saturation Region

Assume R_D is small enough that the FET is in saturation (active) region

If I_D is known (often a design parameter): $I_D = \frac{1}{2} \cdot k'_n \cdot \frac{W}{L} \cdot (V_{GS} - V_t)^2$

$$\frac{2}{k'_n \cdot \frac{W}{L}} \cdot I_D = (V_{GS} - V_t)^2 \quad V_{GS} = \sqrt{\frac{2 \cdot I_D}{k'_n \cdot \frac{W}{L}}} + V_t$$

Now, depending on which other variable is known:

If V_G is known: $V_S = V_G - V_{GS}$

$$R_S = \frac{V_S}{I_D}$$

If R_S is known: $V_S = I_D \cdot R_S$

$$V_G = V_S + V_{GS}$$

Spice #S2, due: M, 4/7 2 handouts

A. Stolp
4/6/03

HW #20, due: W, 4/9 Ex4.45, Ex4.46

Problems 4.120, 4.119, The transistor of HW 19, problem 1 has $C_\pi = 11\text{pF}$, $C_\mu = 2\text{pF}$ & 1pF of stray capacitance between the base & collector, find f_{CH} due to these.

ans 4.120 $r_o \parallel \frac{r_\mu + r_\pi}{\beta + 1}$ last problem: $1.82 \cdot \text{MHz}$

HW # 21, due: F 4/11 Ex5.1 - Ex5.8

ans: Ex5.7: $V_{SB}=4\text{V}$, $i_D=0.182\text{mA}$, $r_o=578\text{ohm}$

HW # 22, due: W 4/16 Ex5.9 - Ex5.16

Check assumptions, esp saturation on 15 & 16

OR, the old equations still work fine

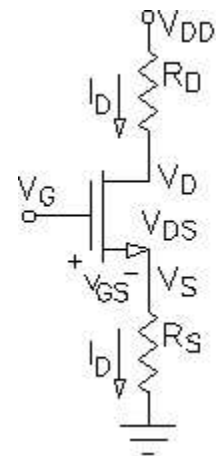
$$r_{DS} = \frac{1}{k'_n \cdot \frac{W}{L} \cdot (v_{GS} - V_t)}$$

$$i_D = k'_n \cdot \frac{W}{L} \cdot \left[(v_{GS} - V_t) \cdot v_{DS} - \frac{1}{2} \cdot v_{DS}^2 \right]$$

$$i_D = \frac{1}{2} \cdot k'_n \cdot \frac{W}{L} \cdot (v_{GS} - V_t)^2$$

$$i_D = \frac{1}{2} \cdot k'_n \cdot \frac{W}{L} \cdot (v_{GS} - V_t)^2 \cdot (1 + \lambda \cdot v_{DS})$$

$$g_m = k'_n \cdot \frac{W}{L} \cdot (V_{GS} - V_t) = \sqrt{k'_n \cdot \frac{W}{L} \cdot 2 \cdot I_D}$$



These equations are for the circuit above. If your circuit is different, modify the equations accordingly.

If I_D is unknown, things get a little harder: $V_{GS} = V_G - R_S \cdot I_D$

$$I_D = \frac{1}{2} \cdot k'_n \cdot \frac{W}{L} \cdot (V_{GS} - V_t)^2 = \frac{1}{2} \cdot k'_n \cdot \frac{W}{L} \cdot (V_G - R_S \cdot I_D - V_t)^2 = \frac{1}{2} \cdot k'_n \cdot \frac{W}{L} \cdot [(V_G - V_t) - R_S \cdot I_D]^2$$

$$\frac{2}{k'_n \cdot \frac{W}{L}} \cdot I_D = [(V_G - V_t) - R_S \cdot I_D] \cdot [(V_G - V_t) - R_S \cdot I_D] = (V_G - V_t)^2 - 2 \cdot (V_G - V_t) \cdot R_S \cdot I_D + R_S^2 \cdot I_D^2$$

$$\text{Solve for } I_D: \quad 0 = \underbrace{R_S^2 \cdot I_D^2}_{a} - 2 \cdot \underbrace{(V_G - V_t) \cdot R_S}_{b} + \underbrace{\frac{2}{k'_n \cdot \frac{W}{L}}}_{c} \cdot I_D \quad \begin{aligned} I_{D1} &= \frac{-b + \sqrt{b^2 - 4 \cdot a \cdot c}}{2 \cdot a} \\ I_{D2} &= \frac{-b - \sqrt{b^2 - 4 \cdot a \cdot c}}{2 \cdot a} \end{aligned}$$

Of these two answers, only one will make sense. ($V_{GS} > V_t$).

Finally, you MUST check to see that the MOSFET is actually in the saturation region. If not, you'll have to start over or modify your design.

Go through the examples in your book, p381 - 387

Triode Region If R_D is too big, the FET is in triode region

If I_D is known: $I_D = k'_n \cdot \frac{W}{L} \cdot \left[(V_{GS} - V_t) \cdot V_{DS} - \frac{1}{2} \cdot V_{DS}^2 \right]$

$$\frac{1}{k'_n \cdot \frac{W}{L}} \cdot I_D = \left[(V_{GS} - V_t) \cdot V_{DS} - \frac{1}{2} \cdot V_{DS}^2 \right]$$

$$\frac{1}{k'_n \cdot \frac{W}{L}} \cdot I_D = (V_{GS} - V_t) - \frac{1}{2} \cdot V_{DS}$$

$$V_{GS} = \frac{I_D}{k'_n \cdot \frac{W}{L} \cdot V_{DS}} + V_t + \frac{1}{2} \cdot V_{DS}$$

Where: $V_{DS} = V_{DD} - R_D \cdot I_D - R_S \cdot I_D = V_{DD} - (R_D + R_S) \cdot I_D$

Now, depending on which other variable is known: If V_G is known: $V_S = V_G - V_{GS} \quad R_S = \frac{V_S}{I_D}$

If R_S is known: $V_S = I_D \cdot R_S \quad V_G = V_S + V_{GS}$

If I_D is unknown, things get downright messy: Define: $K = k'_n \cdot \frac{W}{L} \quad \& \quad R_T = R_D + R_S$

$$I_D = k'_n \cdot \frac{W}{L} \cdot \left[(V_{GS} - V_t) \cdot V_{DS} - \frac{1}{2} \cdot V_{DS}^2 \right] \quad V_{GS} - V_t = V_G - R_S \cdot I_D - V_t \quad V_{DS} = V_{DD} - R_T \cdot I_D$$

$$= K \cdot \left[(V_G - R_S \cdot I_D - V_t) \cdot (V_{DD} - R_T \cdot I_D) - \frac{1}{2} \cdot (V_{DD} - R_T \cdot I_D)^2 \right] =$$

$$K \cdot \left(V_G \cdot V_{DD} - V_G \cdot R_T \cdot I_D - R_S \cdot I_D \cdot V_{DD} + R_S \cdot I_D^2 \cdot R_T - V_t \cdot V_{DD} + V_t \cdot R_T \cdot I_D - \frac{1}{2} \cdot V_{DD}^2 + V_{DD} \cdot R_T \cdot I_D - \frac{1}{2} \cdot R_T^2 \cdot I_D^2 \right)$$

$$0 = \underbrace{\left(R_S \cdot R_T - \frac{1}{2} \cdot R_T^2 \right)}_a \cdot I_D^2 + \underbrace{\left[V_{DD} \cdot R_D - (V_G - V_t) \cdot R_T - \frac{1}{K} \right]}_b \cdot I_D + \underbrace{\left[(V_G - V_t) \cdot V_{DD} - \frac{1}{2} \cdot V_{DD}^2 \right]}_c$$

$$I_{D1} = \frac{-b + \sqrt{b^2 - 4 \cdot a \cdot c}}{2 \cdot a}$$

$$I_{D2} = \frac{-b - \sqrt{b^2 - 4 \cdot a \cdot c}}{2 \cdot a}$$

Of these two answers, only one will make sense. ($V_{GS} > V_t$).