

Silicon atoms & crystals

Silicon atoms each have 4 valence electrons (electrons in their outermost shell). In its crystalline form, each atom covalently bonds with four neighboring atoms to form a tetrahedral crystal. In the pure, "intrinsic" crystal, practically all the electrons are used in bonds and all the spaces are filled, which leaves very few electrons free to move and thus no way to make current flow.



By the effects of heat, light and/or large electric fields, a few electrons do break free of the bonds and become "free" carriers. That is, they're free to move about crystal and "carry" an electrical current. For each free electron created, there will also be a free "hole". A hole is the space left behind by the now free electron and is considered to be a carrier (+) in its own right.

By heat:

Intrinsic free electron concentration (elect/cm³): $n_i := \sqrt{B \cdot T^3 \cdot e^{-\frac{E_g}{k \cdot T}}}$

Bandgap Energy: $E_g := 1.12 \cdot \text{eV}$

Electron volt: $\text{eV} := 1.60 \cdot 10^{-19} \cdot \text{joule}$

Absolute temperature: $T = ^\circ\text{C} + 273$

Boltzmann's constant: $k := 8.63 \cdot 10^{-5} \cdot \frac{\text{eV}}{\text{K}}$

Material-dependant parameter: $B := 5.4 \cdot 10^{31} \cdot \frac{1}{\text{K}^3 \cdot \text{cm}^6}$ for Silicon

$$n_i^2 = 5.4 \cdot 10^{31} \cdot \frac{1}{\text{K}^3 \cdot \text{cm}^6} \cdot T^3 \cdot e^{-\frac{1.3 \cdot 10^4 \cdot \text{K}}{T}}$$

$$\frac{E_g}{k} = 1.298 \cdot 10^4 \cdot \text{K}$$

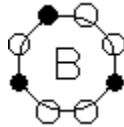
Intrinsic free hole concentration (holes/cm³): $p_i = n_i$ (One for each free electron)

These are the *equilibrium* concentrations: Rate of free carrier creation = Rate of *recombination*.

Doping

p-type

Replace some of the silicon atoms in an intrinsic crystal with trivalent atoms like boron or aluminum. These atoms are called "acceptor" atoms.

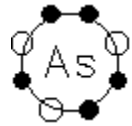


Acceptor atom concentration (atoms/cm³): N_A

free hole concentration (holes/cm³): $p \simeq N_A$ Majority carriers

n-type

Replace some of the silicon atoms in an intrinsic crystal with pentavalent atoms like arsenic or phosphorus. These atoms are called "donor" atoms.



Donor atom concentration (atoms/cm³): N_D

free electron concentration (elect/cm³): $n \simeq N_D$

If you make one concentration larger, the other becomes proportionately smaller:

If there are 10 times as many free electrons, then free holes will be filled 10 times faster and exist only 1/10th as long before recombining.

$$p \cdot n = n_i^2$$

If there are 10 times as many holes, then free electrons only last 1/10th as long before recombining.

free electron concentration (elect/cm³): $n_{po} \simeq \frac{n_i^2}{N_A}$

(Minority carrier) in p-type equilibrium

free hole concentration (holes/cm³): $p_{no} \simeq \frac{n_i^2}{N_D}$

(Minority carrier) in n-type equilibrium

Drift Current

Drift current is the common kind of current that you're used to seeing in wires. Under the influence of an electric field, the free negative carriers drift towards the positive charge and the free positive carriers drift towards the negative voltage.

Drift current density (amp/cm²): $J_{\text{drift}} = q \cdot (p \cdot \mu_p + n \cdot \mu_n) \cdot E$

Electric field (V/cm)

Note: $\frac{\text{cm}}{10^4 \cdot \mu\text{m}} = 1$

Electron charge: $q := 1.60 \cdot 10^{-19} \cdot \text{coul}$

Electron mobility: $\mu_n := 1350 \cdot \frac{\text{cm}^2}{\text{V} \cdot \text{s}}$ typical

Hole mobility: $\mu_p := 480 \cdot \frac{\text{cm}^2}{\text{V} \cdot \text{s}}$ typical

Resistivity (Ωcm): $\rho = \frac{1}{q \cdot (p \cdot \mu_p + n \cdot \mu_n)}$

Current: $I = J \cdot (\text{Cross_sectional_area})$

Semiconductor Physics Notes p.2

Diffusion Current

If you add some colored dye to one end of a tub filled with water the color would eventually diffuse to the other side until all the water is equally colored. There will be a net flow of colored water from the side of high concentration to the side of low concentration. The same will happen if you add carriers at one end of a piece of semiconductor, there will be a net flow away from the area of higher concentration. (Diffusion current also happens in regular conductors, but it happens so fast we rarely consider it and just assume the charges equalize instantly.)

Diffusion electron current density: $J_{ndiff} = q \cdot D_n \cdot \frac{dn}{dx}$

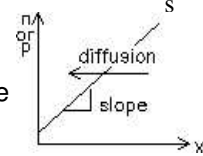
Electron charge: $q := 1.60 \cdot 10^{-19} \cdot \text{coul}$

Diffusion hole current density: $J_{pdiff} = -q \cdot D_p \cdot \frac{dp}{dx}$

Current: $I = J \cdot (\text{Cross_sectional_area})$

Diffusion constant for electrons: $D_n := 34 \cdot \frac{\text{cm}^2}{\text{s}}$ typical

slope of concentration profile



Diffusion constant for holes: $D_p := 12 \cdot \frac{\text{cm}^2}{\text{s}}$ typical

Positive slope results in leftward movement of holes and thus a negative current.

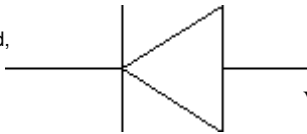
Diffusion constant / mobility relationship

Not surprisingly the diffusion constant of a carrier is related to the mobility of the carrier.

$$\frac{\text{Diffusion constant}}{\text{mobility}} = \frac{D_n}{\mu_n} = \frac{D_p}{\mu_p} = V_T = \text{Thermal voltage} = \frac{k \cdot T}{q} \approx 25 \cdot \text{mV} \quad (\text{Einstein relationship})$$

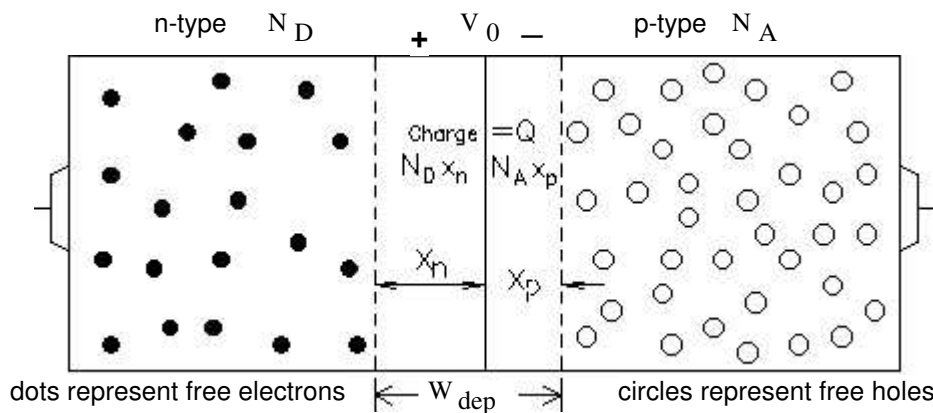
Diodes, no bias or Reverse bias

Note that the diode is pointed leftward, which may seem a little weird right now, but is done for a reason.



$$V_0 = V_T \cdot \ln\left(\frac{N_A \cdot N_D}{n_i^2}\right) = \text{Barrier potential (built-in voltage)} \quad \text{typical: } 0.6\text{V to } 0.8\text{V}$$

Since the doping drives the creation of the depletion region, The barrier potential and the width of the depletion region depend on the doping.



Note: This drawing is NOT to scale. The depletion region is actually very small compared to the size of the diode. Also, there are far more free carriers than shown.

$$\frac{x_n}{x_p} = \frac{N_A}{N_D}$$

$x_n > x_p$ because $N_A > N_D$ in this particular representation

Permittivity of silicon: $\epsilon_s := 1.035 \cdot 10^{-12} \cdot \frac{\text{F}}{\text{cm}}$

$$\text{Width of depletion region: } W_{dep} = \sqrt{\frac{2 \cdot \epsilon_s}{q} \cdot \left(\frac{1}{N_A} + \frac{1}{N_D}\right) \cdot (V_0 + V_R)}$$

$$\frac{2 \cdot \epsilon_s}{q} = 1.294 \cdot 10^7 \cdot \frac{1}{\text{V} \cdot \text{cm}}$$

Reverse bias voltage

$$x_n + x_p = W_{dep} = \sqrt{1.294 \cdot 10^7 \cdot \frac{1}{\text{V} \cdot \text{cm}} \cdot \left(\frac{1}{N_A} + \frac{1}{N_D}\right) \cdot (V_0 + V_R)}$$

Extent into n-region: $x_n = \frac{N_A}{N_A + N_D} \cdot W_{dep}$

Extent into p-region: $x_p = \frac{N_D}{N_A + N_D} \cdot W_{dep}$

Semiconductor Physics Notes p.3

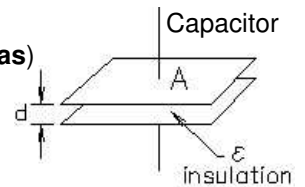
Depletion or Junction Capacitance (due to depletion region, primarily for reverse bias)

Junction capacitance

no bias, forward or reverse: $C_{j0} = \frac{\epsilon_s \cdot \text{Area}}{W_{\text{dep}}} = A \cdot \sqrt{\frac{\epsilon_s \cdot q}{2 \cdot \left(\frac{1}{N_A} + \frac{1}{N_D} \right) \cdot V_0}}$ C_{j0} is often given on part data sheets

Permittivity of silicon: $\epsilon_s := 1.035 \cdot 10^{-12} \frac{\text{F}}{\text{cm}}$

Barrier potential, see previous page



$$C = \epsilon \cdot \frac{A}{d} = \frac{Q}{V} = \frac{dq}{dv}$$

reverse bias: $C_j = A \cdot \sqrt{\frac{\epsilon_s \cdot q}{2 \cdot \left(\frac{1}{N_A} + \frac{1}{N_D} \right) \cdot (V_0 + V_R)}} = A \cdot \sqrt{\frac{8.28 \cdot 10^{-32} \frac{\text{F}^2 \cdot \text{V}}{\text{cm}}}{\left(\frac{1}{N_A} + \frac{1}{N_D} \right) \cdot (V_0 + V_R)}}$ $C_j = \frac{C_{j0}}{\left(1 + \frac{V_R}{V_0} \right)^m}$ $m = 1/2$ or Fudge, $1/3$ to $1/2$

forward bias: $C_j \approx 2 \cdot C_{j0}$

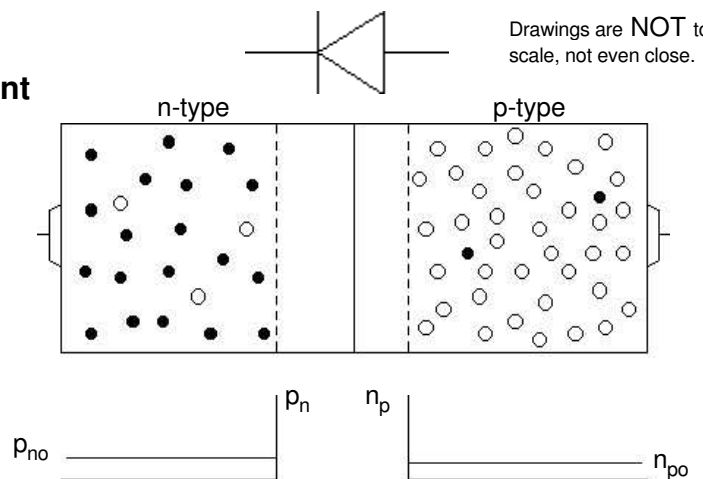
Diodes, forward bias

Minority Carriers make the forward bias current

Even in an n-type region, there will be some free holes: $p_{no} \approx \frac{n_i^2}{N_D}$

Likewise, there will be some free electrons in the p-type region: $n_{po} \approx \frac{n_i^2}{N_A}$

The graph at right shows these minority carrier concentrations as functions of the position in the diode. Since these lines are flat, no diffusion current will flow. The p_{no} line is a little higher than the n_{po} line, which indicates that the n region is more heavily doped.



Drawings are NOT to scale, not even close.

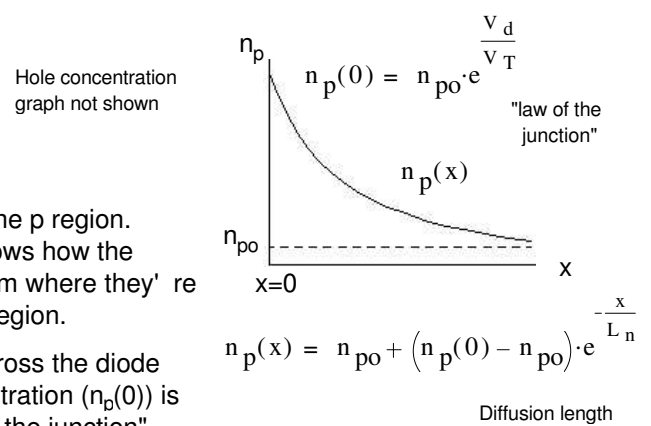
Forward Bias

When the diode is forward biased, holes are "injected" into the n region and electrons are injected into the p region. These carriers are called "excess minority carriers". Excess because there are more than p_{no} and n_{po} . Minority because they're in the wrong region.

There are two minority-carrier currents, the holes in the n region and the electrons in the p region. These two together make up the diode current. There are also majority-carrier currents on each side, but they don't cross the junction and don't limit the diode current, so we don't have to consider those.

The graph at right shows the distribution of minority electrons in the p region. The dotted line is the base level (n_{po}). The exponential curve shows how the number of free electrons tapers off as you move further away from where they're injected. They're being "swallowed" by the holes in this p-type region.

The number of injected electrons is dependent on the voltage across the diode and so is the concentration of free electrons at $x=0$. That concentration ($n_p(0)$) is shown on this graph and given by a relation known as the "law of the junction".



Diffusion length

There are two ways to determine the excess-electron current. One would be to find the total number of excess electrons, divide by their average lifetime, and multiply by q to get the current. The other way would be to find the slope of the $n_p(x)$ curve at $x=0$ and use that to find the diffusion current at $x=0$. Both methods yield the same results, which are shown on the next page.

Semiconductor Physics Notes p.4

Hole injection current: $I_p = \text{Area} \cdot q \cdot n_i^2 \cdot \frac{D_p}{L_p \cdot N_D} \cdot \left(e^{\frac{V_d}{V_T}} - 1 \right)$ Electron injection current: $I_n = \text{Area} \cdot q \cdot \frac{n_i^2}{N_A} \cdot \frac{D_n}{L_n} \cdot \left(e^{\frac{V_d}{V_T}} - 1 \right)$

Usually drop this 1 in forward bias

For a total diode current of:

Diode Saturation Current
(AKA scale current)

I_S

Diffusion constant for holes in n region $D_p := 12 \cdot \frac{\text{cm}^2}{\text{s}}$ typical

Diffusion constant for electrons in p region $D_n := 34 \cdot \frac{\text{cm}^2}{\text{s}}$ typical

diode voltage

Thermal voltage = $\frac{k \cdot T}{q} \simeq 25 \cdot \text{mV}$

$I_d = \text{Area} \cdot q \cdot n_i^2 \cdot \left(\frac{D_p}{L_p \cdot N_D} + \frac{D_n}{L_n \cdot N_A} \right) \cdot \left(e^{\frac{V_d}{V_T}} - 1 \right)$

Junction area

Usually drop this 1 in forward bias

Diffusion length of holes in n region

Diffusion length of electrons in p region

p doping
n doping

Diode Equation

diode voltage

Diode current: $I_d = I_S \cdot \left(e^{\frac{V_d}{n \cdot V_T}} - 1 \right)$

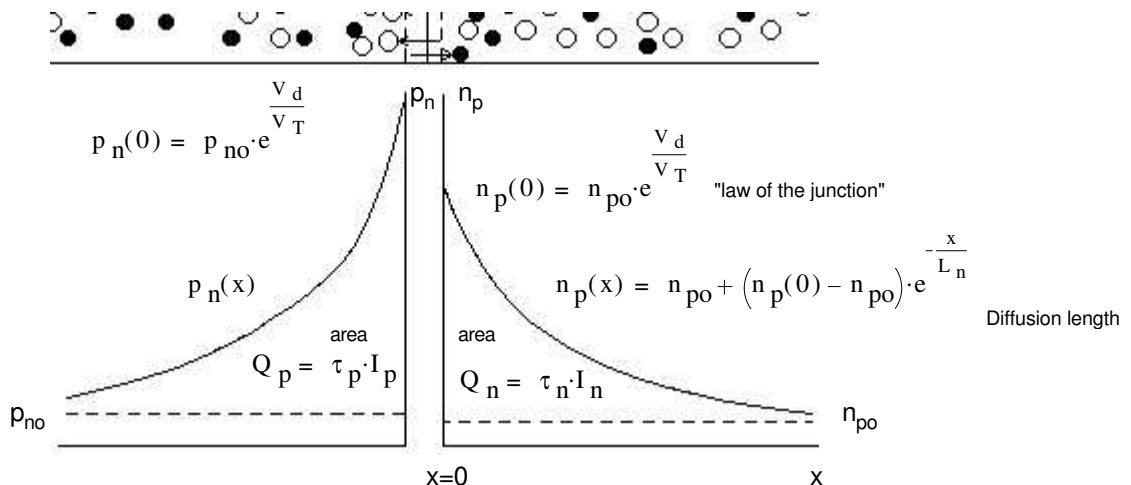
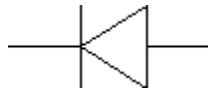
Usually drop this 1 in forward bias

Saturation current
(AKA scale current)

Thermal voltage = $\frac{k \cdot T}{q} \simeq 25 \cdot \text{mV}$

Fudge factor, assume $n = 1$ in ICs and $n = 2$ for discrete parts

Other permutations of the diode equation: $V_d = n \cdot V_T \cdot \ln \left(\frac{I_d}{I_S} \right)$ $I_S = \frac{I_d}{\left(e^{\frac{V_d}{n \cdot V_T}} - 1 \right)}$



Minority carrier charge storage

(excess minority charges waiting to be diffused and absorbed)

Excess hole charge in the n region

Excess electron charge in the p region

The areas under the curves

Total excess minority charge: $Q = Q_n + Q_p$

Mean transit time: $\tau_T = \frac{Q}{I_d}$

Semiconductor Physics Notes p.5

Minority Carrier Lifetime (Average lifetime of an excess minority carrier)

Excess hole lifetime in n region: $\tau_p = \frac{L_p^2}{D_p} = \frac{(\text{Diffusion length of holes in n region})^2}{\text{Diffusion constant for holes in n region: } D_p := 12 \cdot \frac{\text{cm}^2}{\text{s}} \text{ typical}}$

$$L_p = \sqrt{D_p \cdot \tau_p}$$

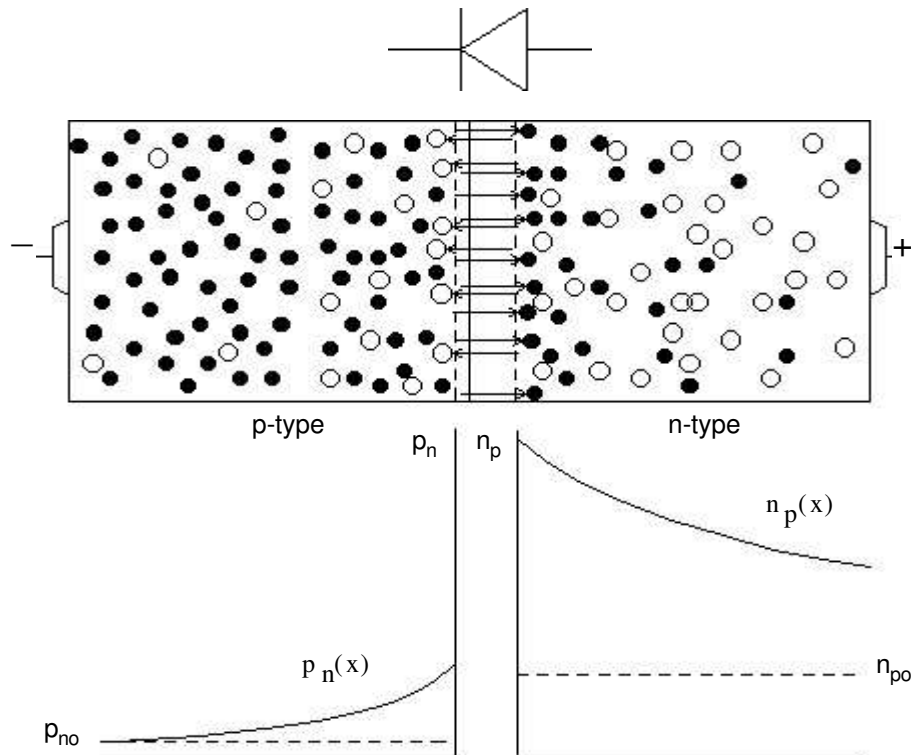
Excess electron lifetime in p region: $\tau_n = \frac{L_n^2}{D_n} = \frac{(\text{Diffusion length of electrons in p region})^2}{\text{Diffusion constant for electrons in p region: } D_n := 34 \cdot \frac{\text{cm}^2}{\text{s}} \text{ typical}}$

$$L_n = \sqrt{D_n \cdot \tau_n}$$

Diffusion Capacitance (due to Minority carrier charge storage) $C_d = \left(\frac{\tau_T}{V_T} \right) \cdot I_d$ diode current

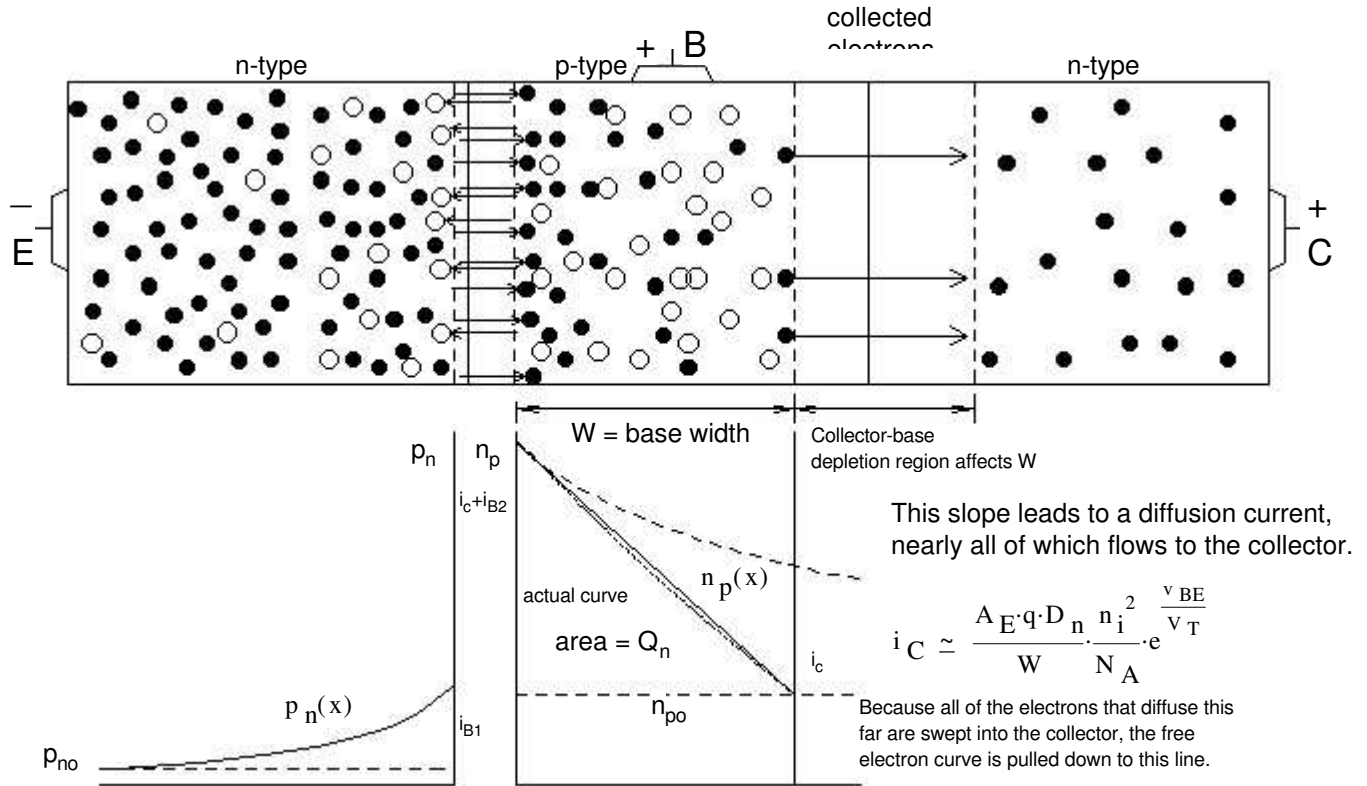
Transistors

Imagine a diode with a heavily doped p region and a lightly doped n region, like this:



Semiconductor Physics Notes p.6

Now imagine placing another diode junction very close to the first, most of the electrons that come across the emitter-base junction will be "collected" by the collector.



Hole-injection current is part of the base current

$$i_{B1} = \left(\frac{A_E \cdot q \cdot D_p}{L_p} \cdot \frac{n_i^2}{N_D} \right) \cdot e^{\frac{V_{BE}}{V_T}}$$

emitter doping
emitter hole diffusion length

Excess-minority charge stored in the base:

$$Q_n = \frac{1}{2} \cdot \left(A_E \cdot q \cdot W \cdot \frac{n_i^2}{N_A} \right) \cdot e^{\frac{V_{BE}}{V_T}}$$

Divide Q_n by the average lifetime of the electrons in the base to get the rate that electrons are absorbed in the base, leading to the second part of the base current.

$$i_{B2} = \frac{Q_n}{\tau_b} = \frac{1}{2} \cdot \left(\frac{A_E \cdot q \cdot W}{\tau_b} \cdot \frac{n_i^2}{N_A} \right) \cdot e^{\frac{V_{BE}}{V_T}}$$

base width
base doping

Collector Current

Ebers-Moll equation: $i_C = I_S \cdot e^{\frac{V_{BE}}{V_T}}$

Saturation current (npn) $I_S = \frac{A_E \cdot q \cdot D_n \cdot n_i^2}{N_A \cdot W}$

emitter hole diffusion constant
base doping
base width
base doping

base minority carrier lifetime
base-emitter junction effective area

base electron diffusion constant
intrinsic carrier density

$D_n := 34 \cdot \frac{\text{cm}^2}{\text{s}}$ typ

Beta

$$\beta = \frac{i_C}{i_{B1} + i_{B2}} = \frac{1}{\frac{D_p \cdot N_A \cdot W}{D_n \cdot N_D \cdot L_p} + \frac{1}{2} \cdot \frac{W^2}{D_n \cdot \tau_b}}$$

base width
base doping
base width
base doping

β depends on the effective base width, W which depends on V_{CB} . This leads to the Early effect, which is expressed as an output resistance.

base minority carrier lifetime = $\frac{L_n^2}{D_n} = \frac{(\text{Diffusion length of electrons in p region})^2}{\text{Diffusion constant for electrons in p region:}}$

emitter hole diffusion length
emitter doping
base electron diffusion constant

$D_n := 34 \cdot \frac{\text{cm}^2}{\text{s}}$ typ

PNP Swap p and n in subscripts Swap N_A and N_D $\tau_b = \frac{L_p^2}{D_p} = \frac{(\text{Diffusion length of holes in n region})^2}{\text{Diffusion constant for holes in n region}}$